

FIG. 4

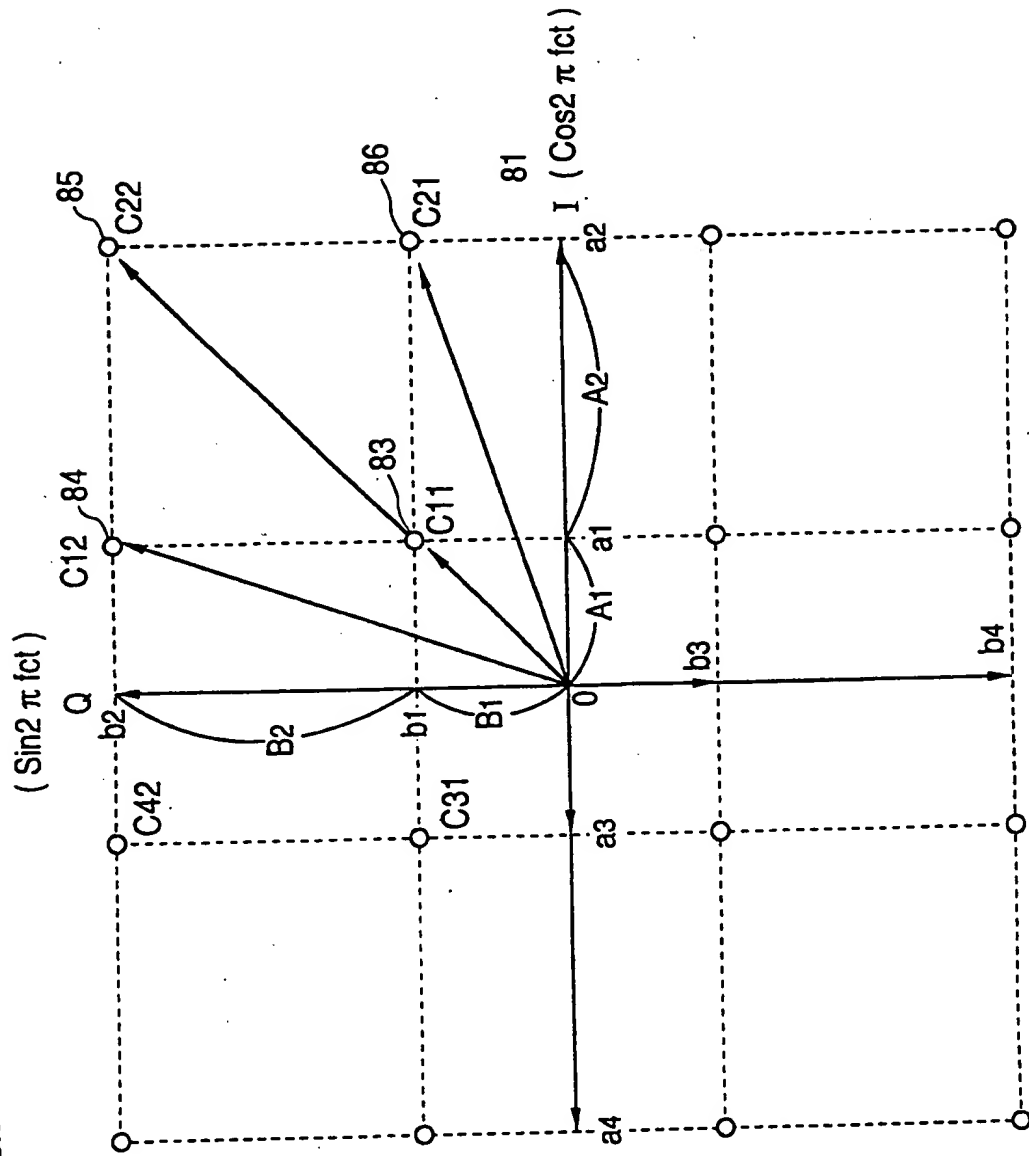


FIG. 5

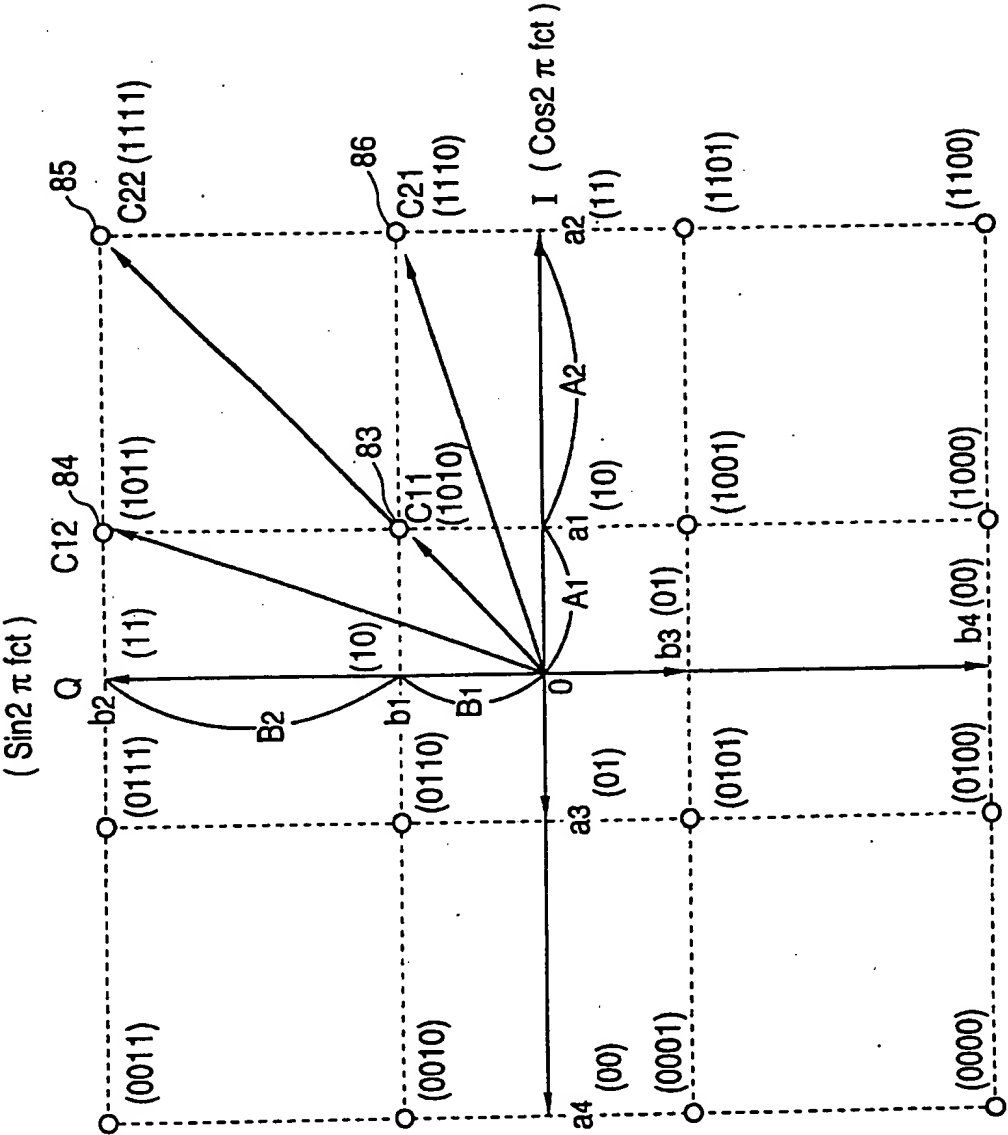


FIG. 6

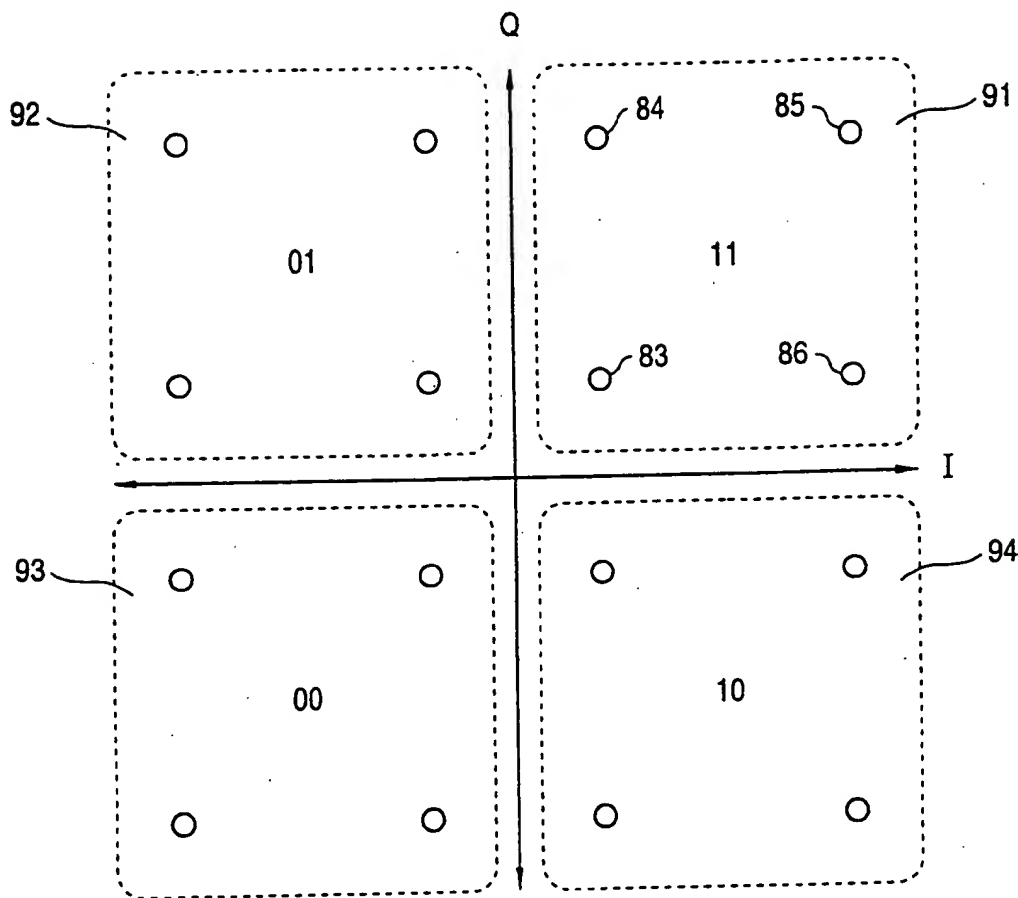


FIG. 7

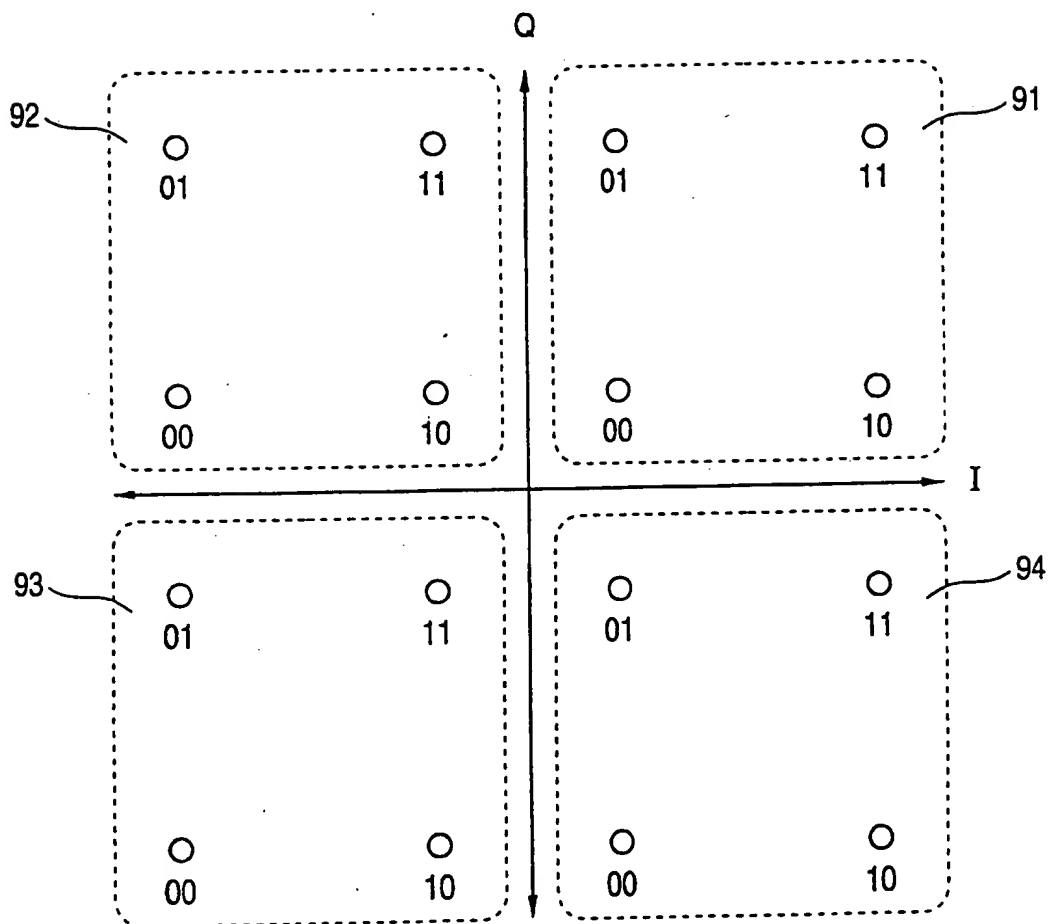


FIG. 8

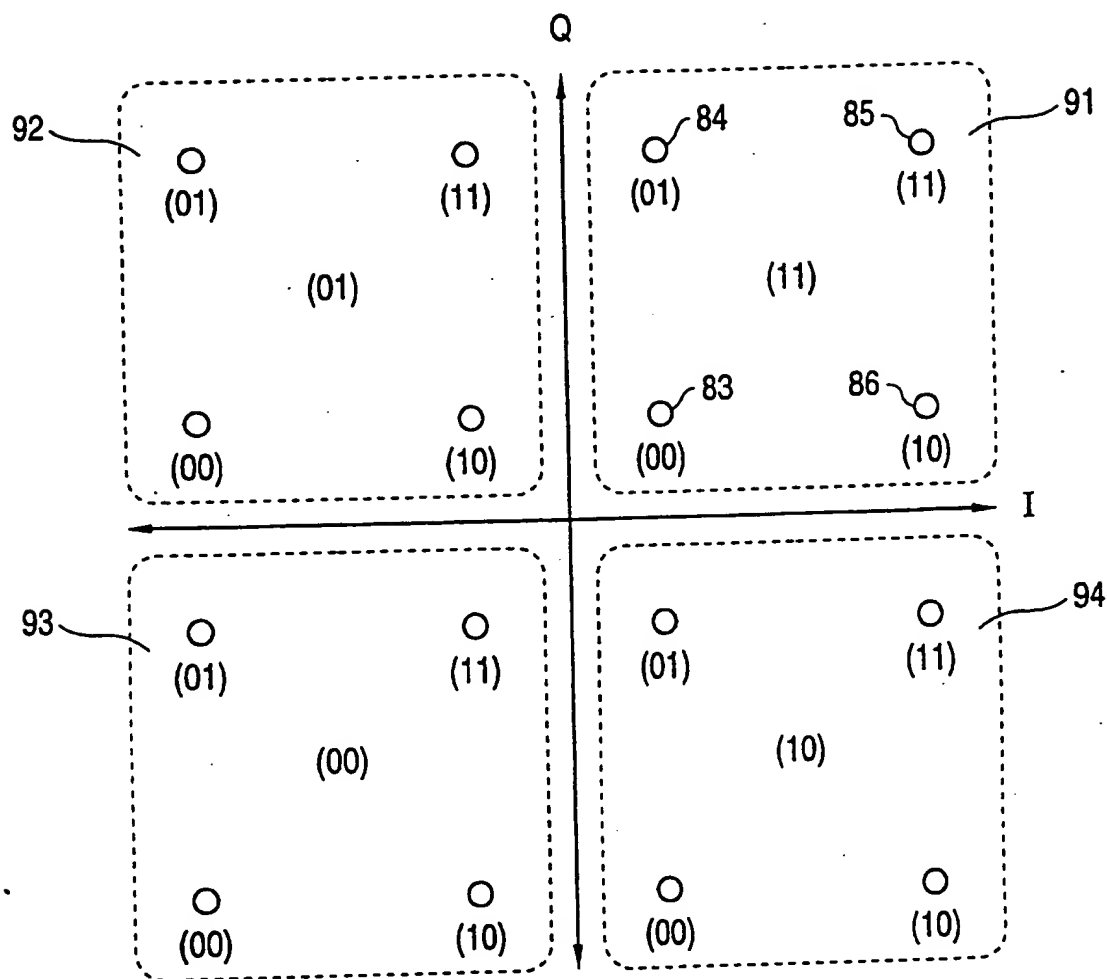


FIG. 9

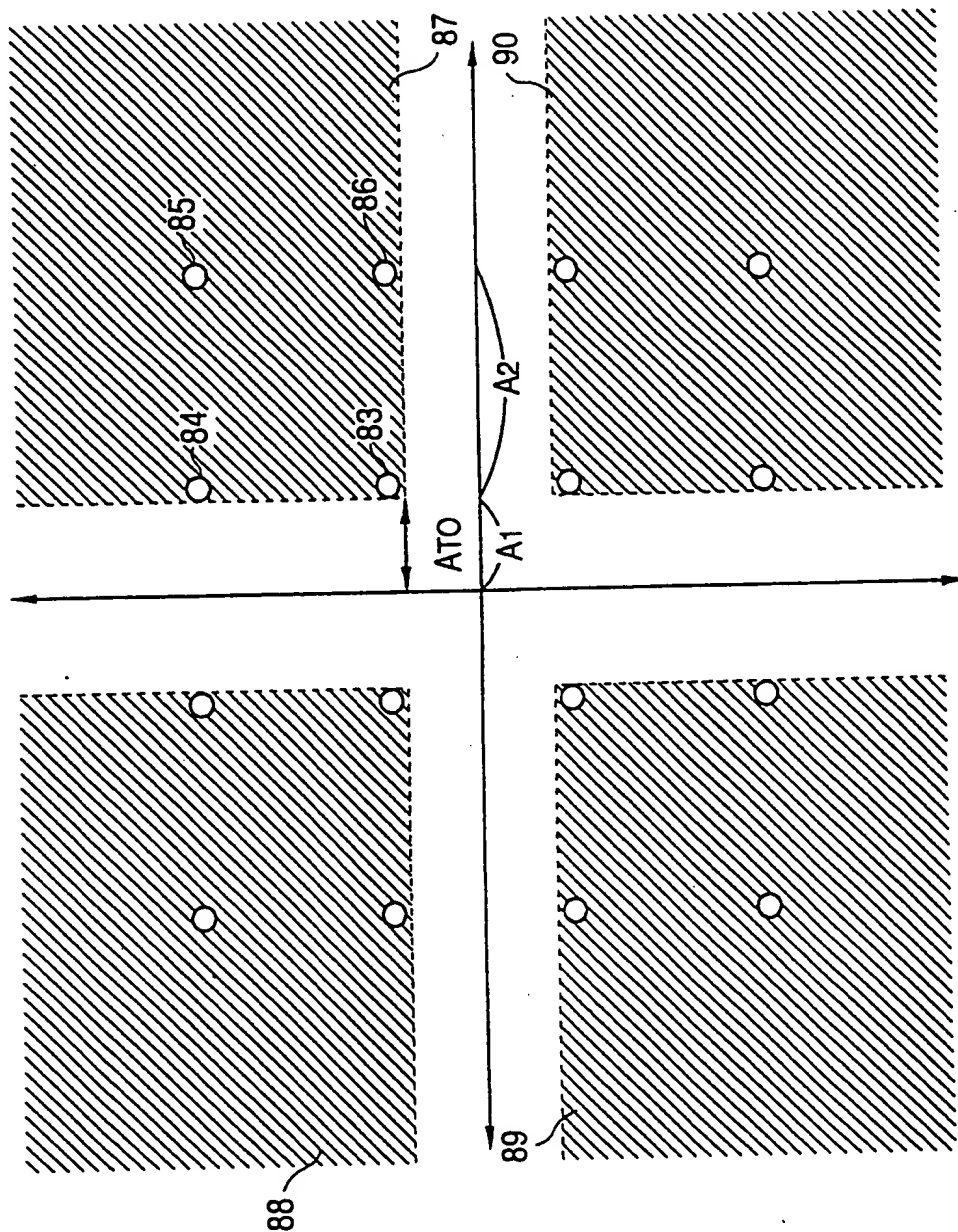


FIG. 10

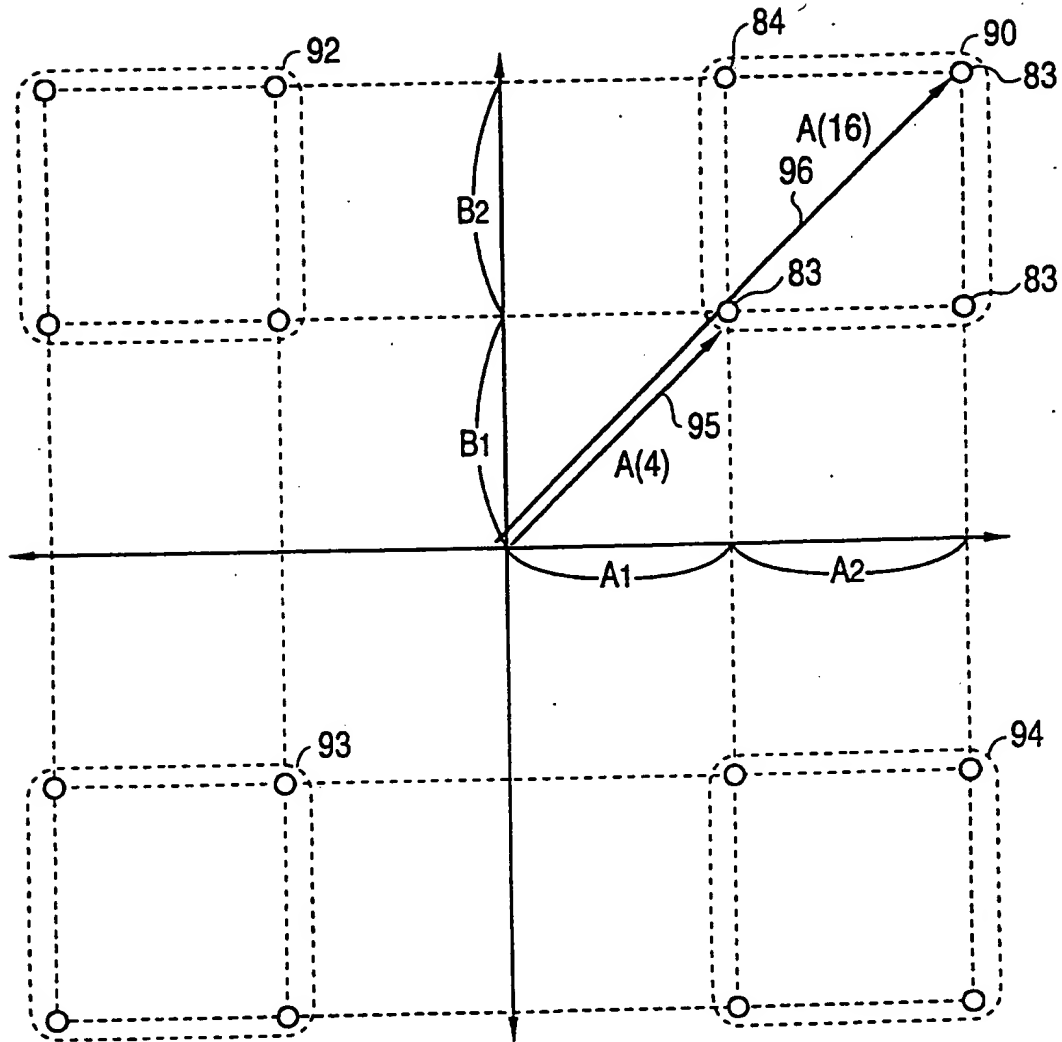


FIG. 11

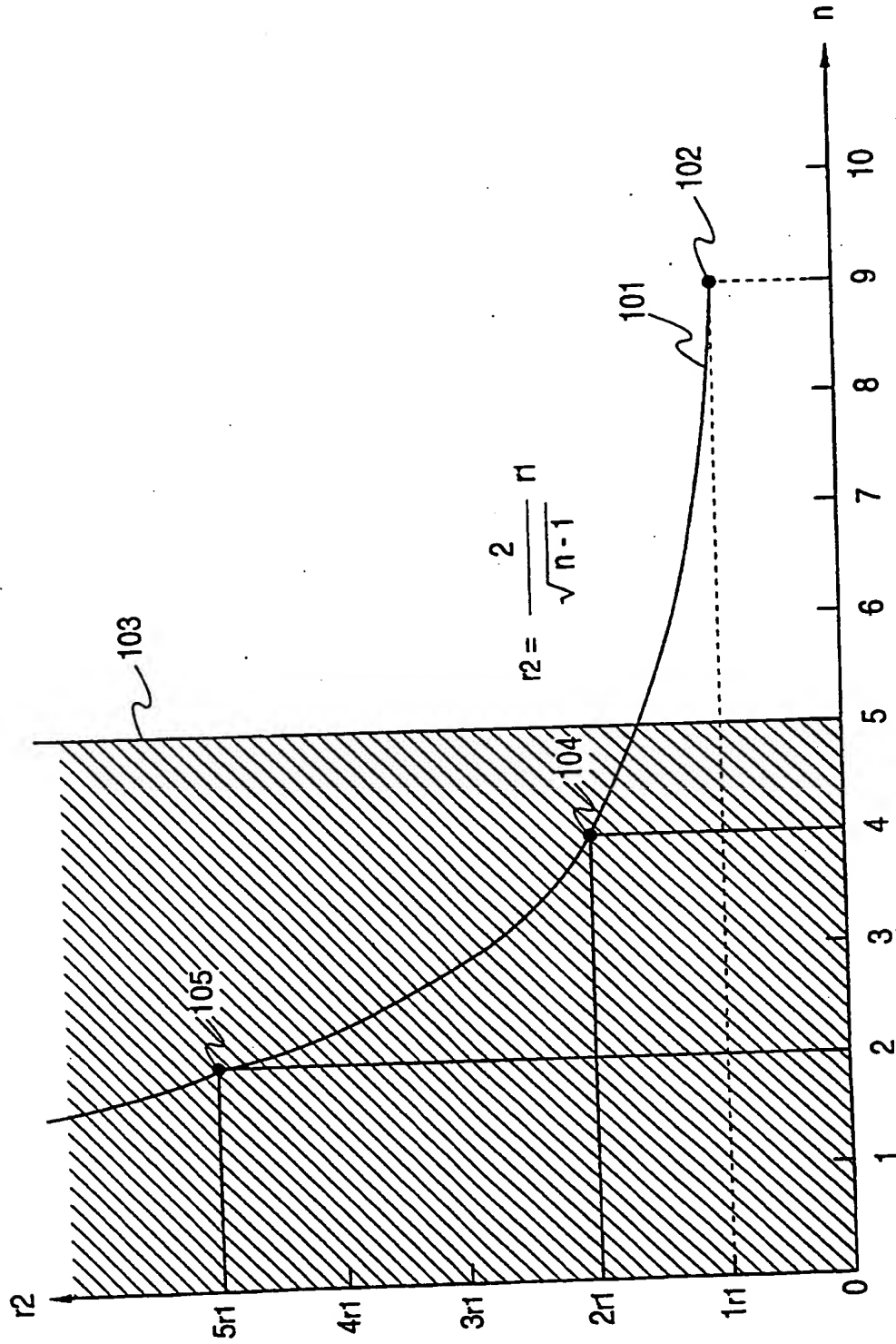


FIG. 12

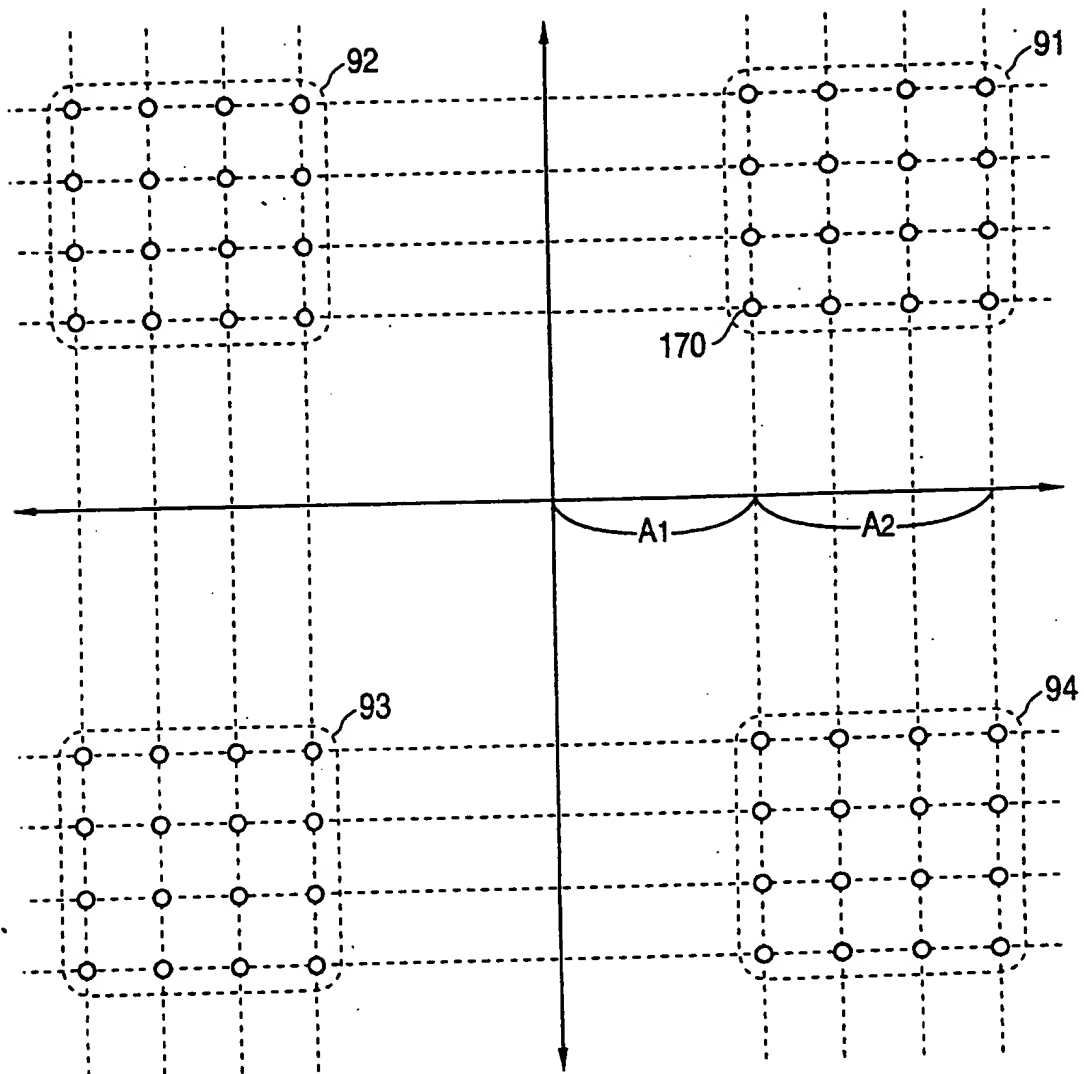


FIG. 13

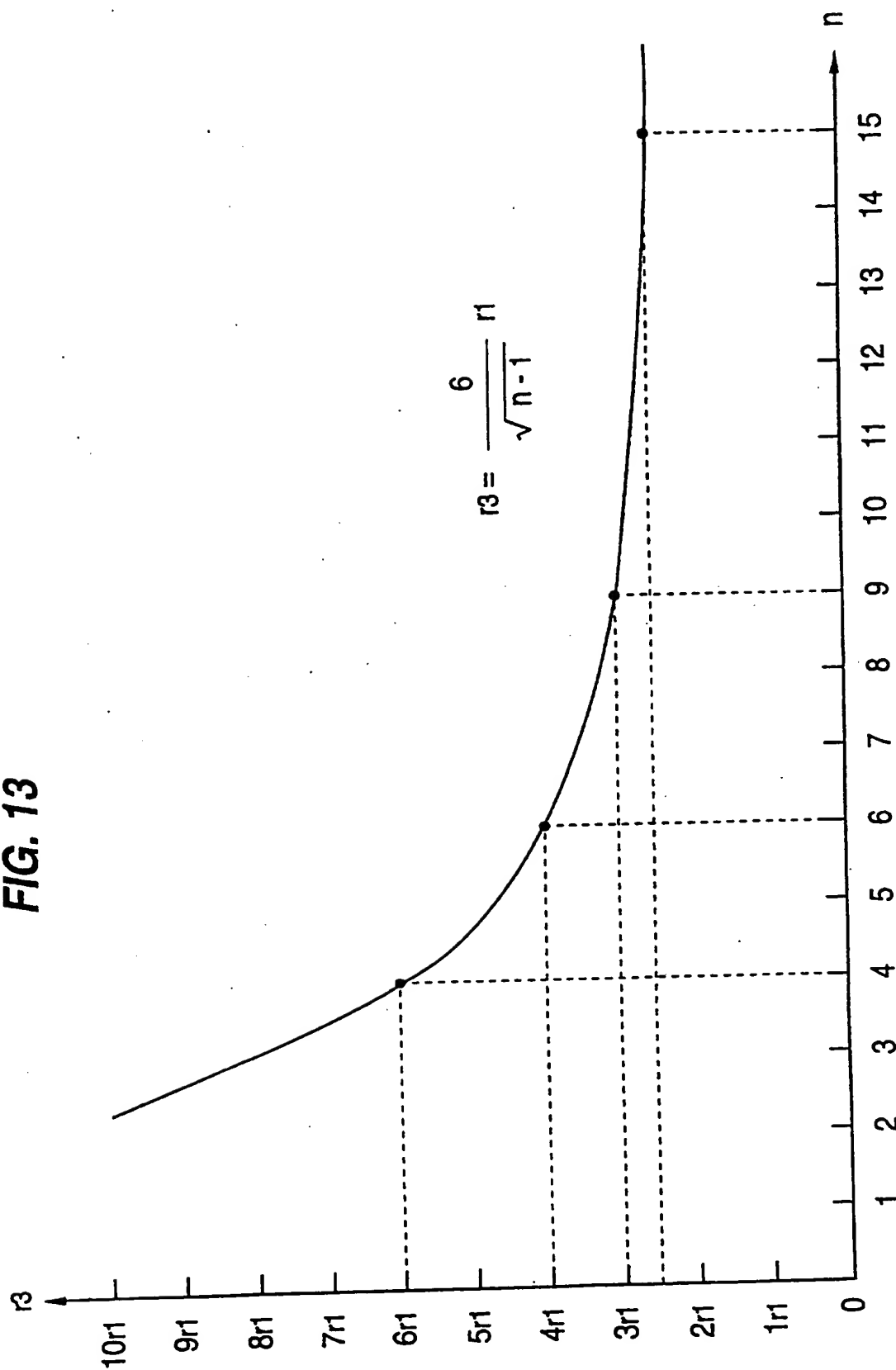


FIG. 14

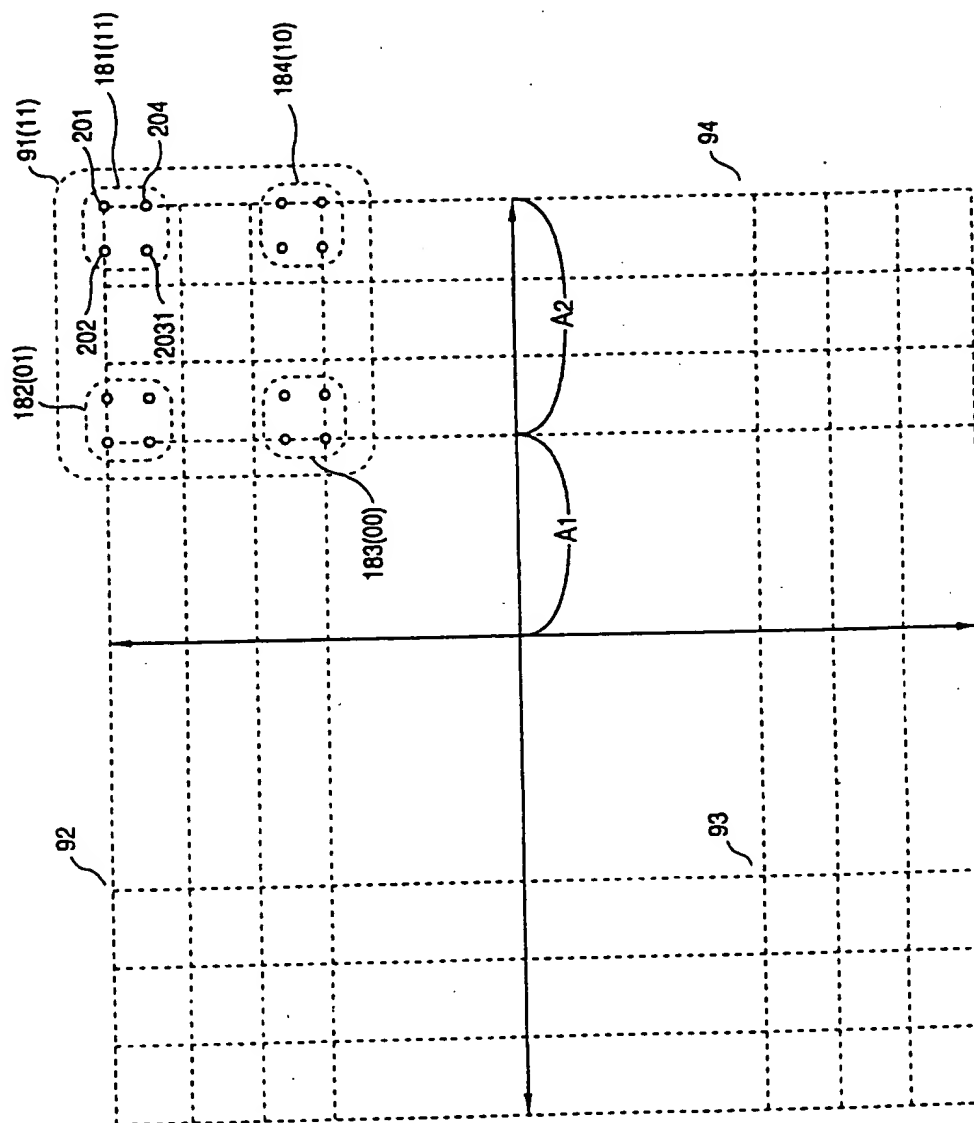


FIG. 15

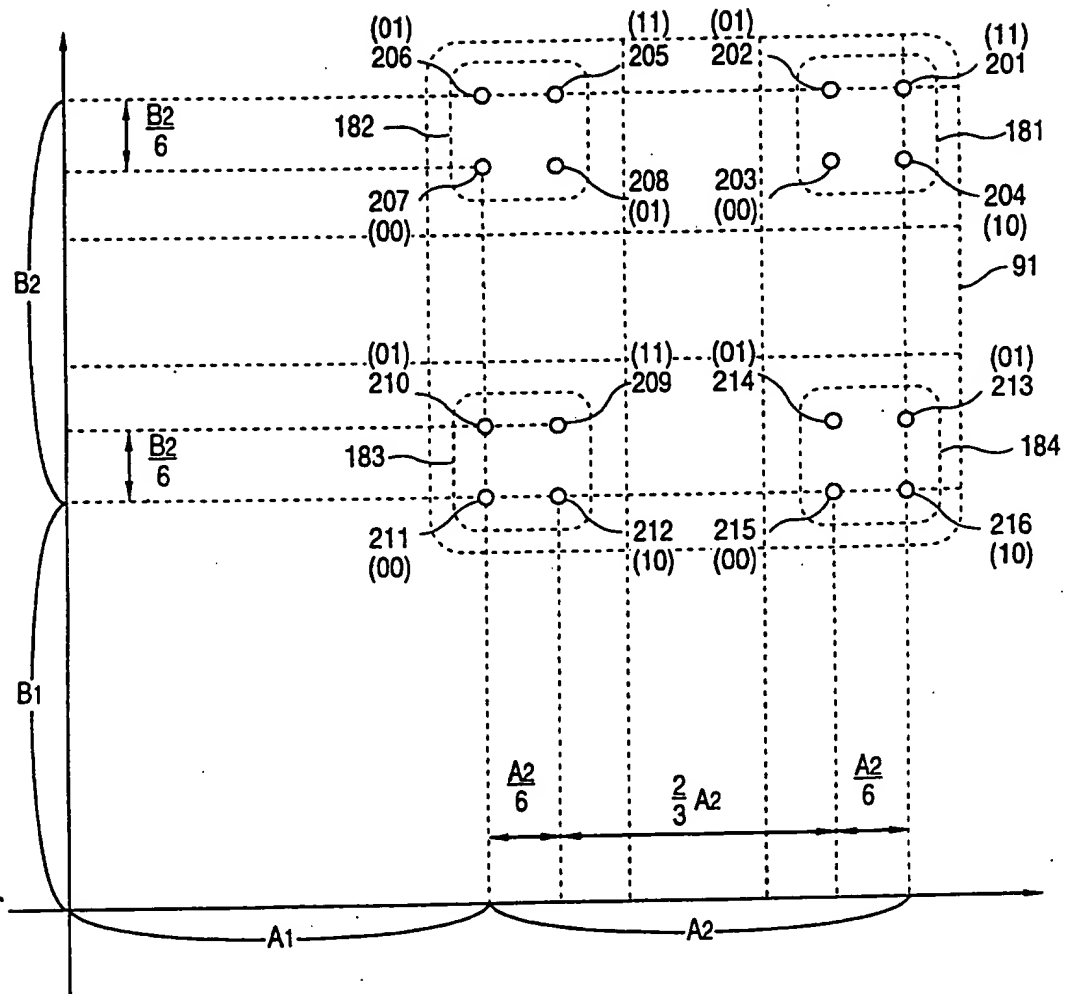


FIG. 16

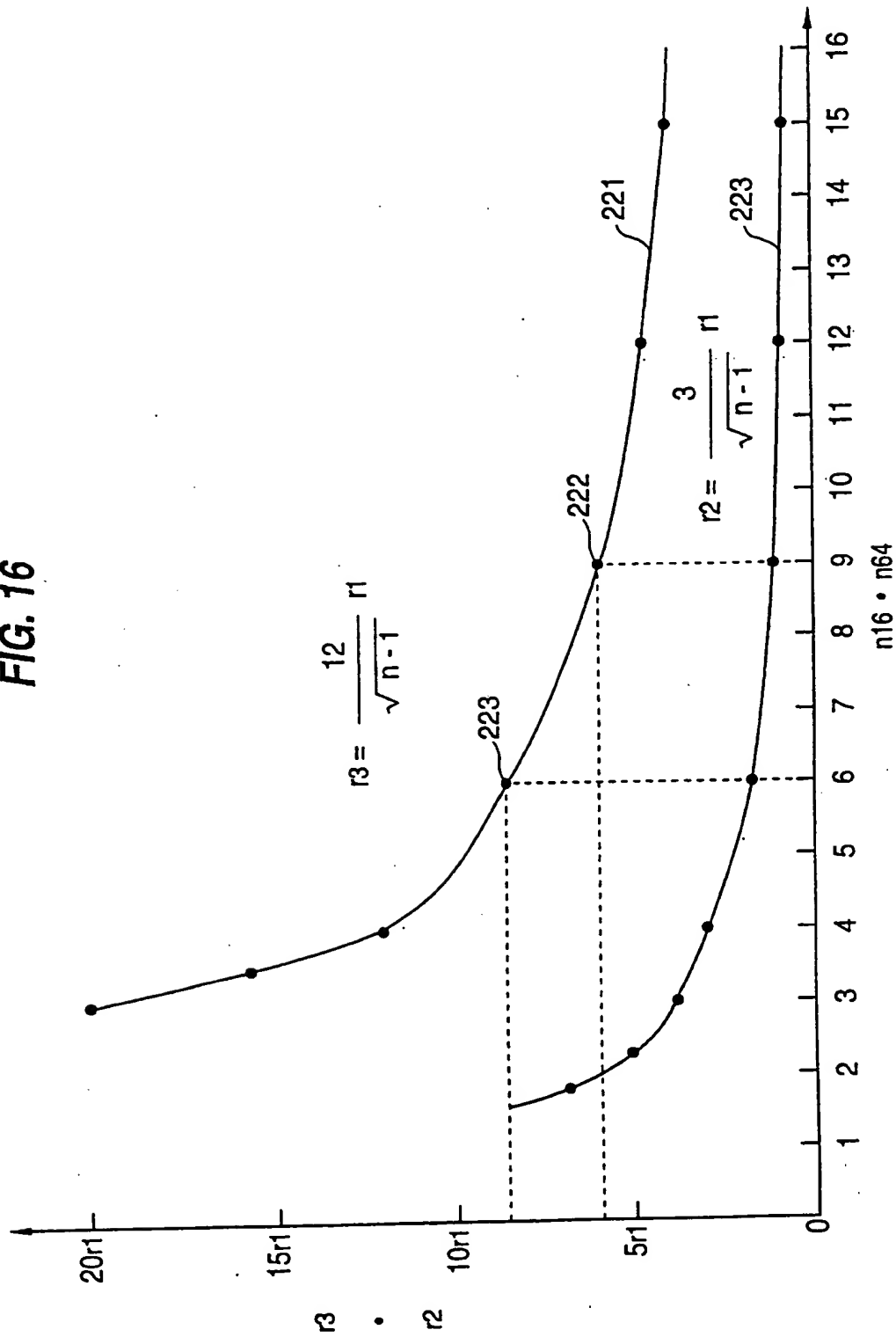


FIG. 17

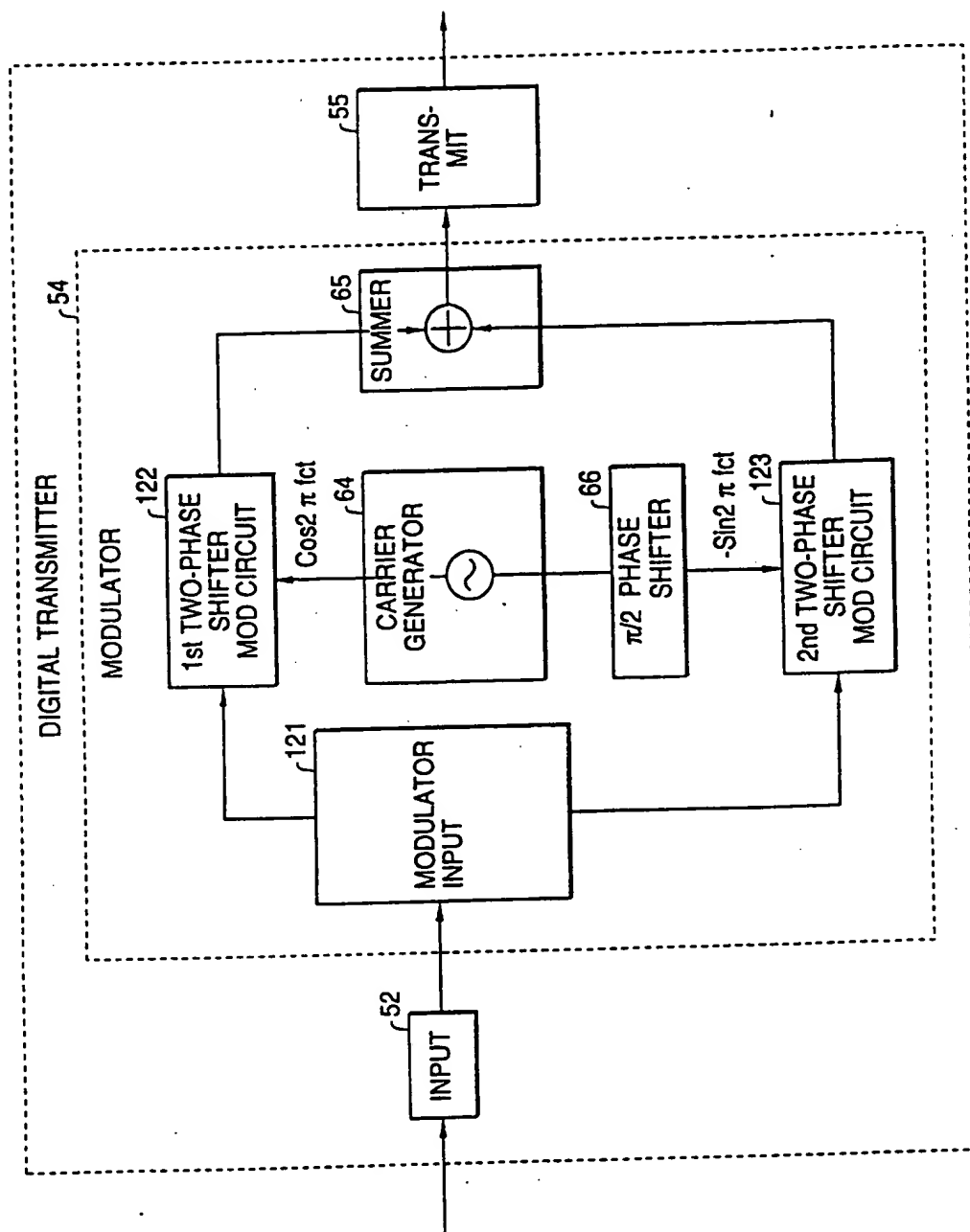


FIG. 18

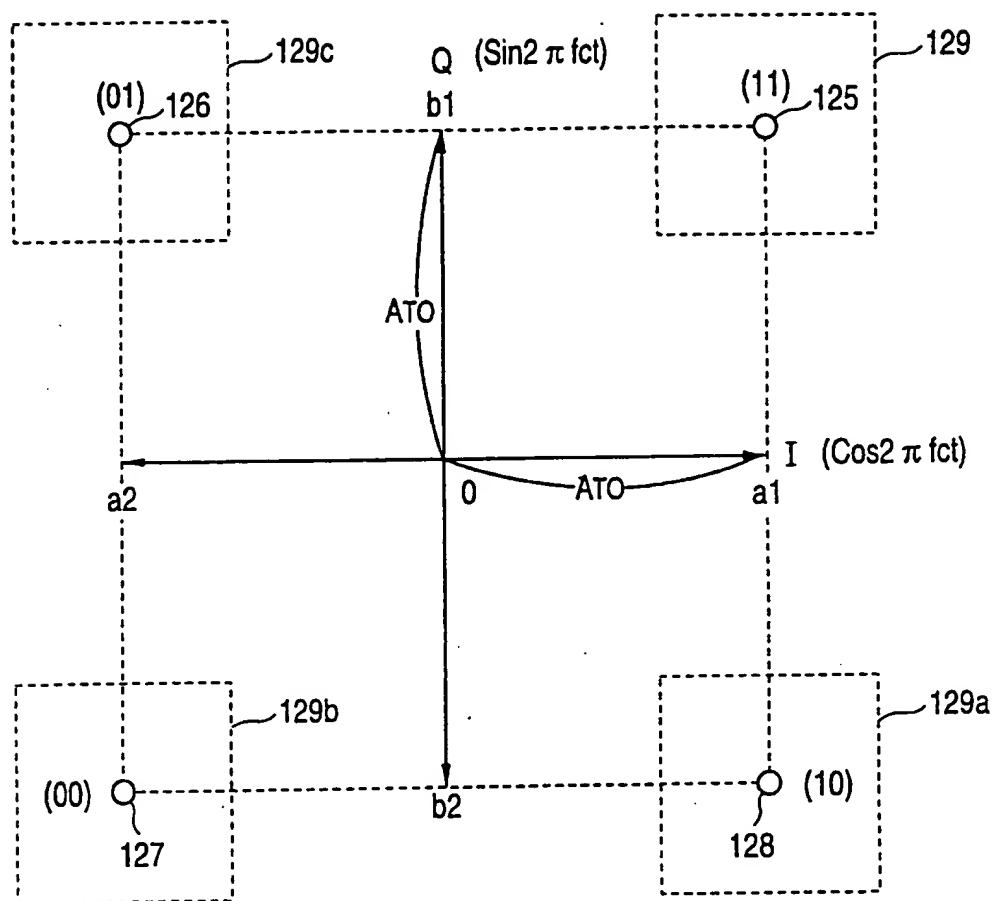


FIG. 19

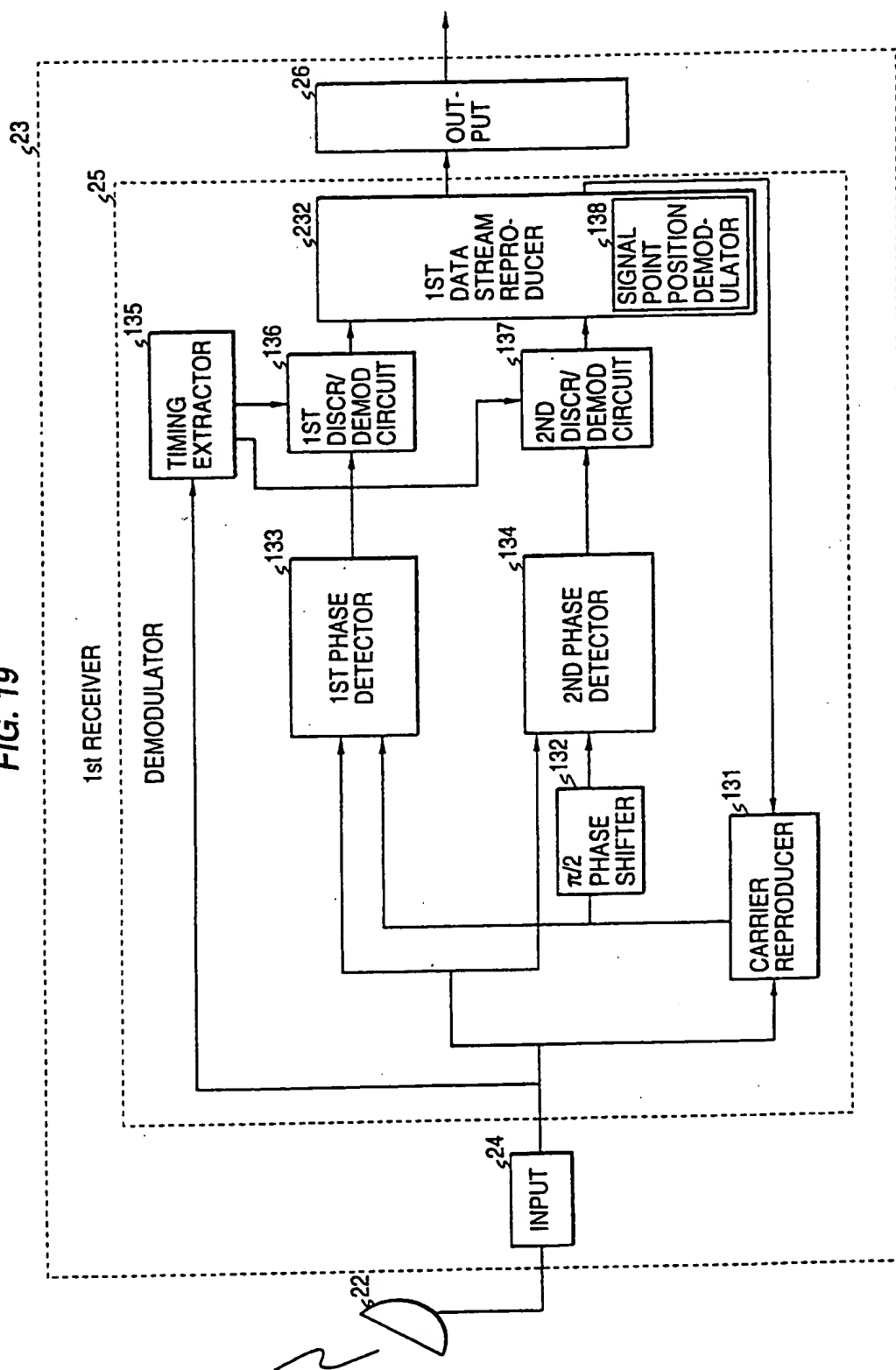


FIG. 20

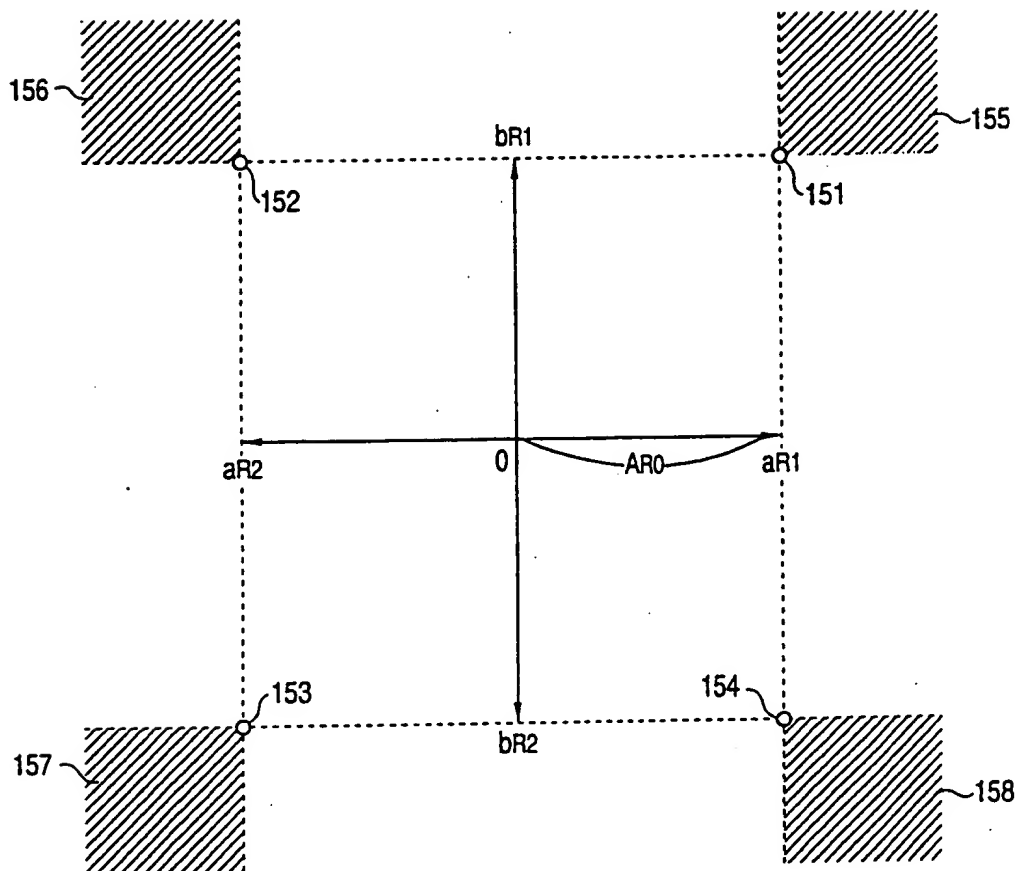


FIG. 21

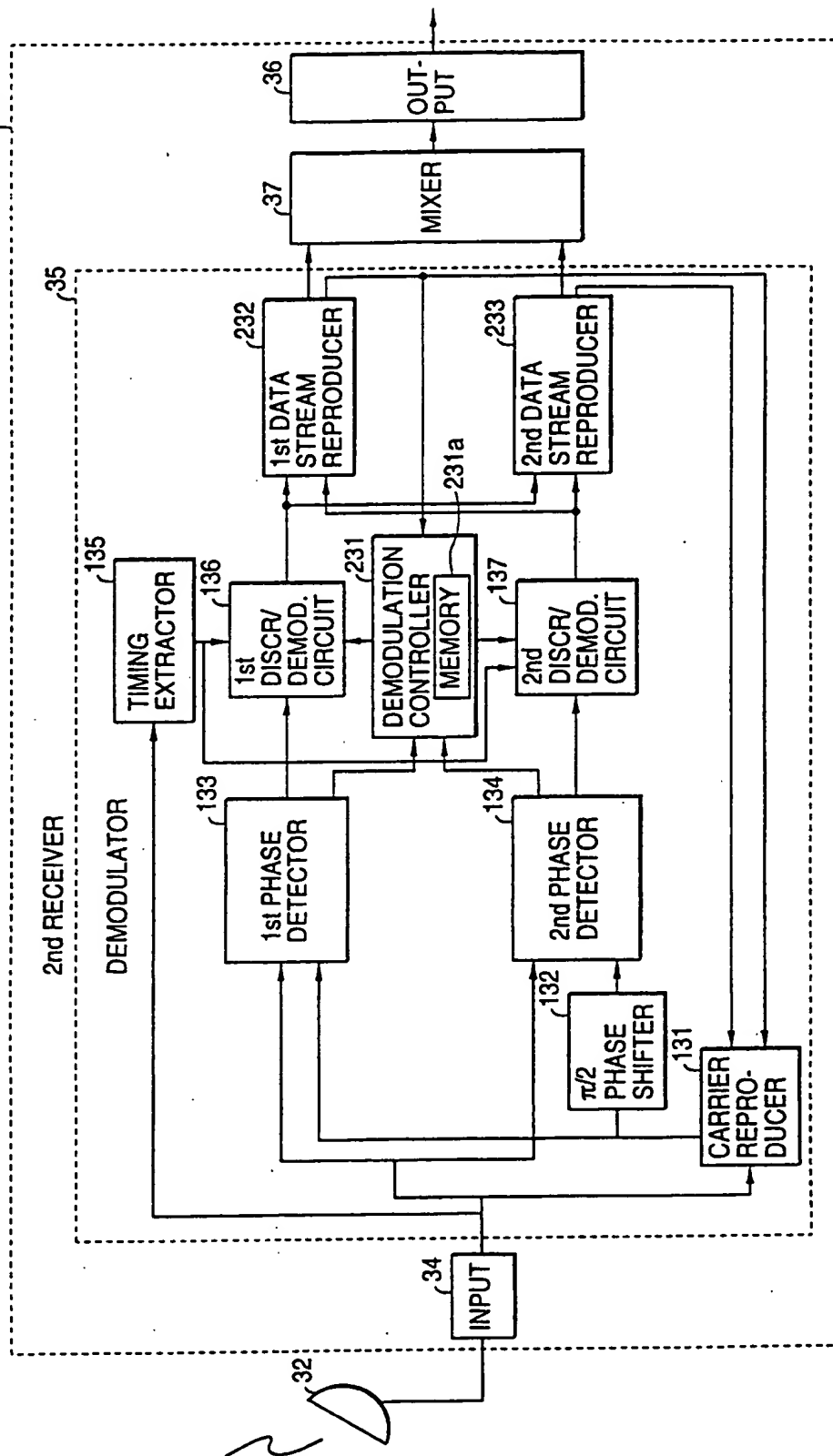
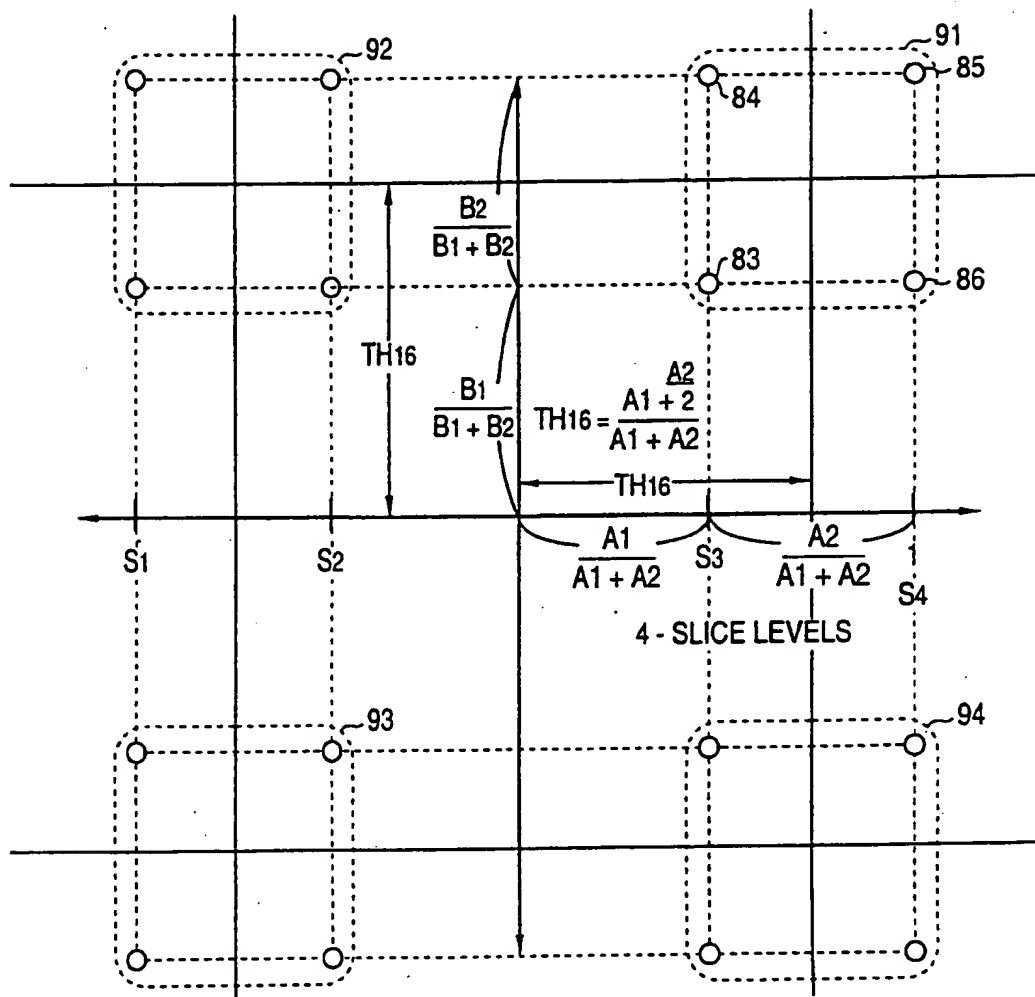


FIG. 22



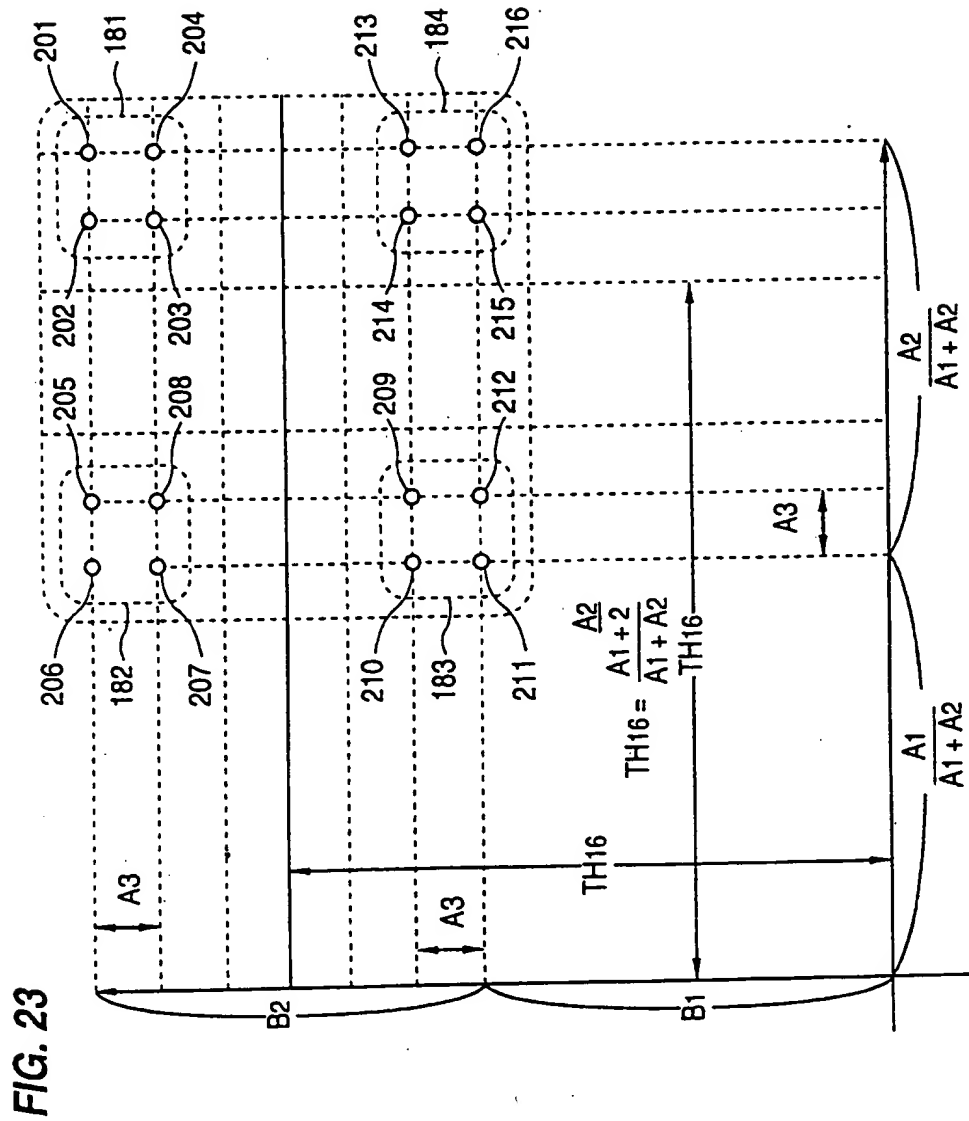


FIG. 24

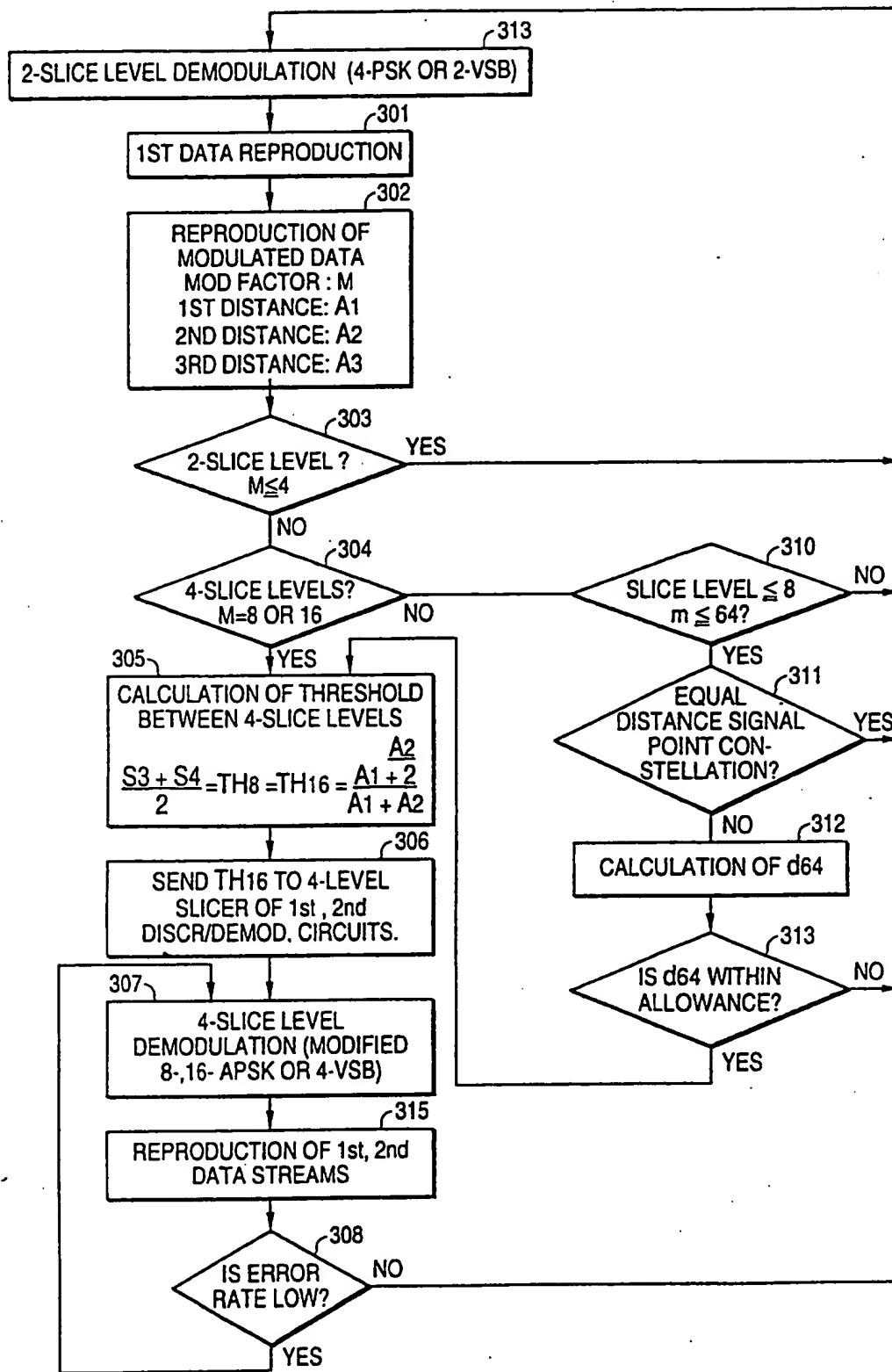


FIG. 25(a)

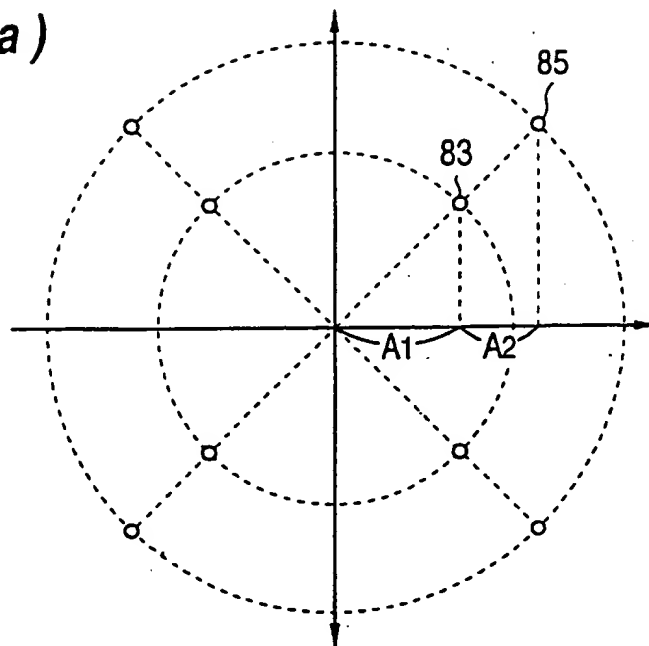


FIG. 25(b)

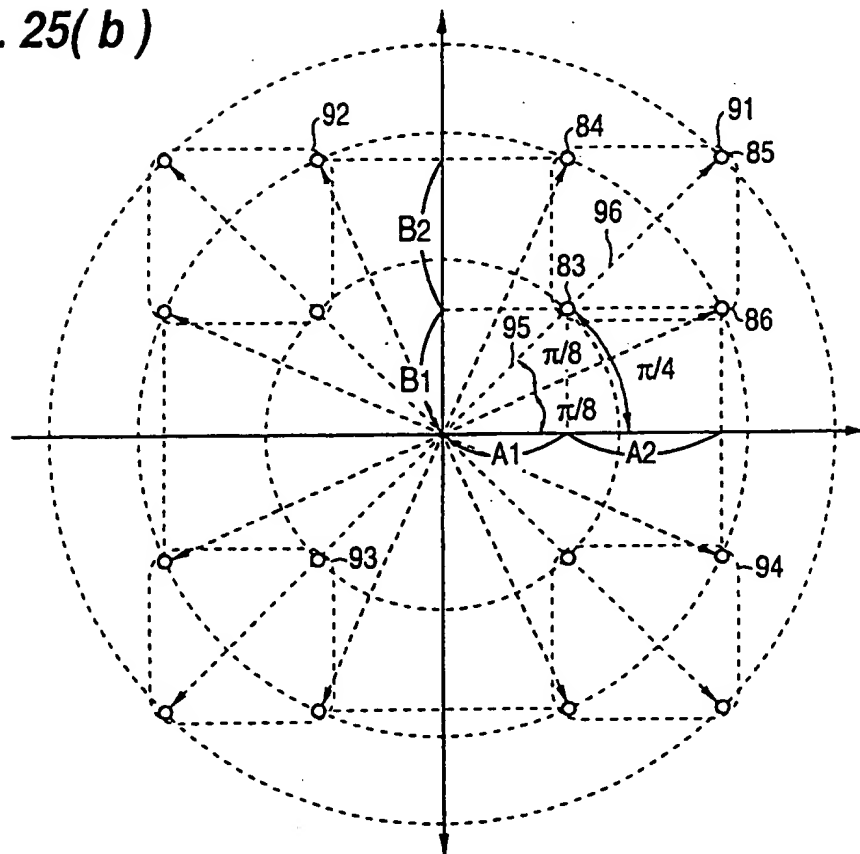
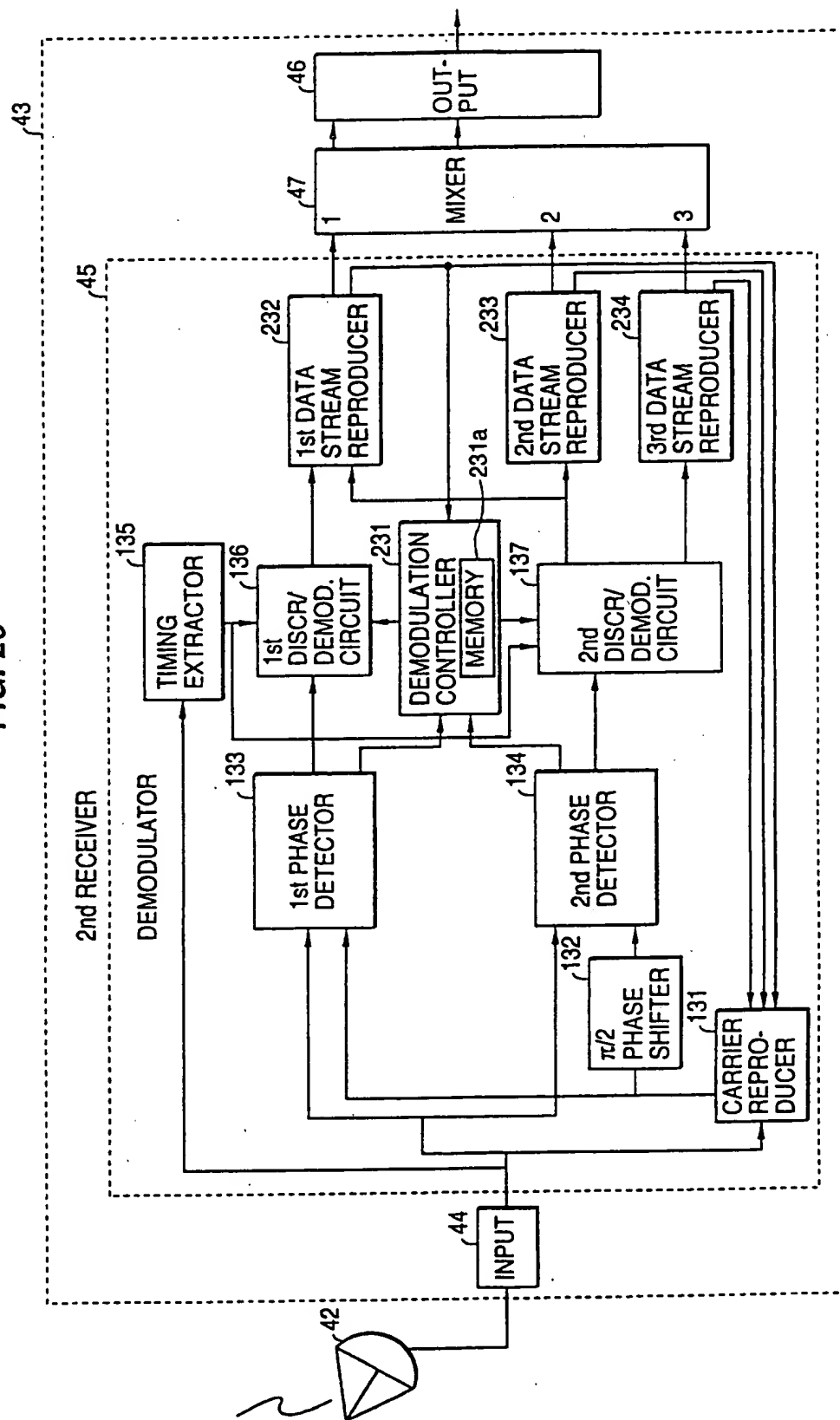


FIG. 26



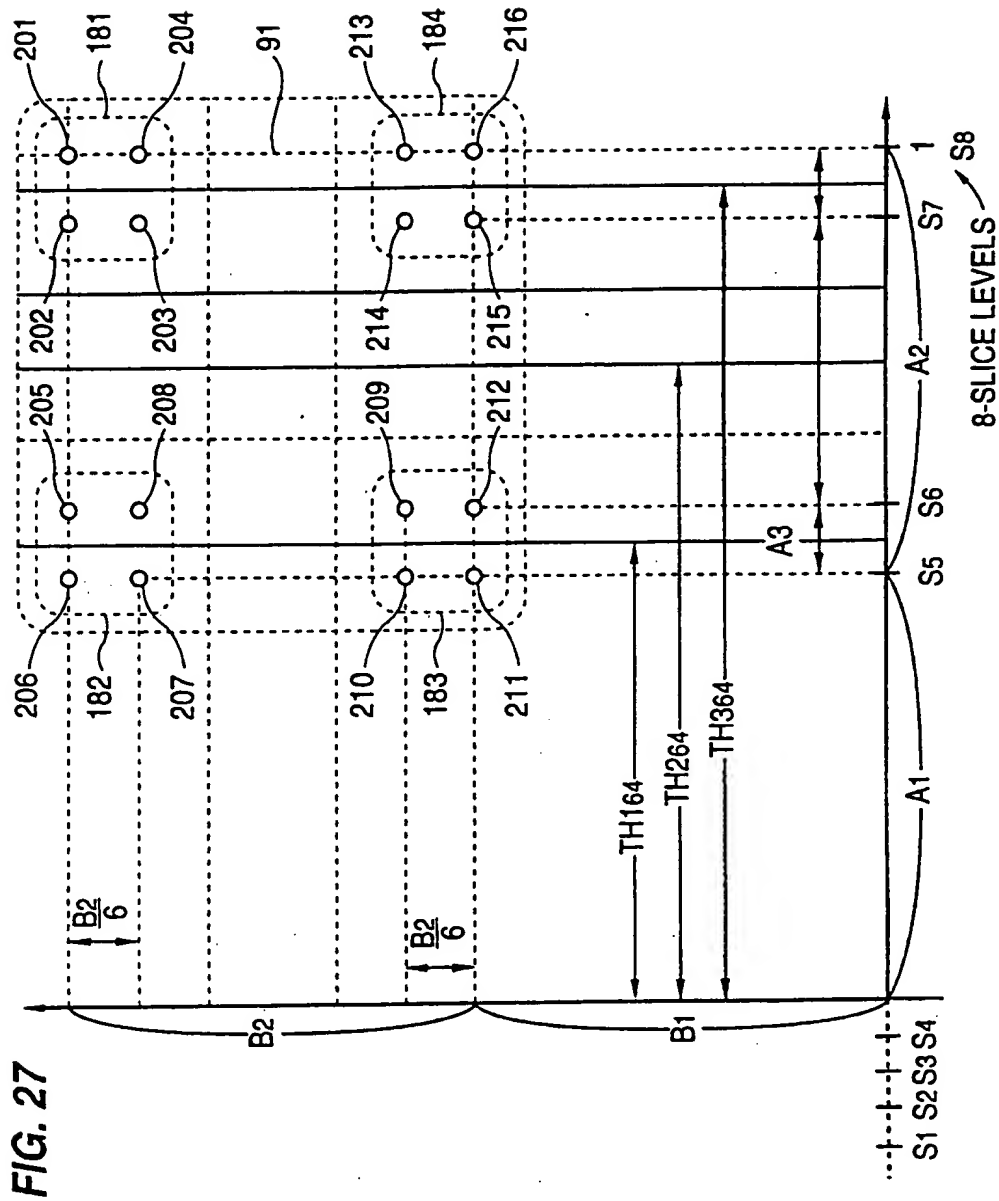
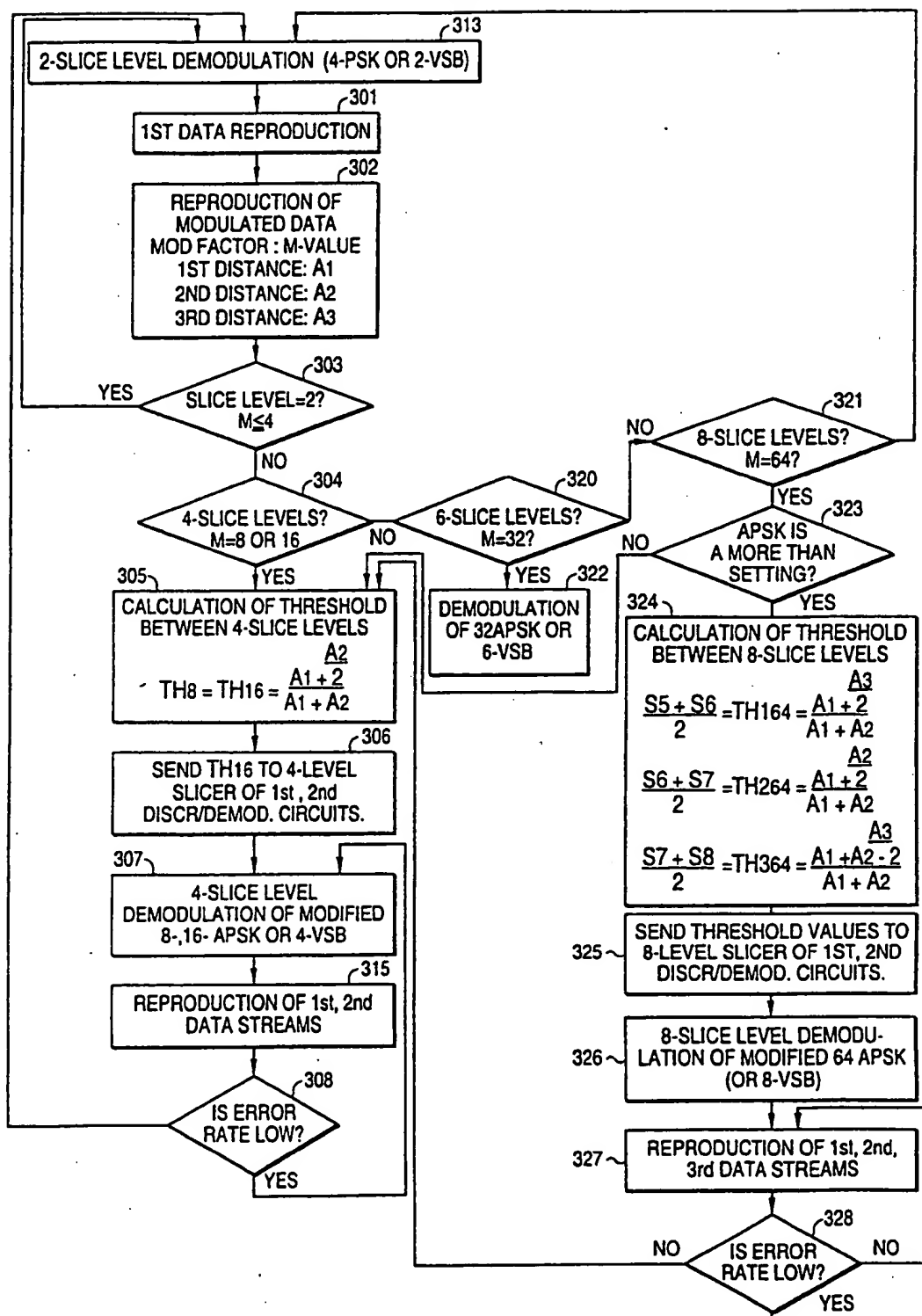


FIG. 28



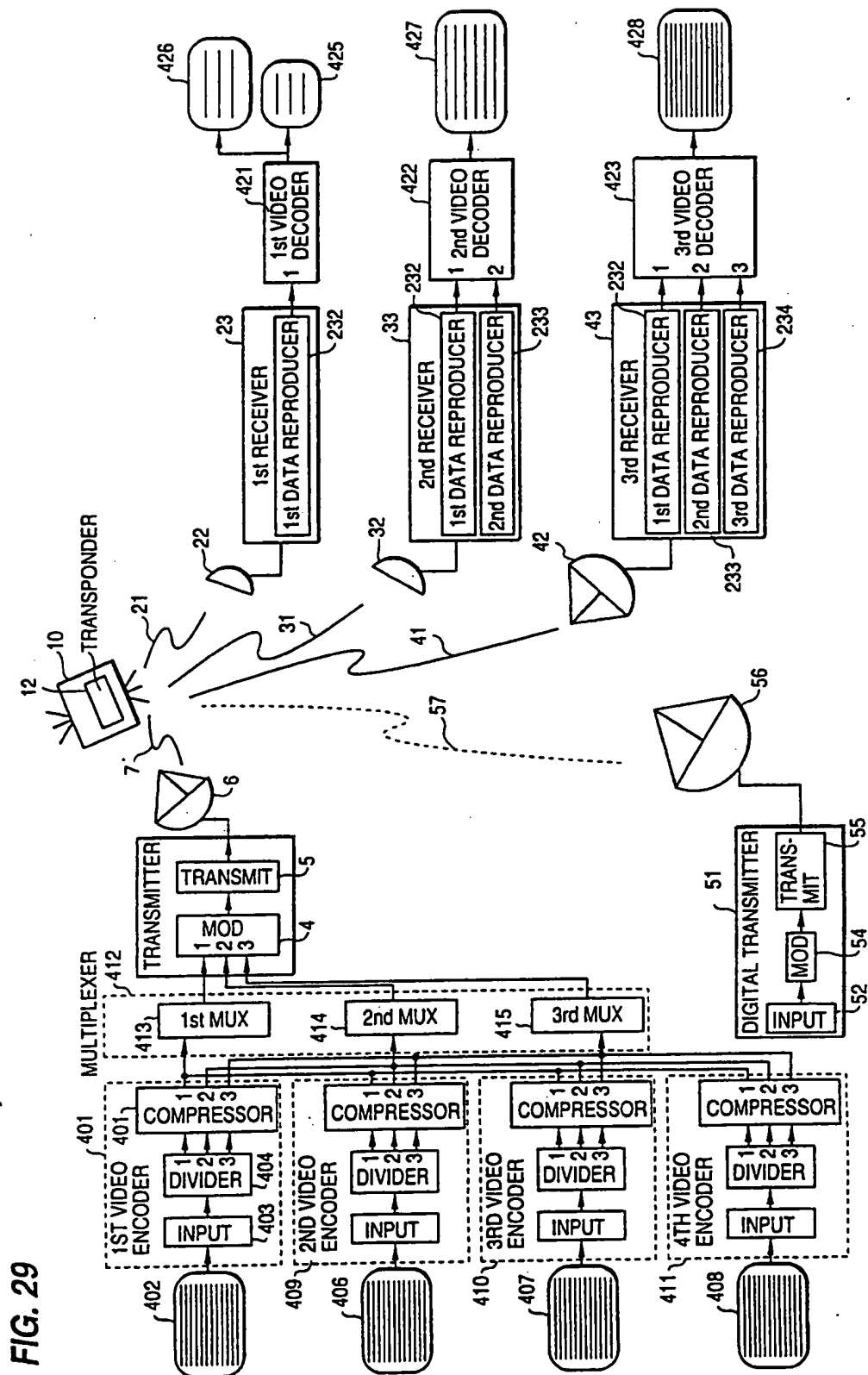
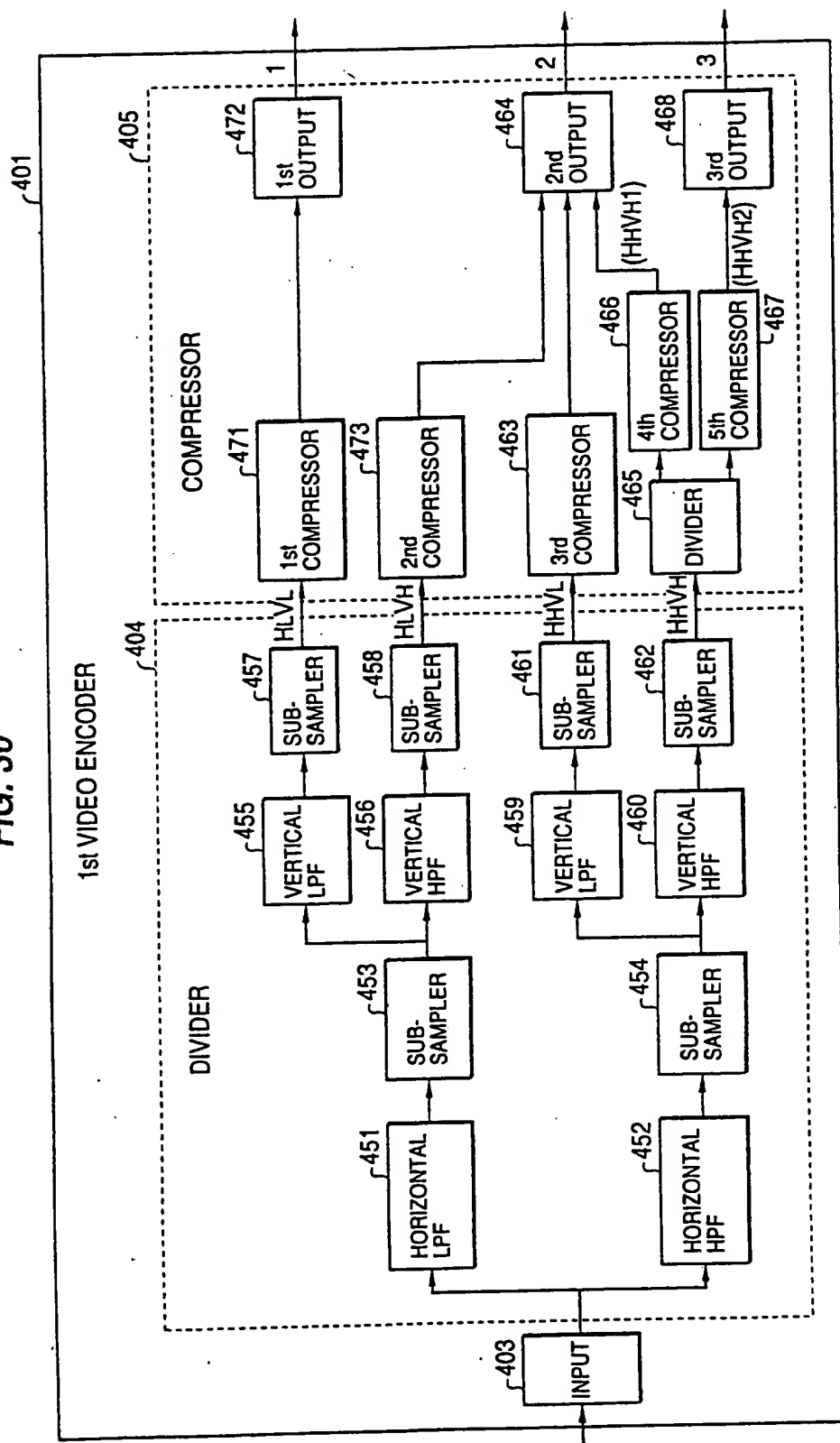


FIG. 30



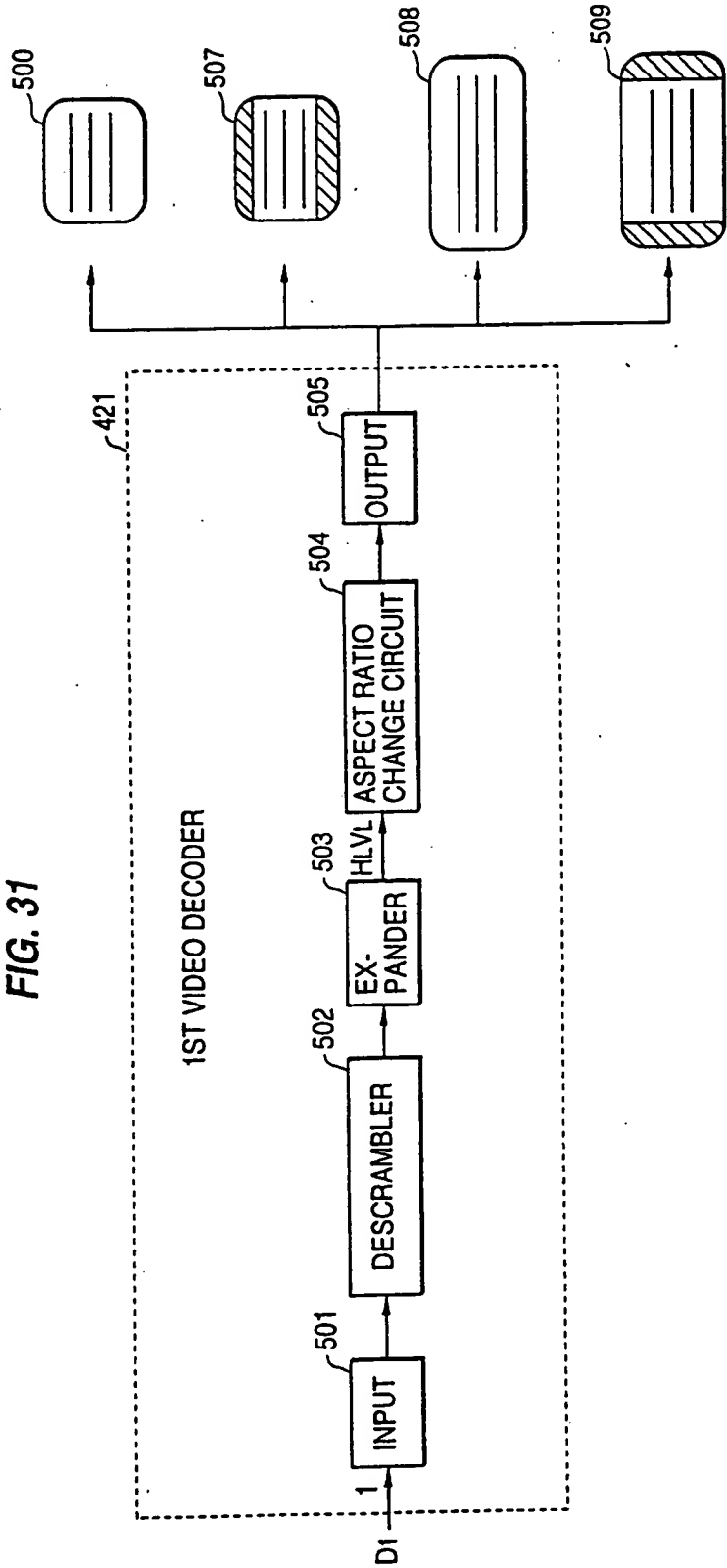


FIG. 32

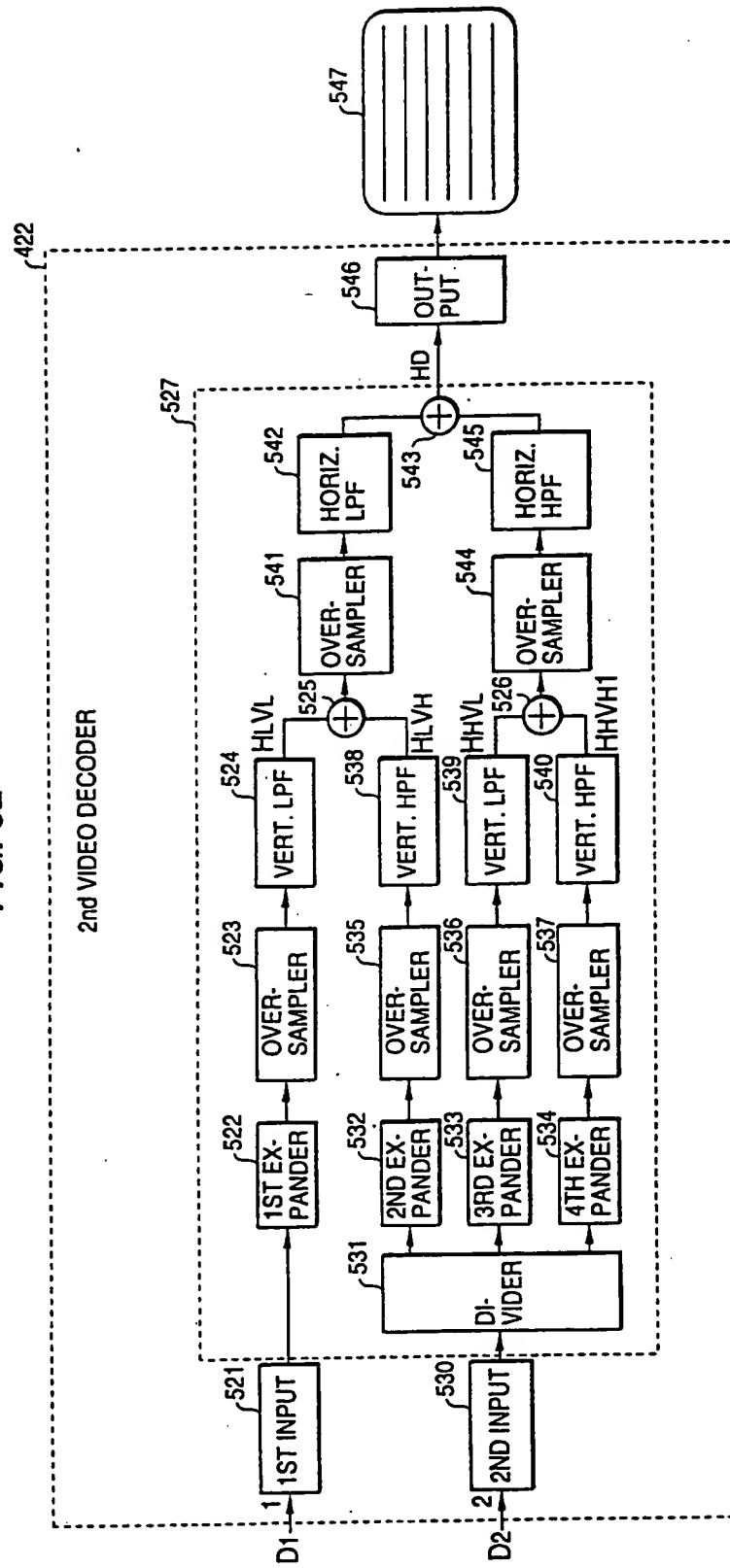


FIG. 33

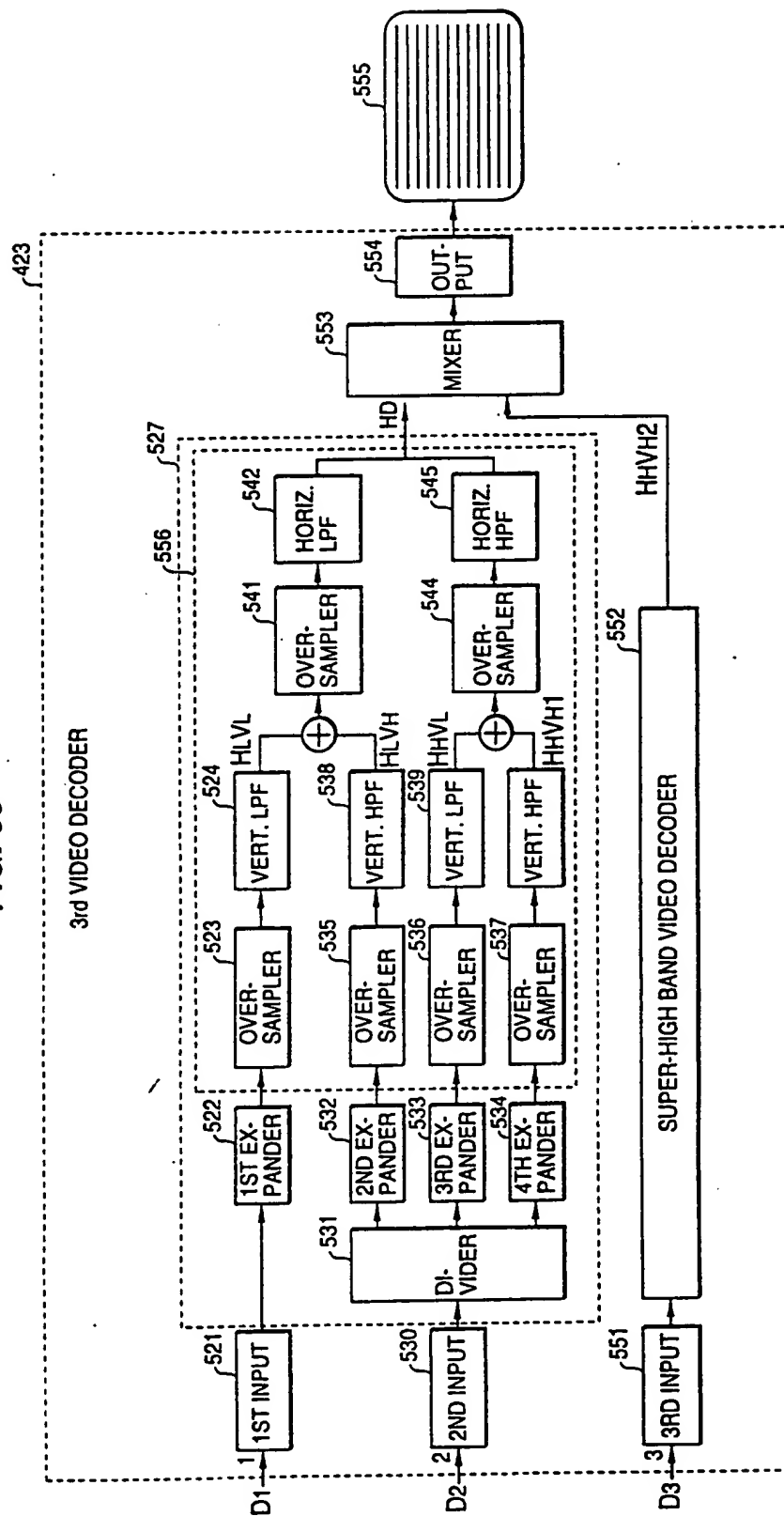
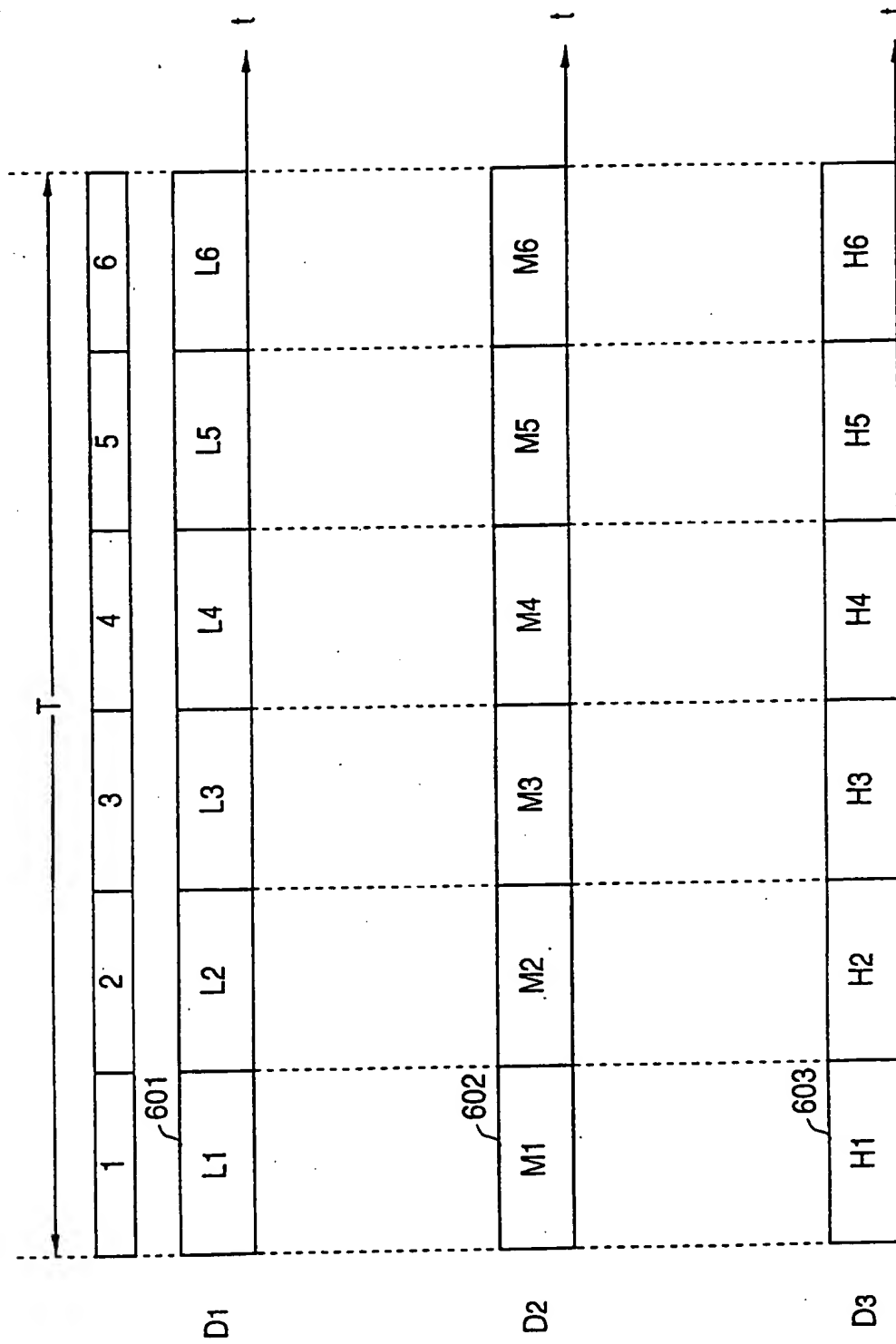


FIG. 34



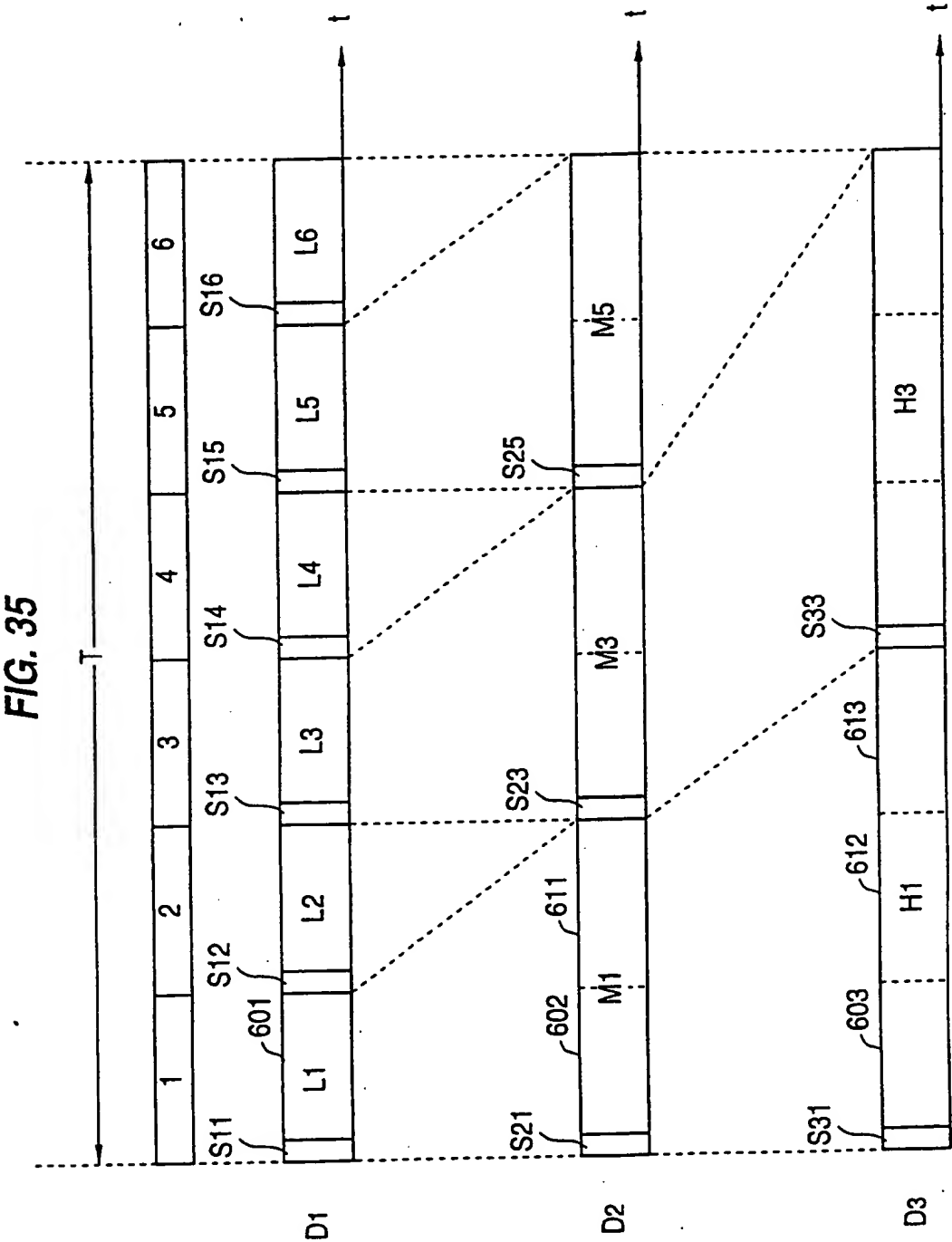


FIG. 36

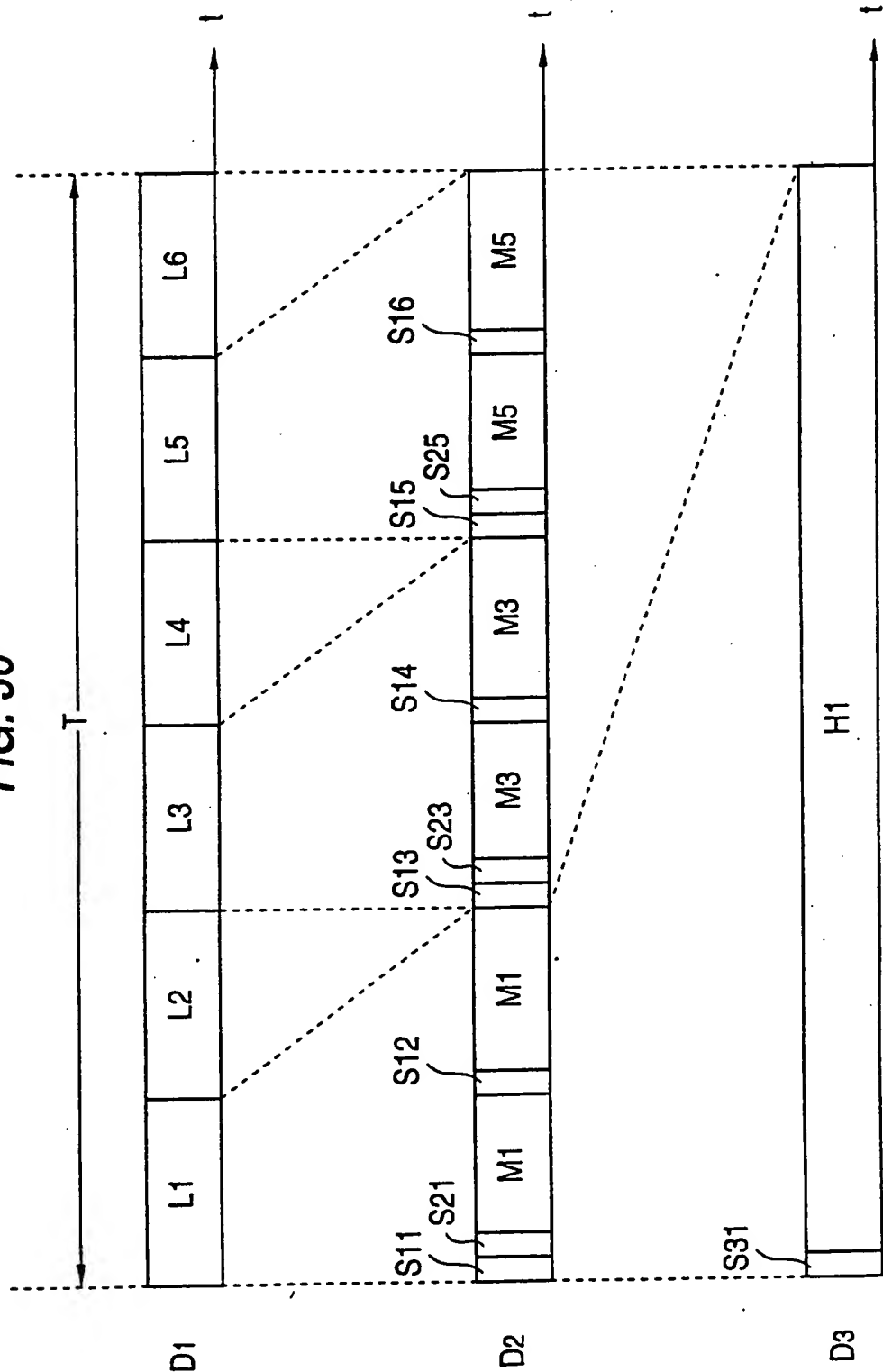


FIG. 37

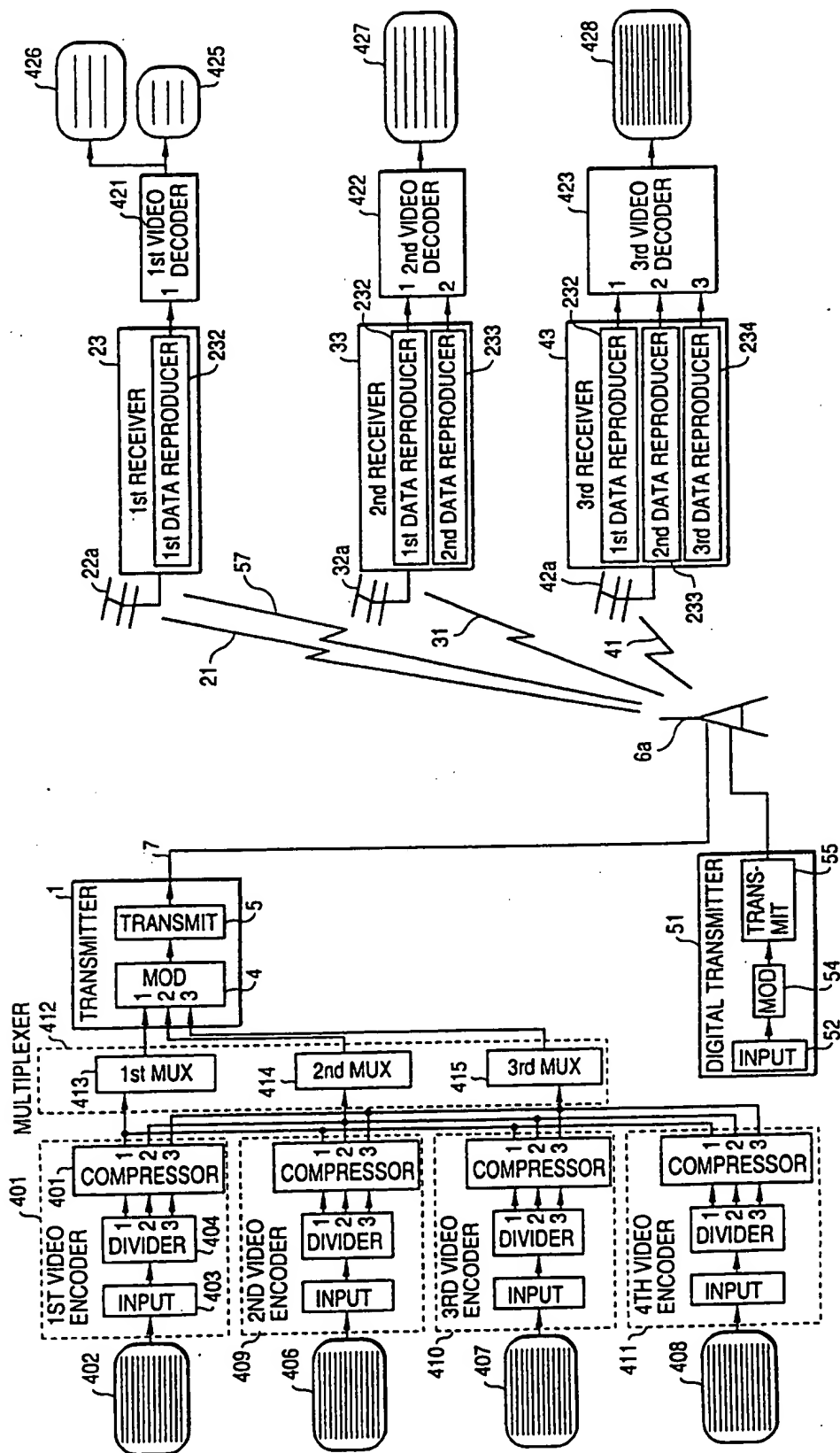


FIG. 38

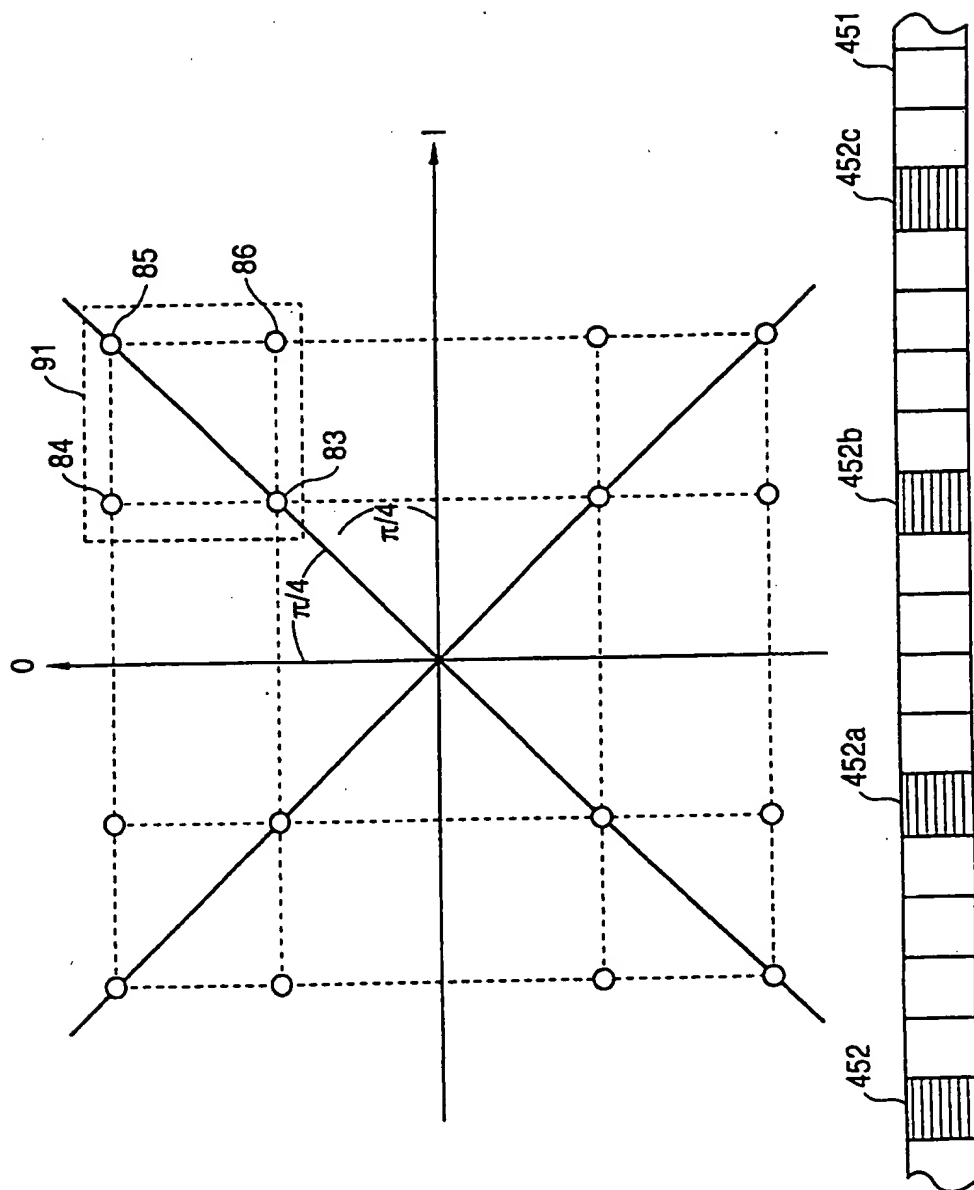


FIG. 39

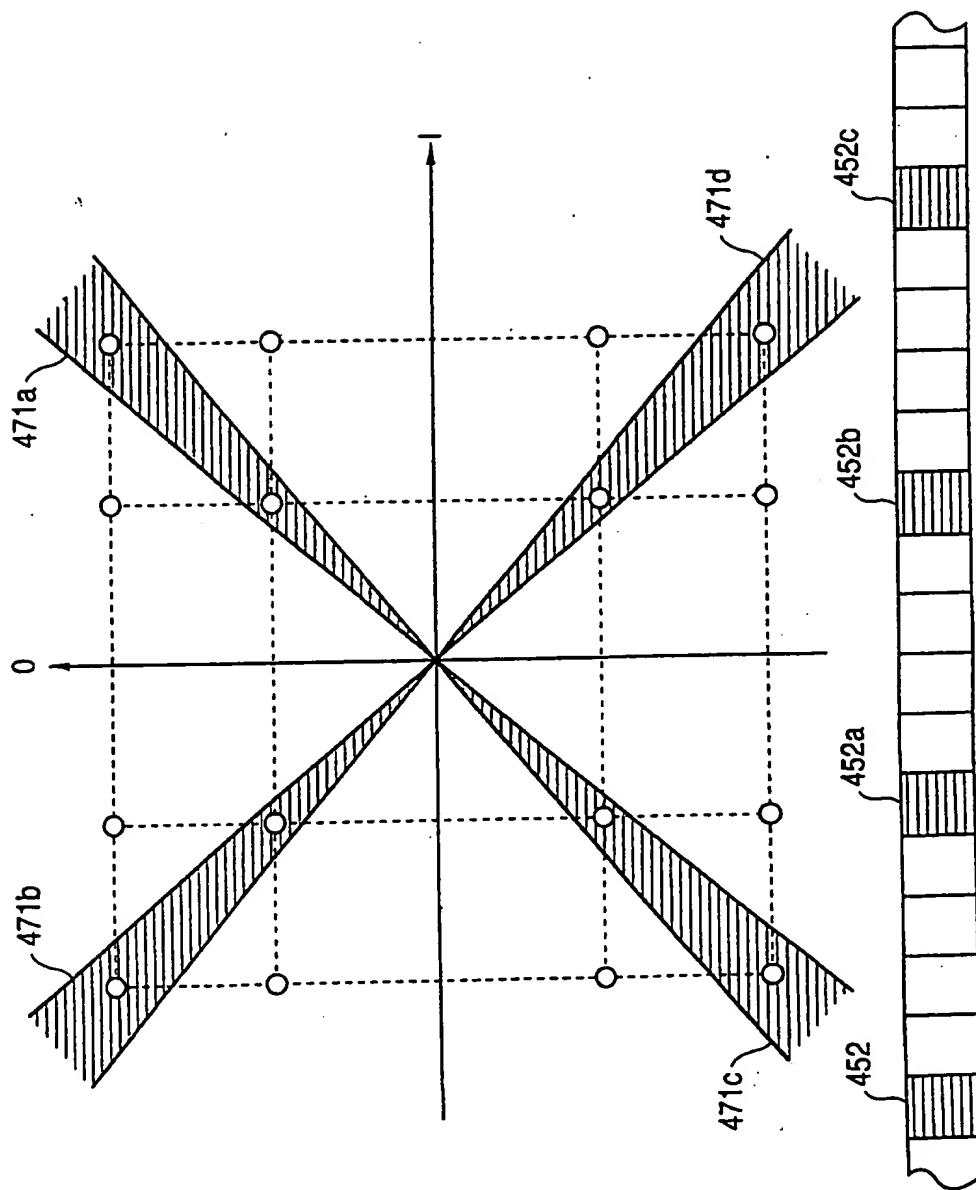


FIG. 40

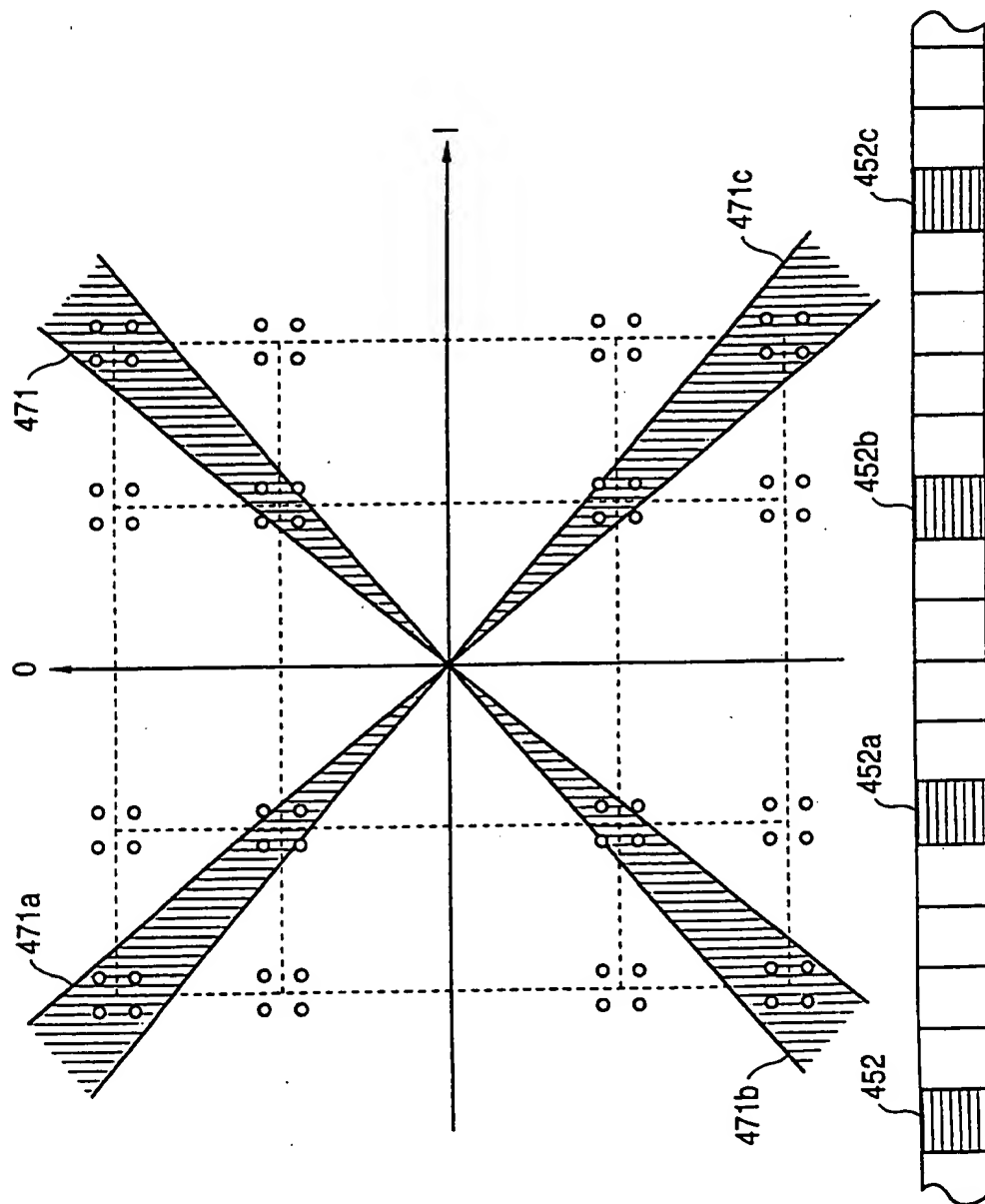


FIG. 41

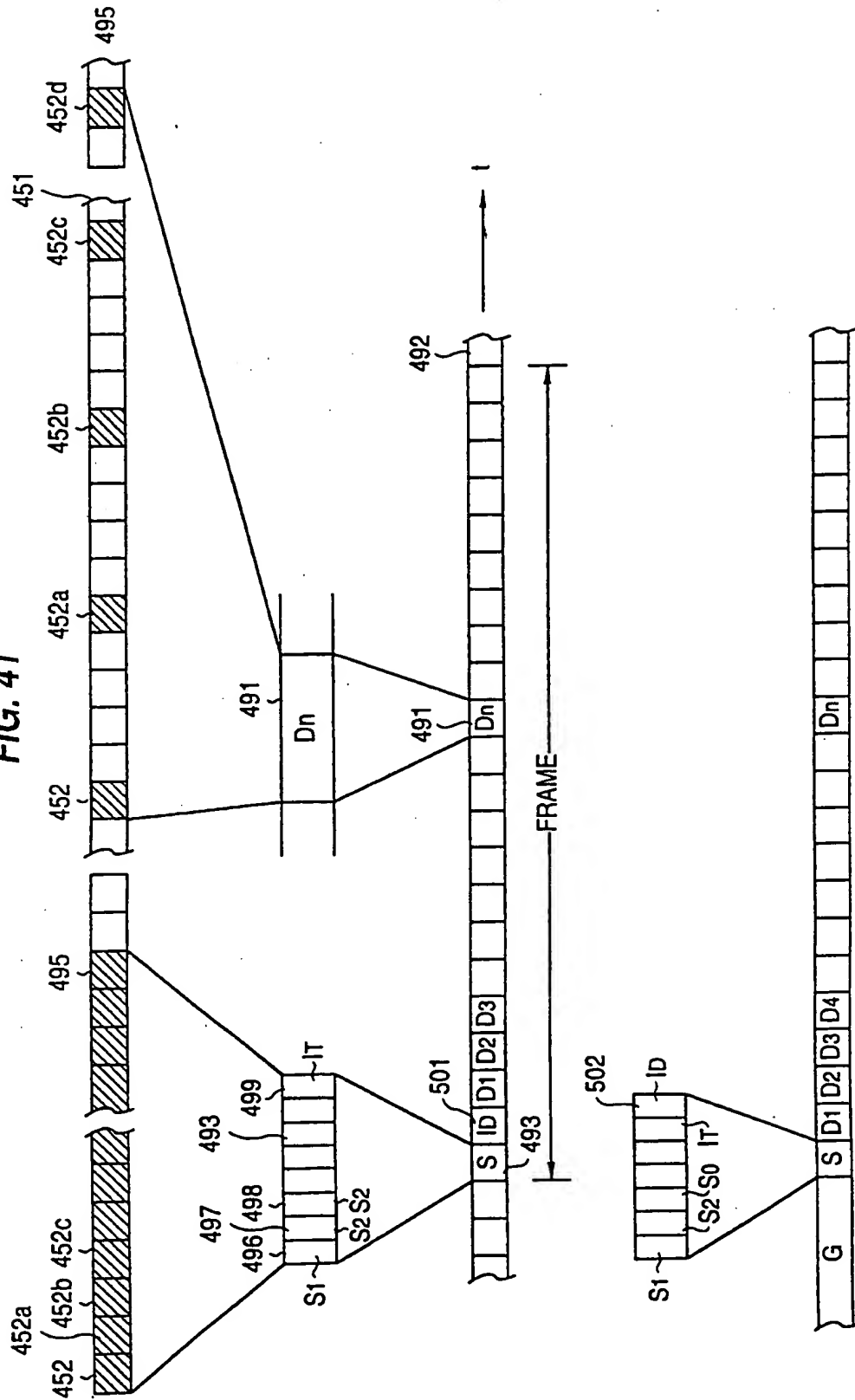


FIG. 42

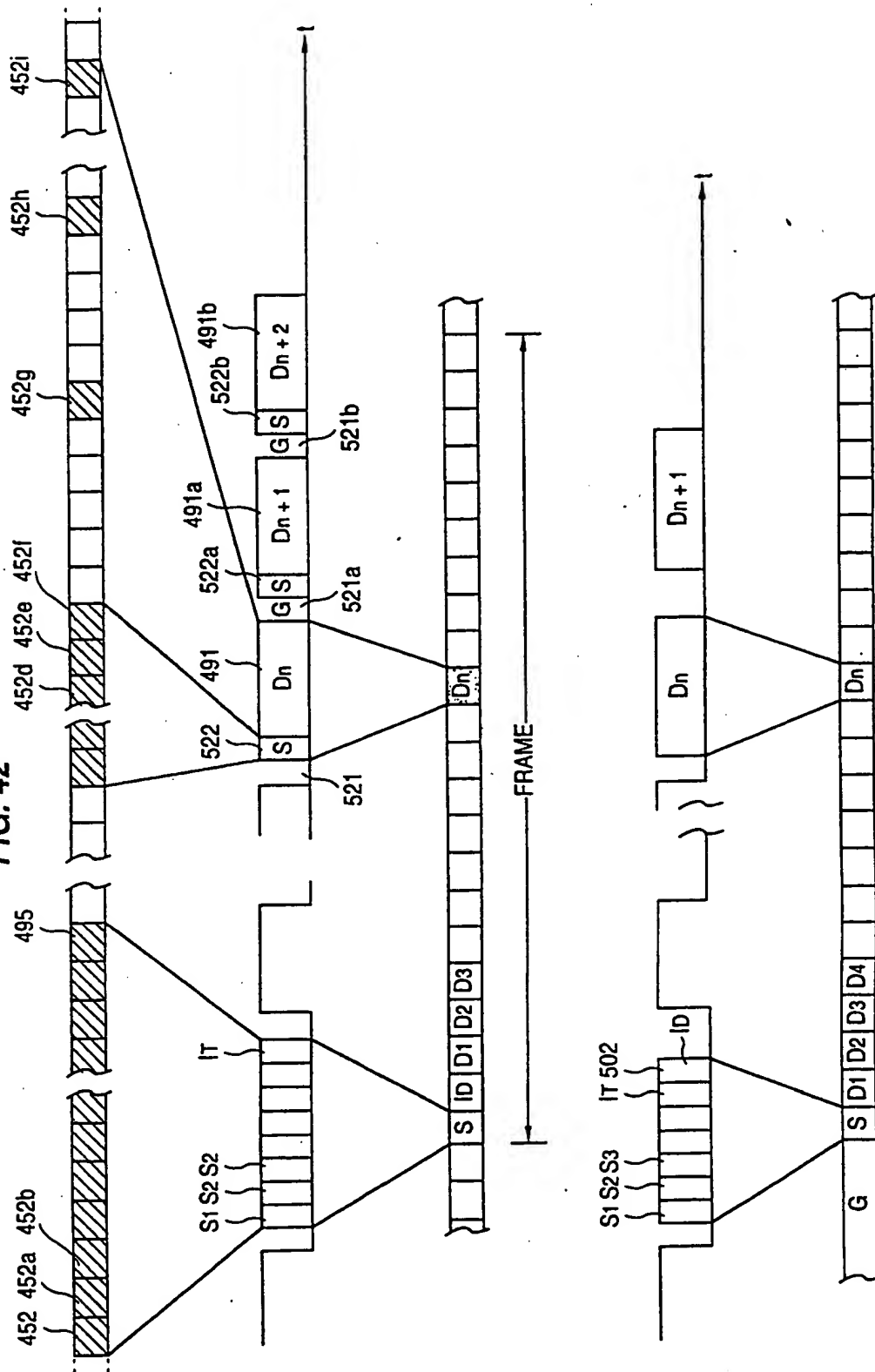


FIG. 43

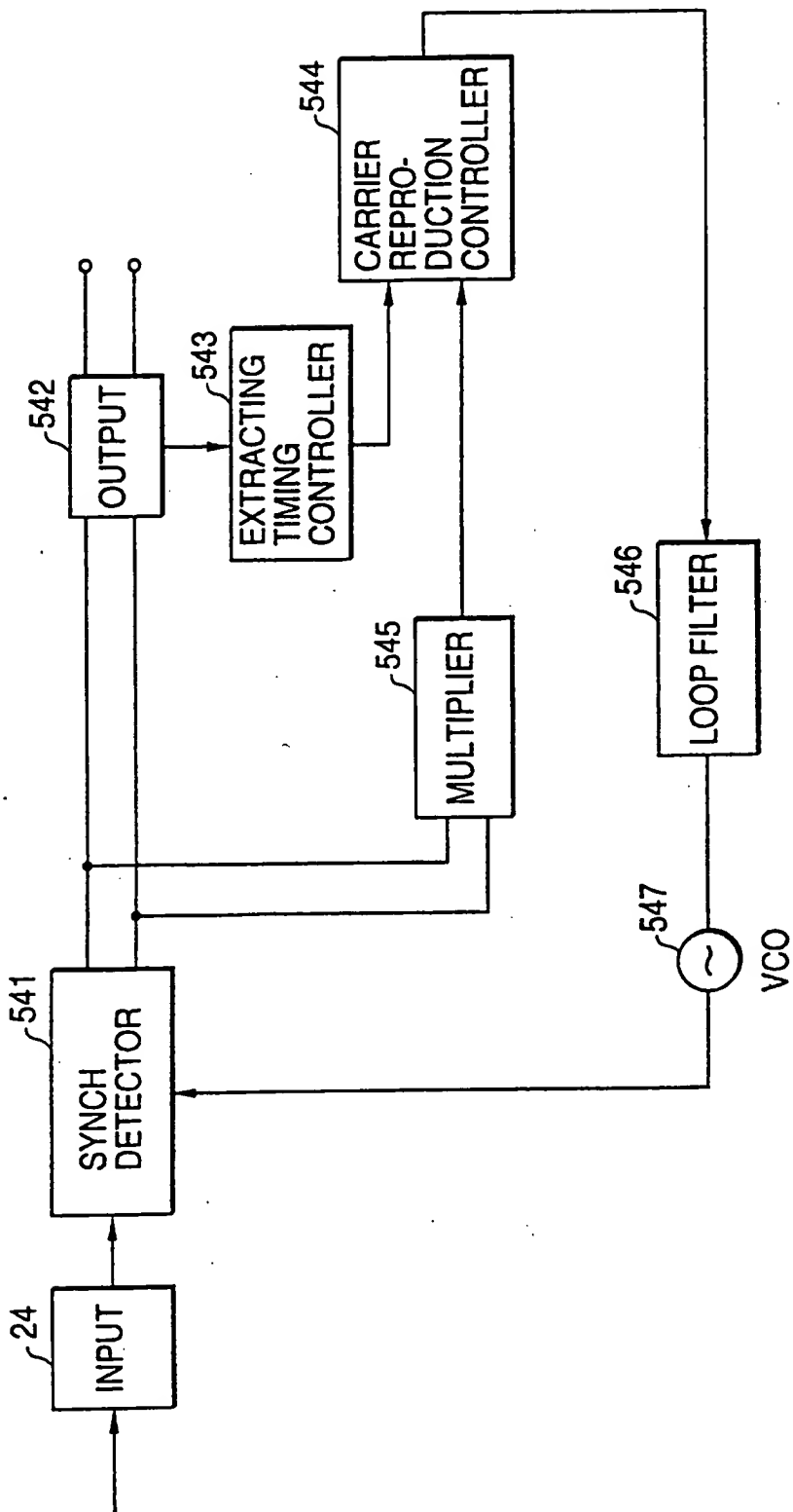


FIG. 44

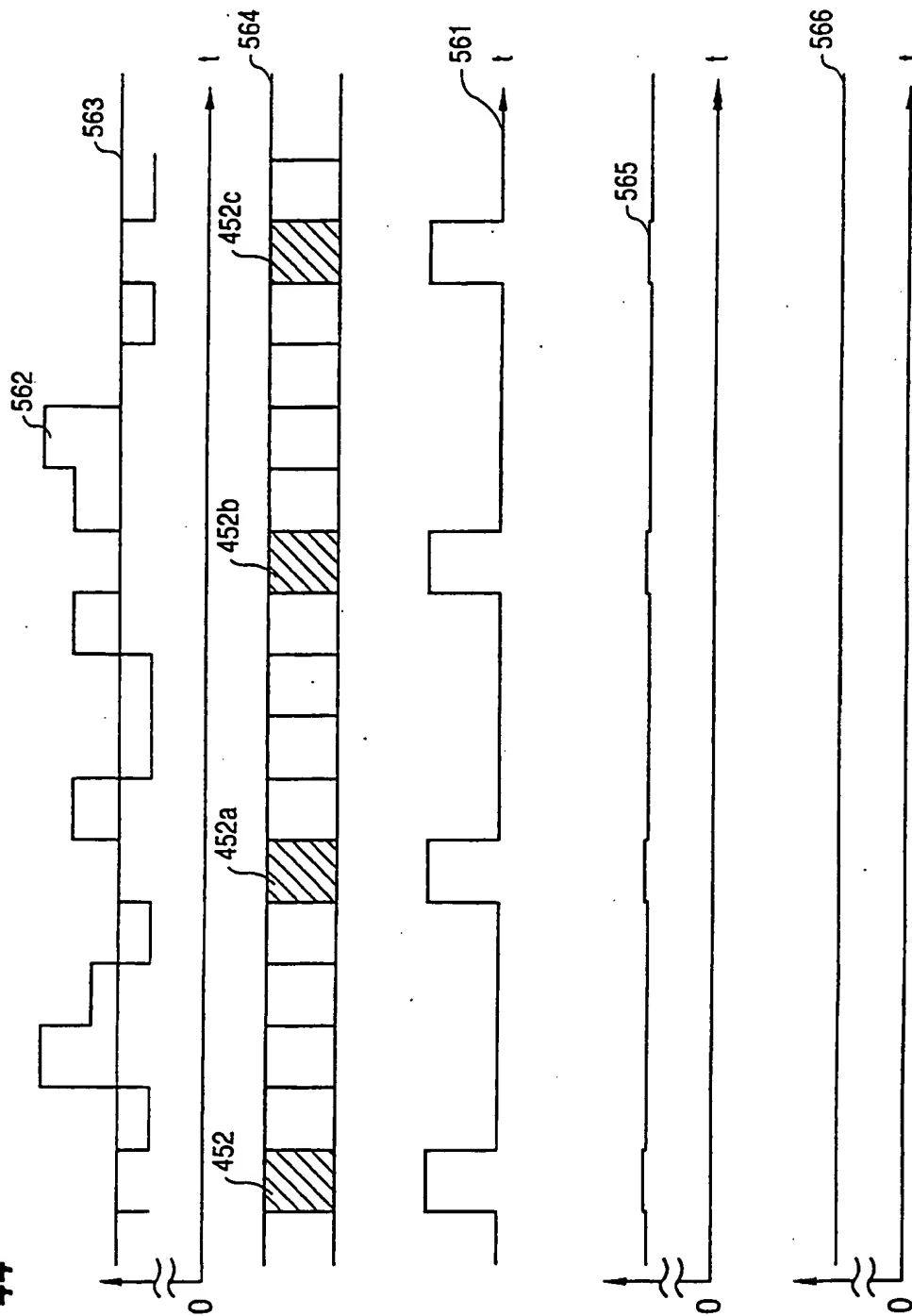


FIG. 45

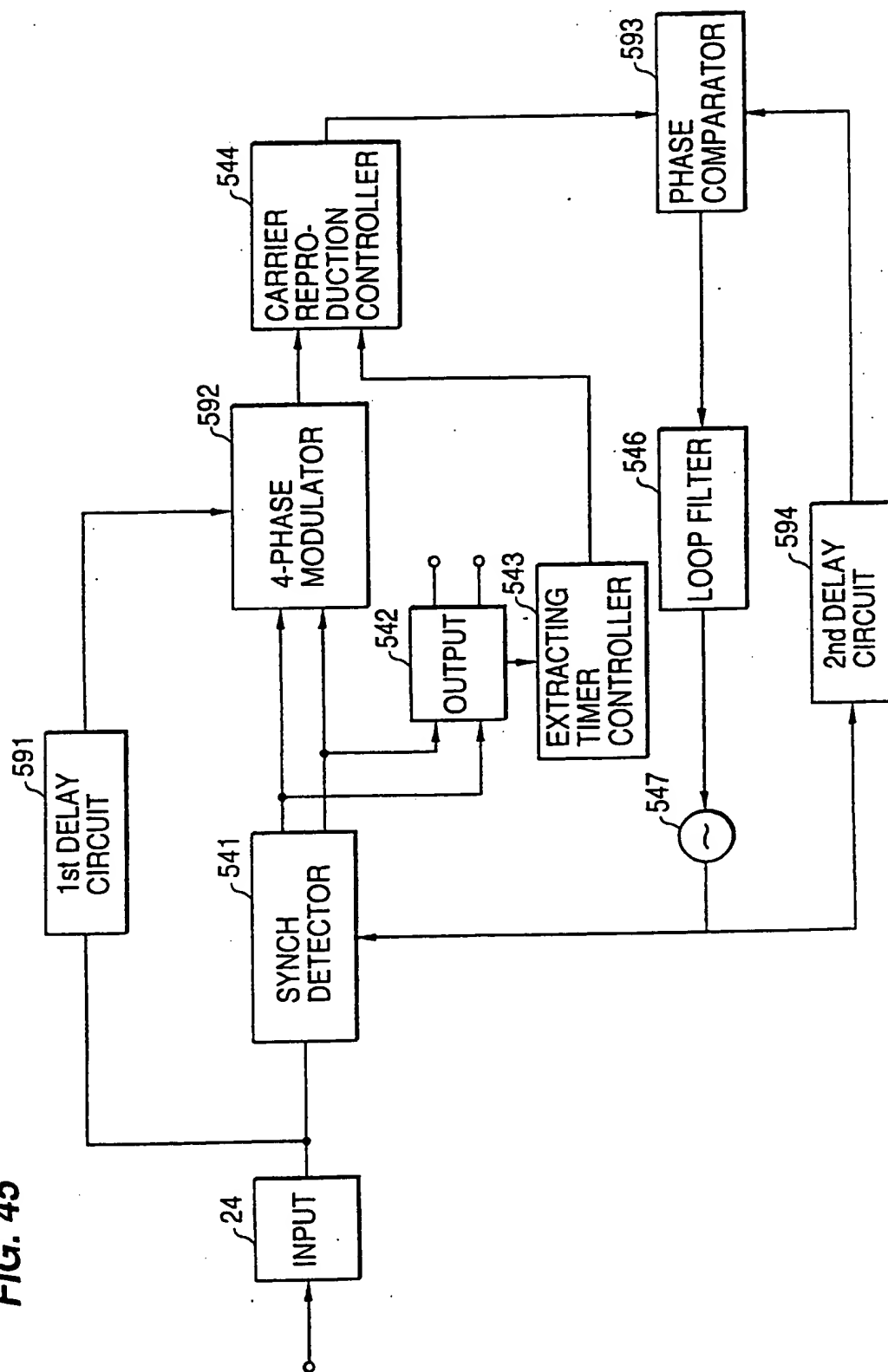


FIG. 46

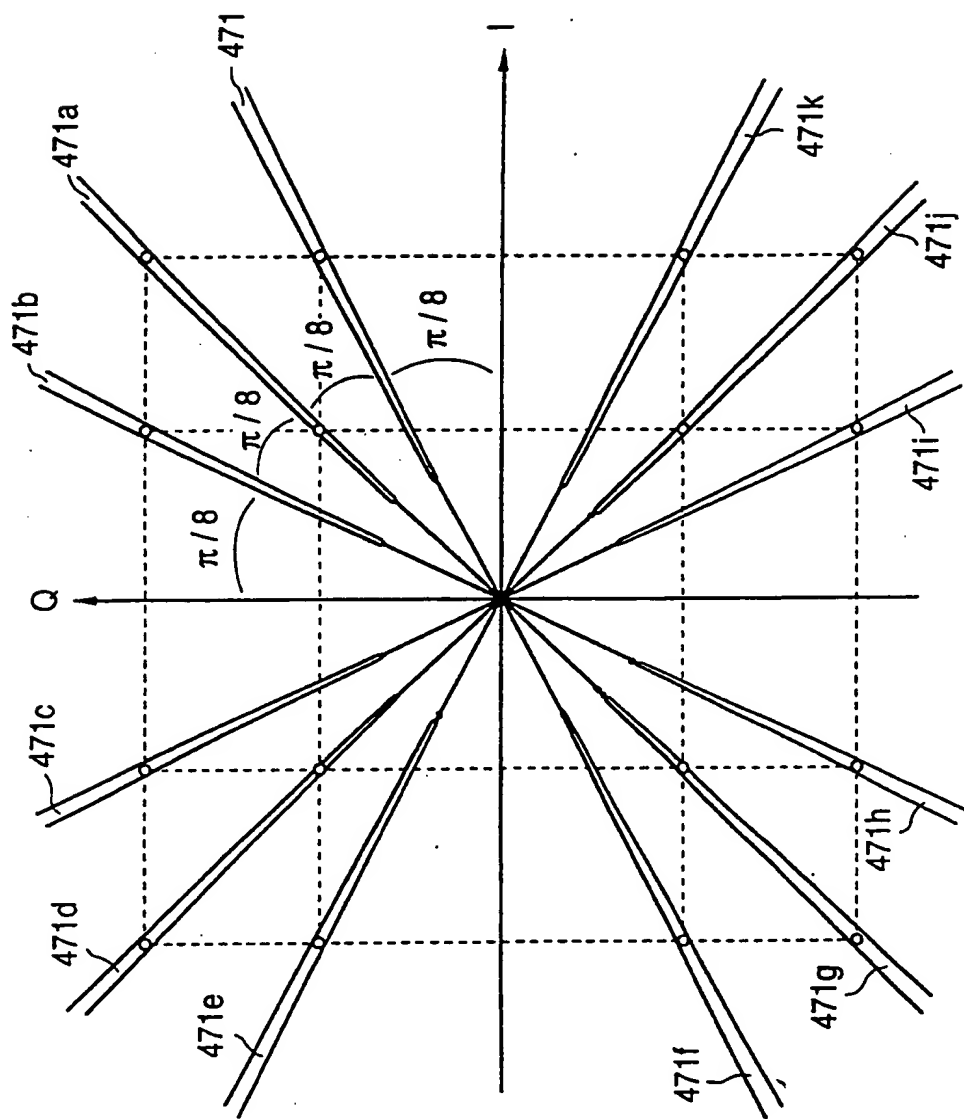


FIG. 47

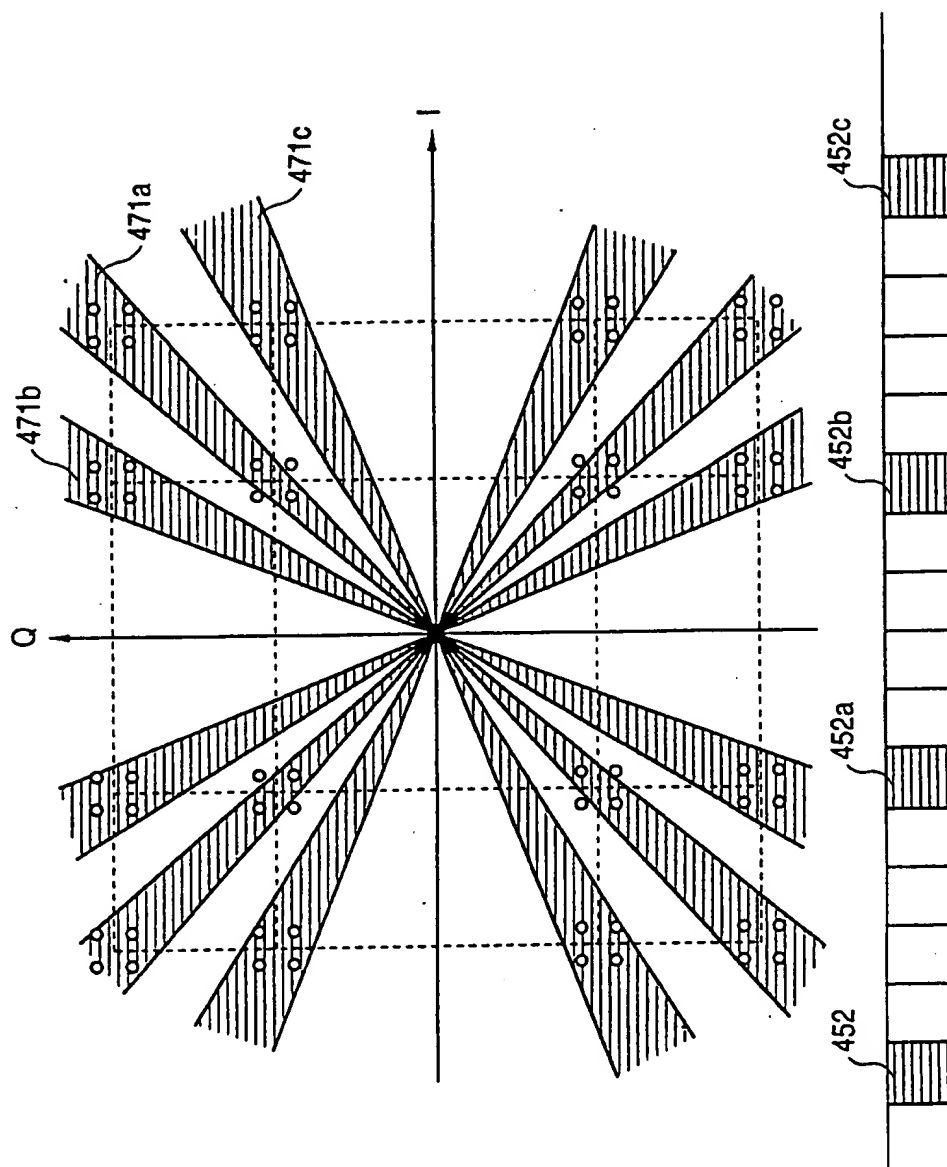


FIG. 48

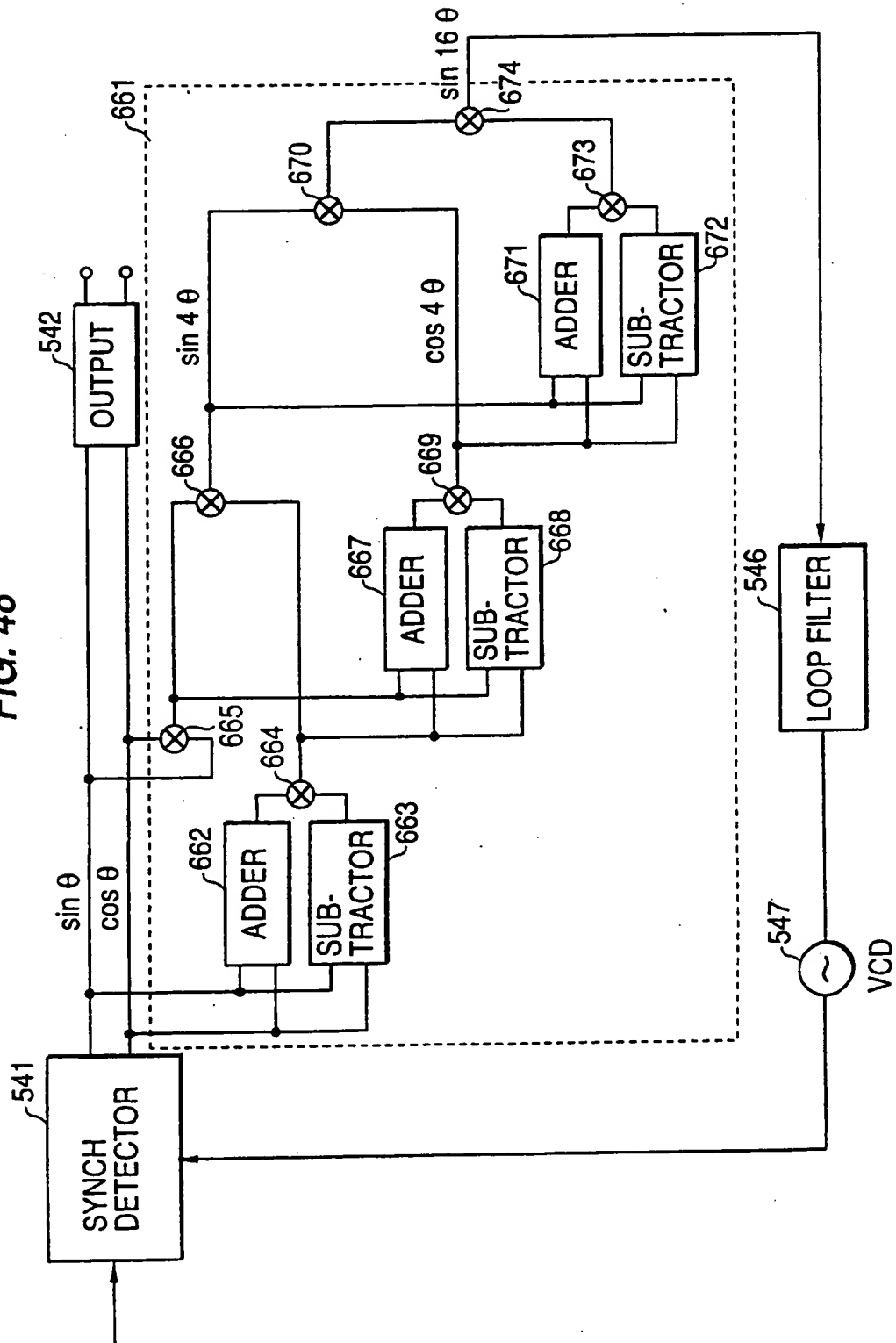


FIG. 49

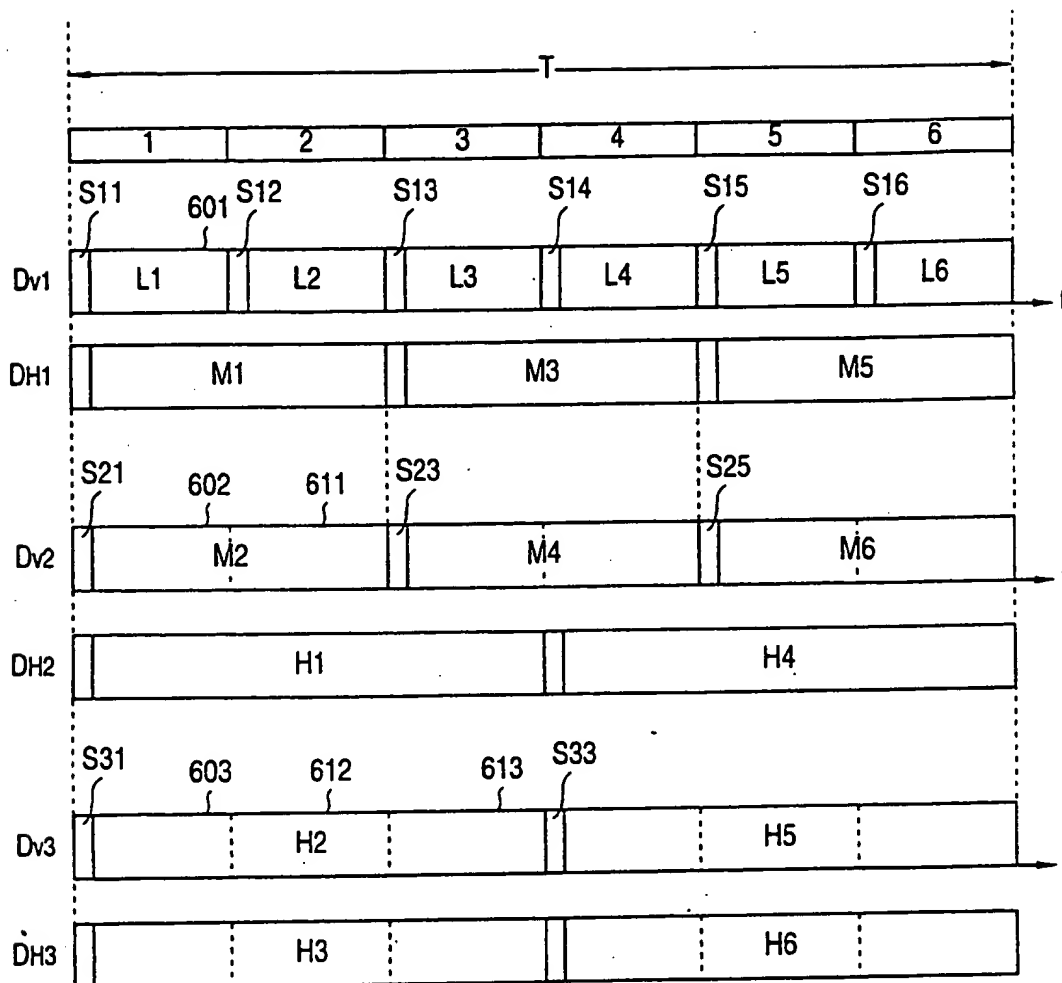


FIG. 50

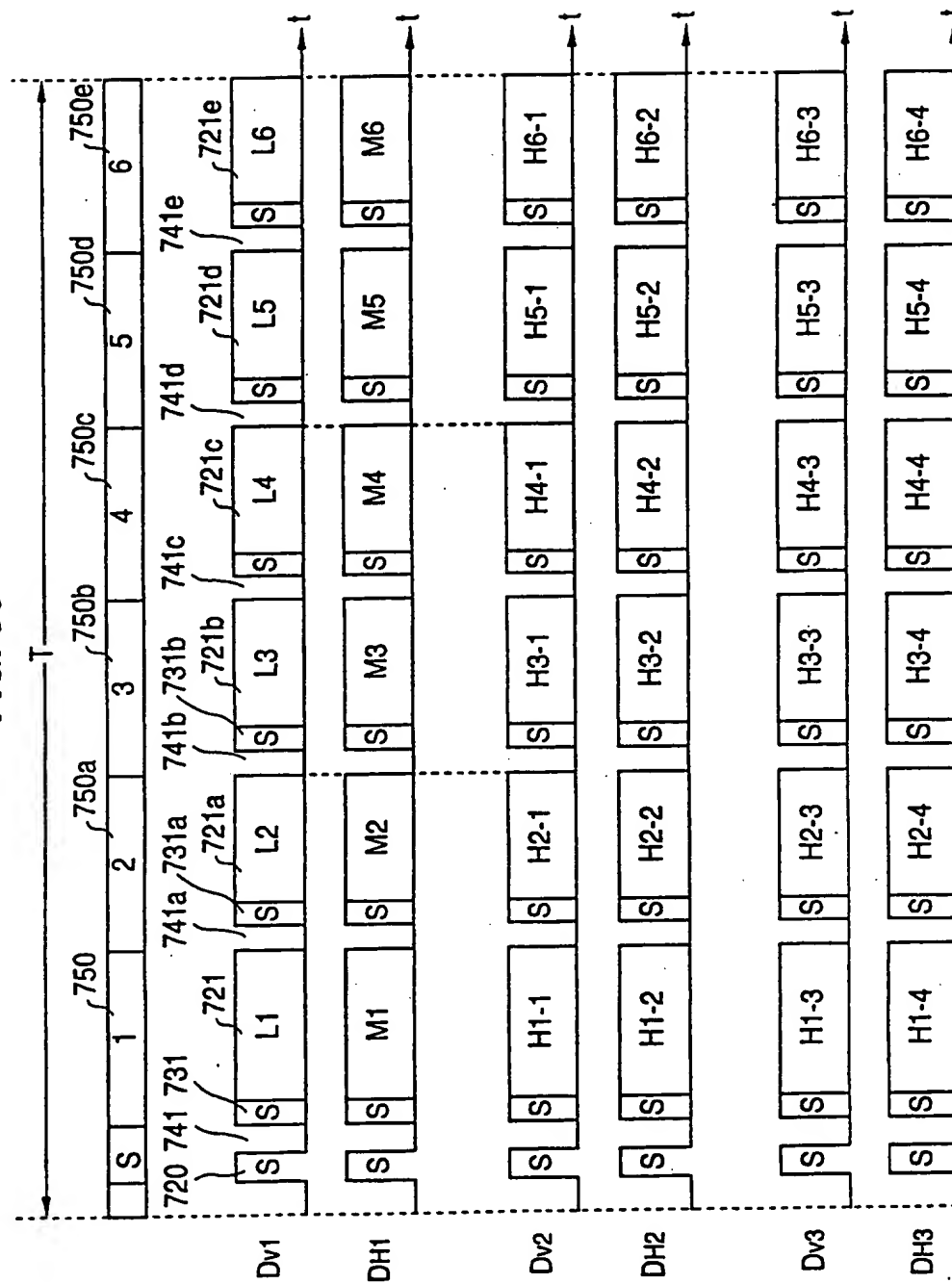


FIG. 51

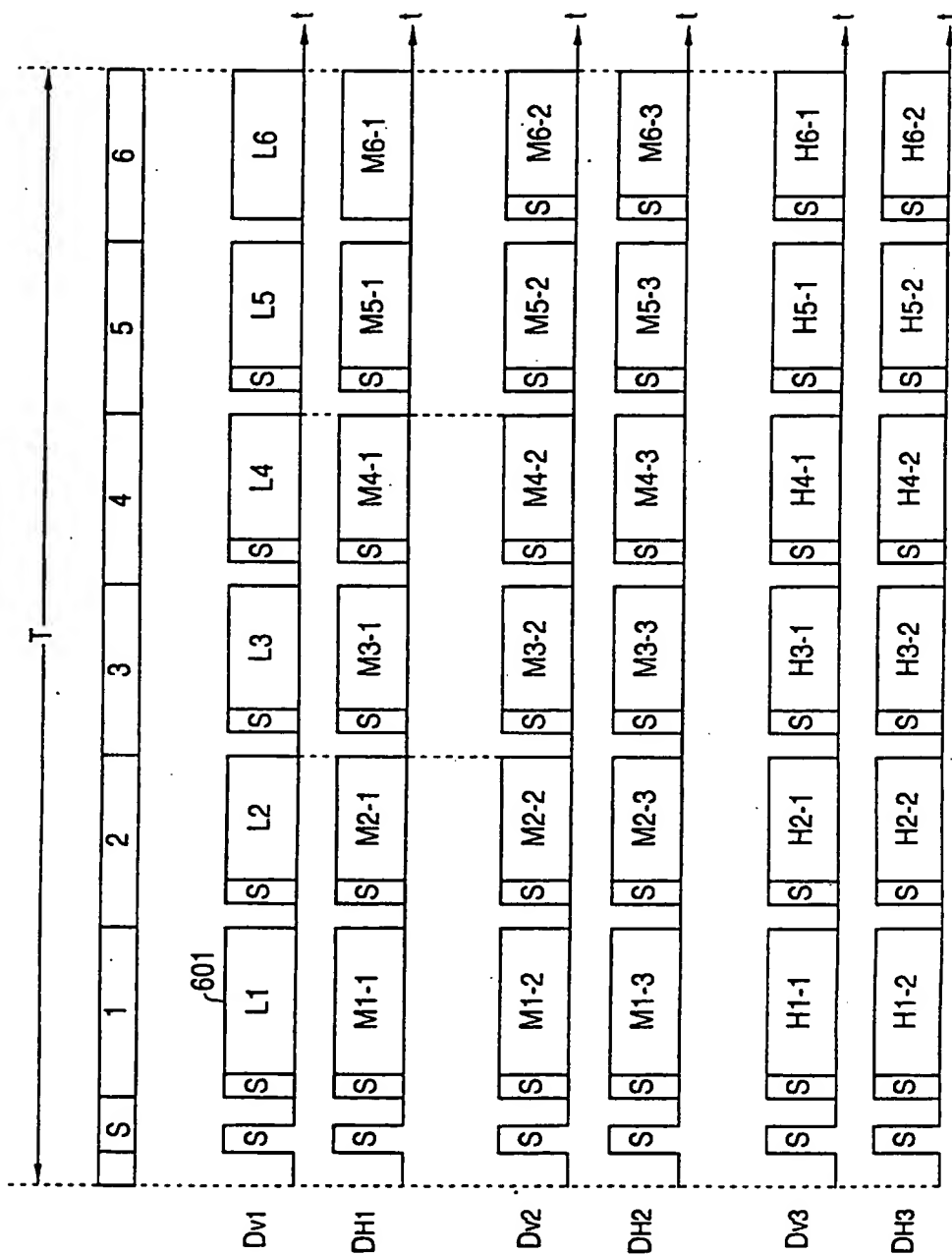


FIG. 52

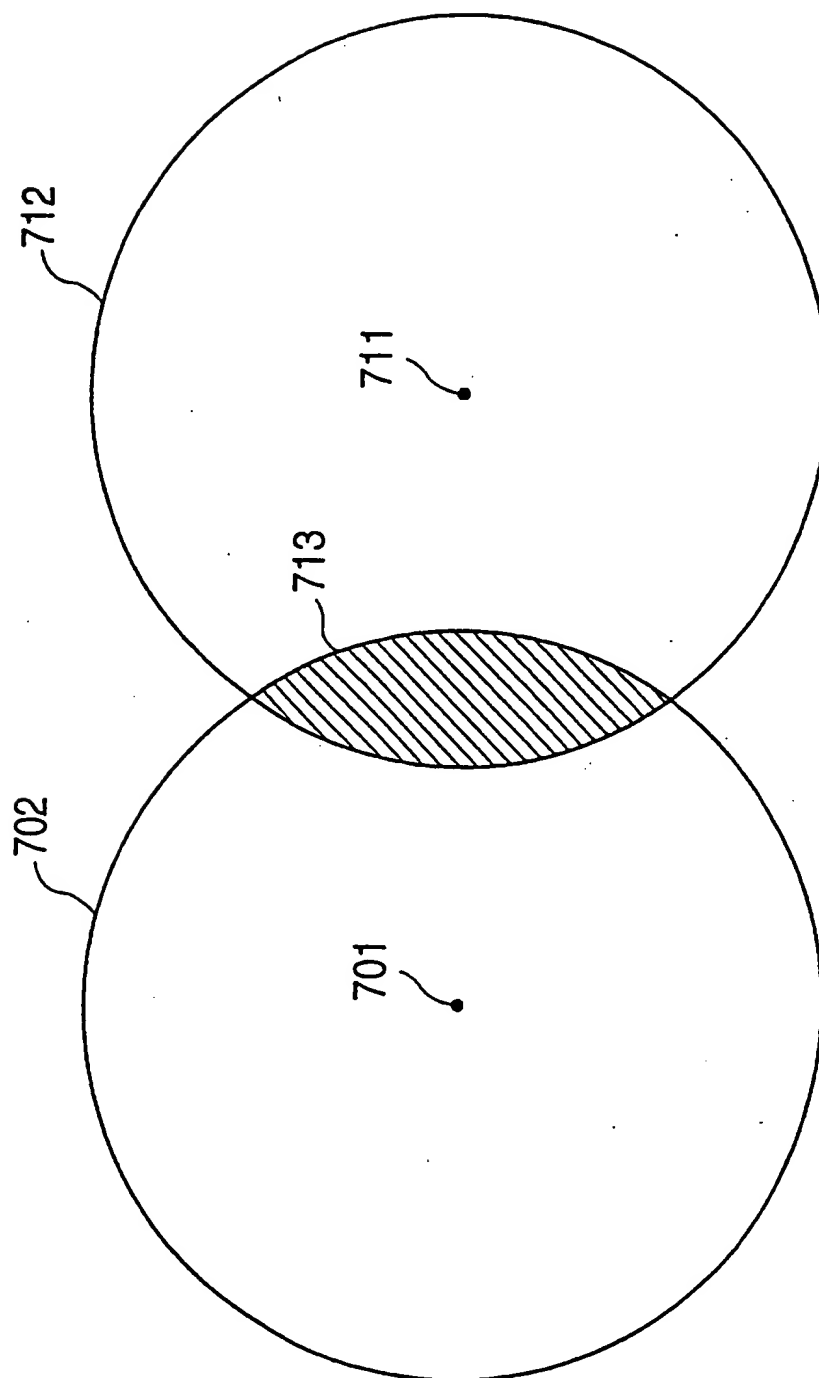


FIG. 53

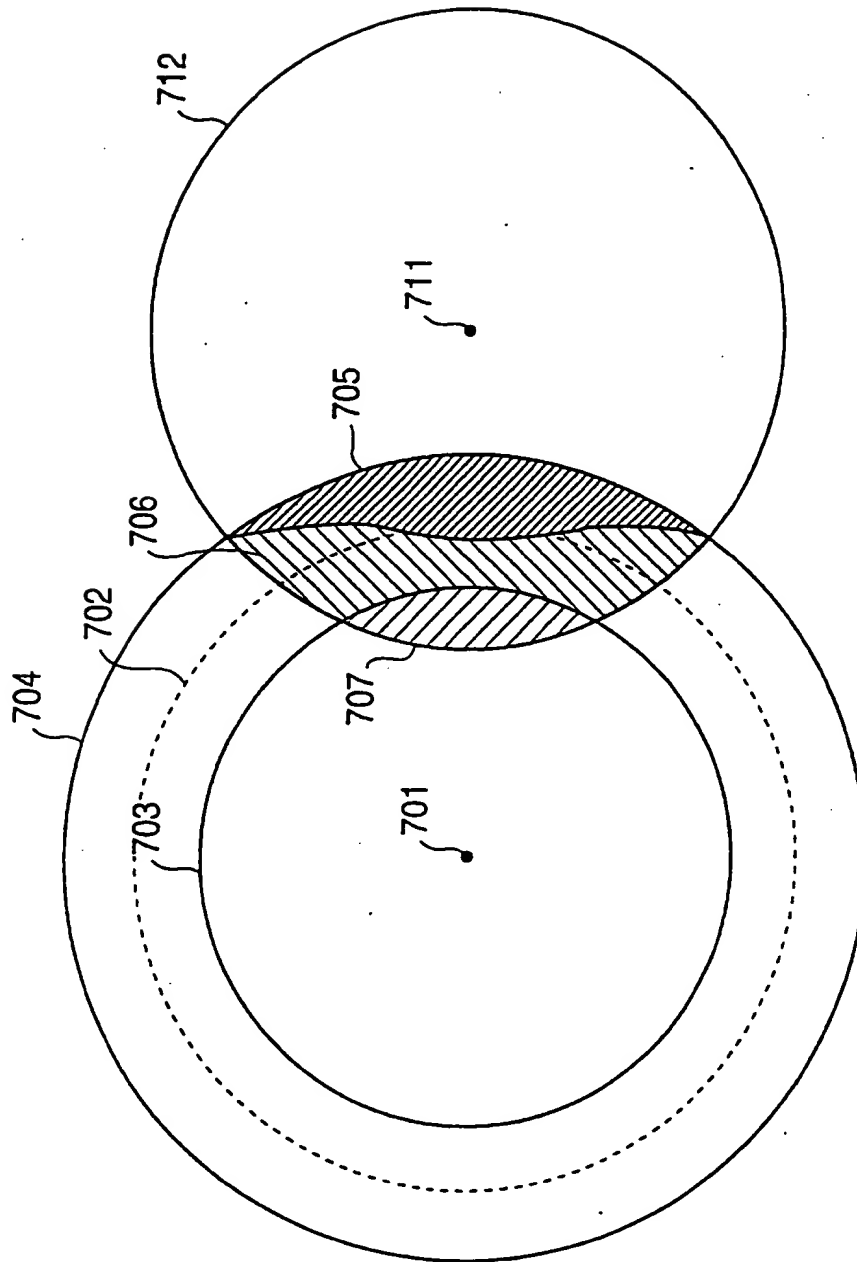


FIG. 54

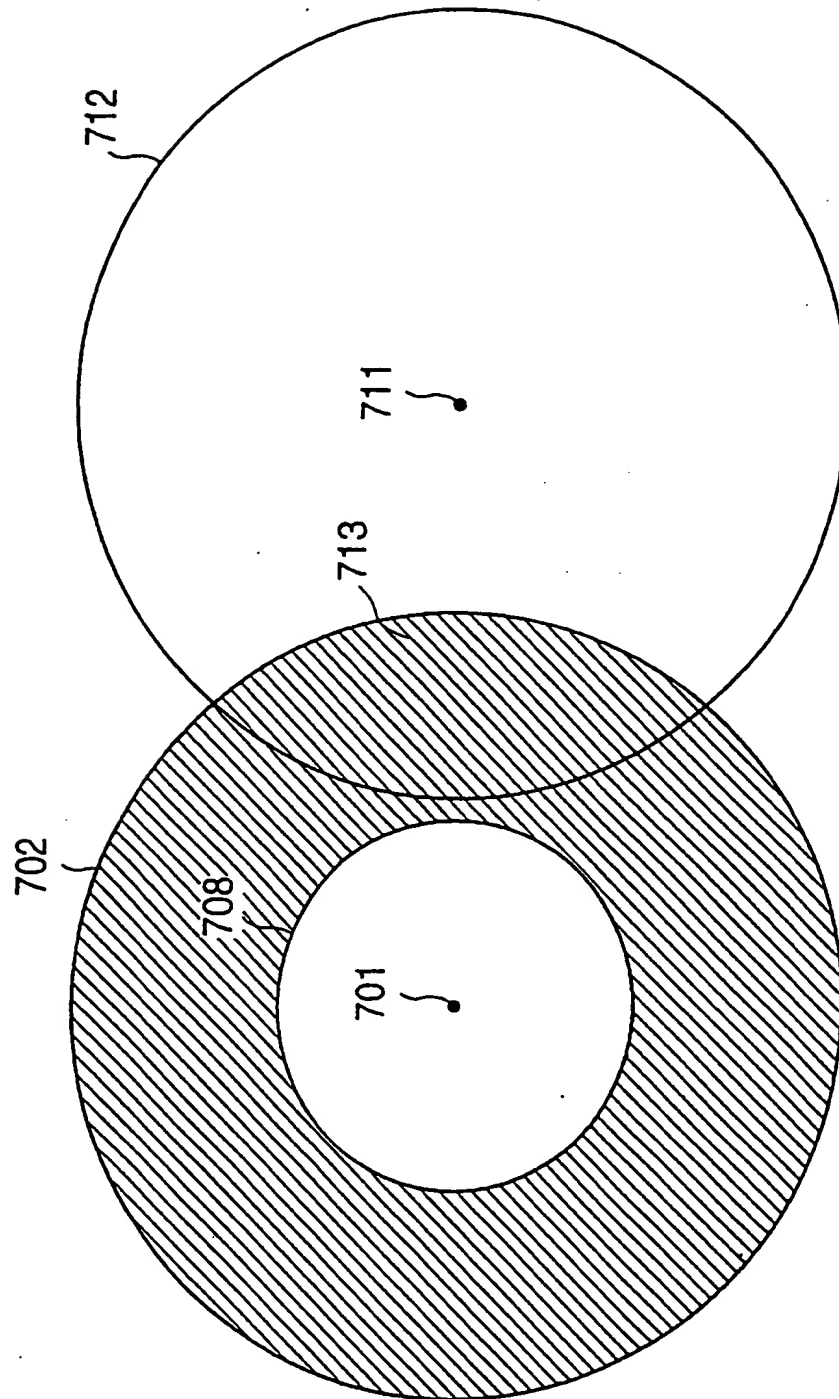


FIG. 55

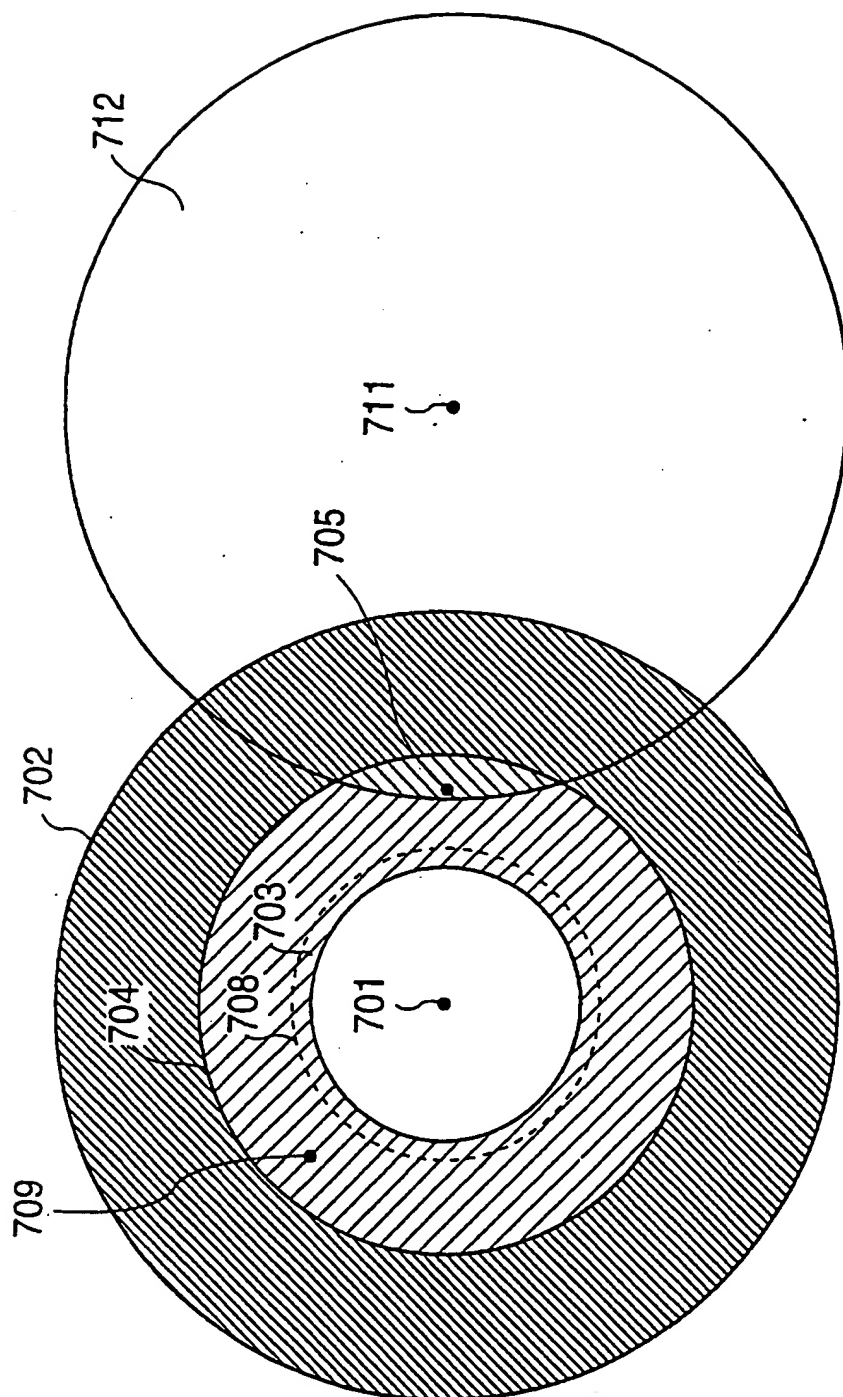


FIG. 56

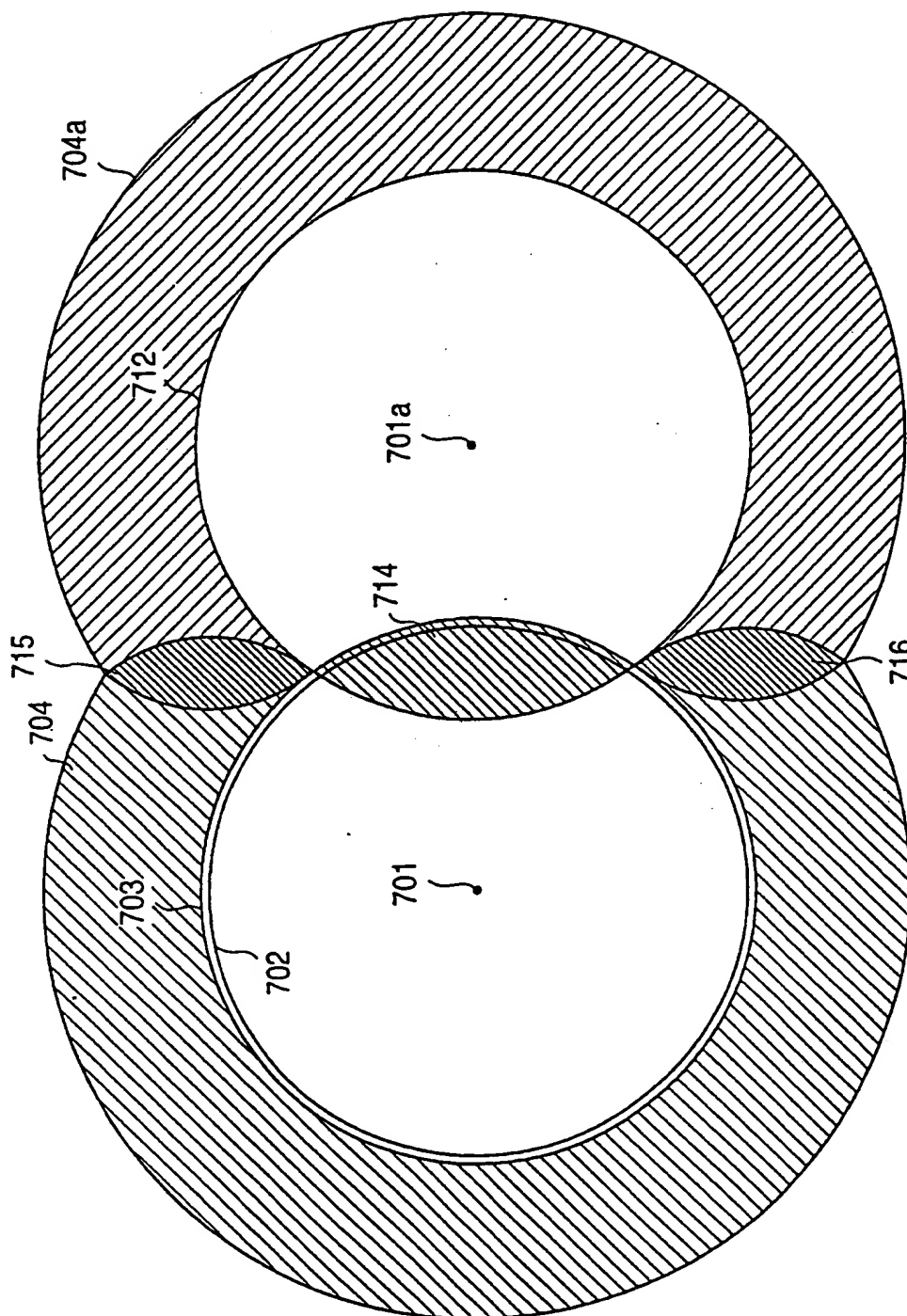


FIG. 57

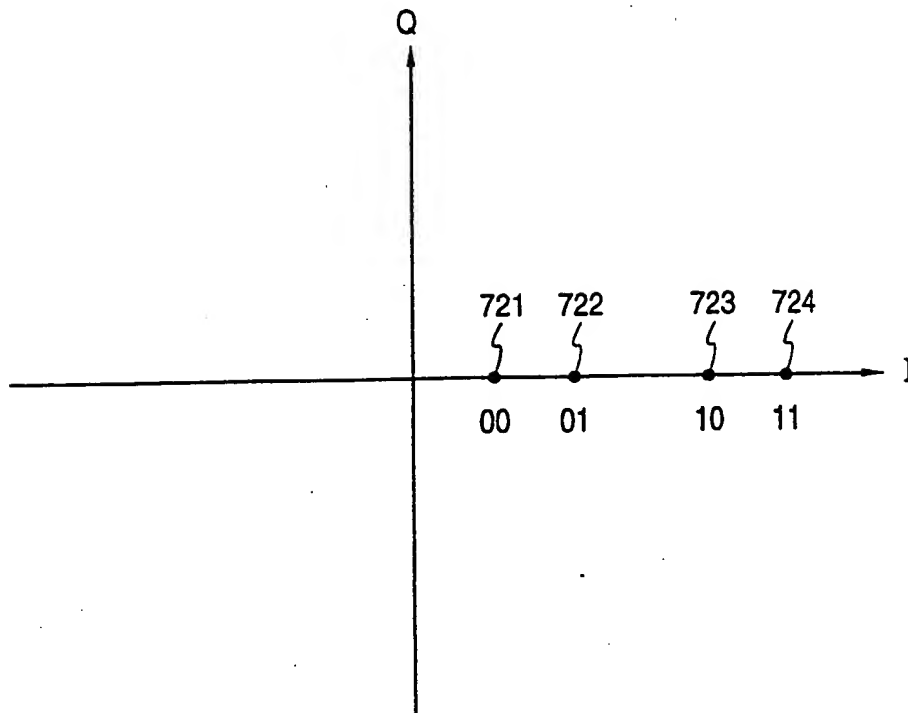


FIG. 58

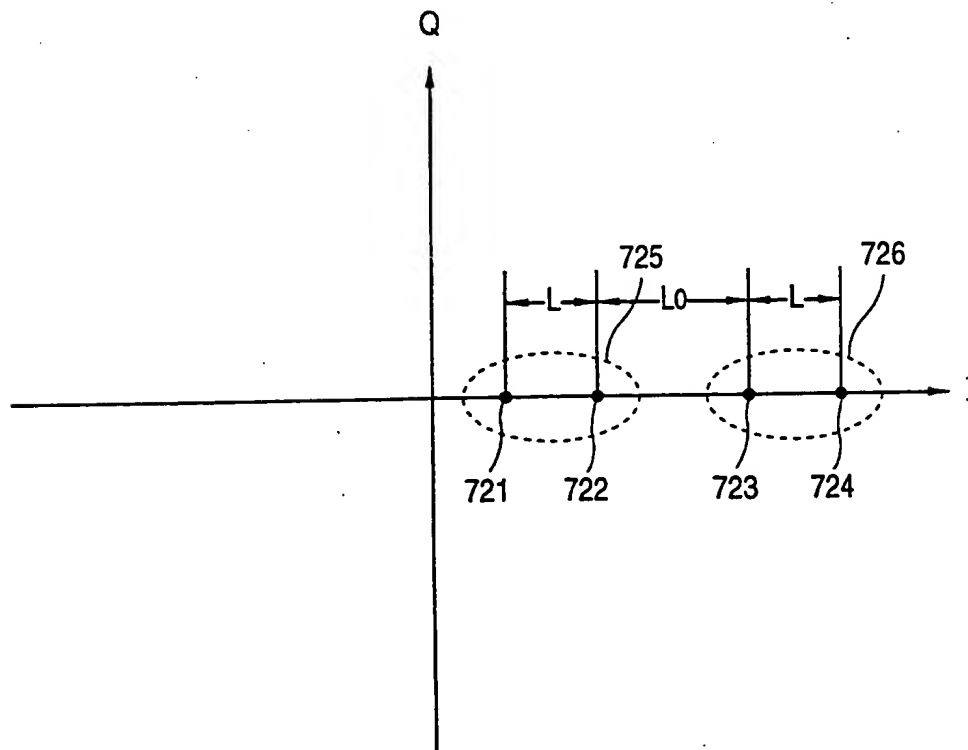


FIG. 59(a)

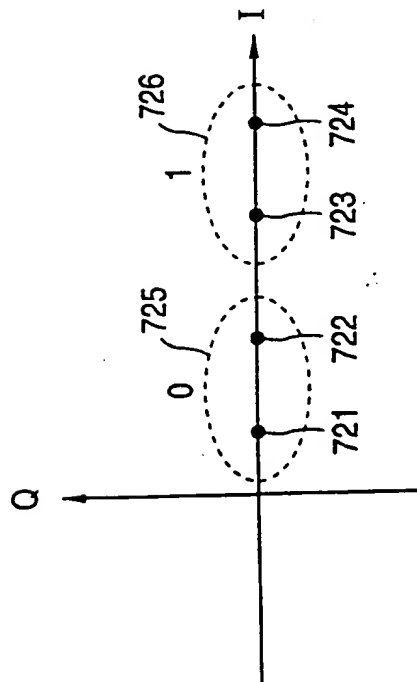


FIG. 59(c)

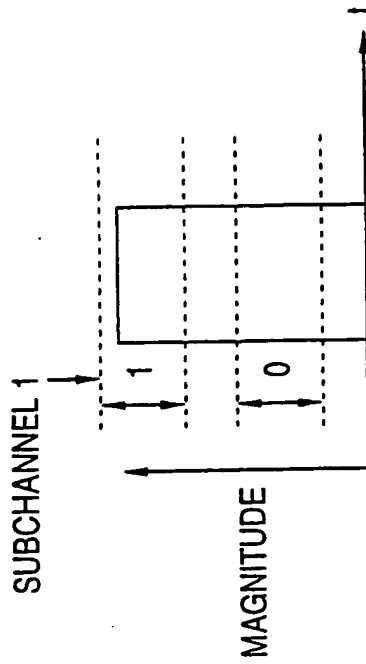


FIG. 59(b)

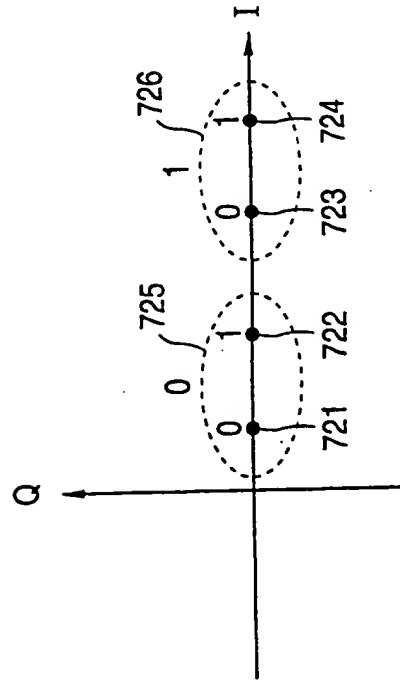


FIG. 59(d)

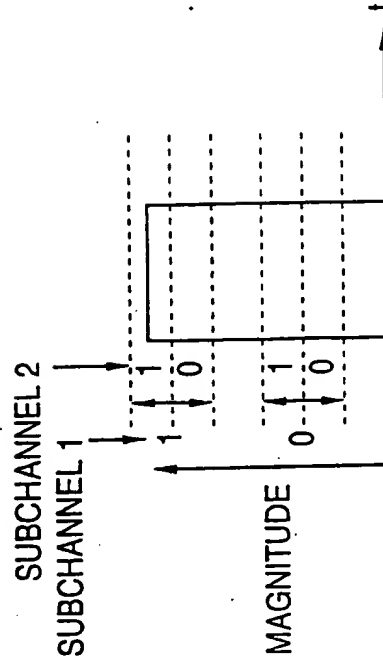


FIG. 60

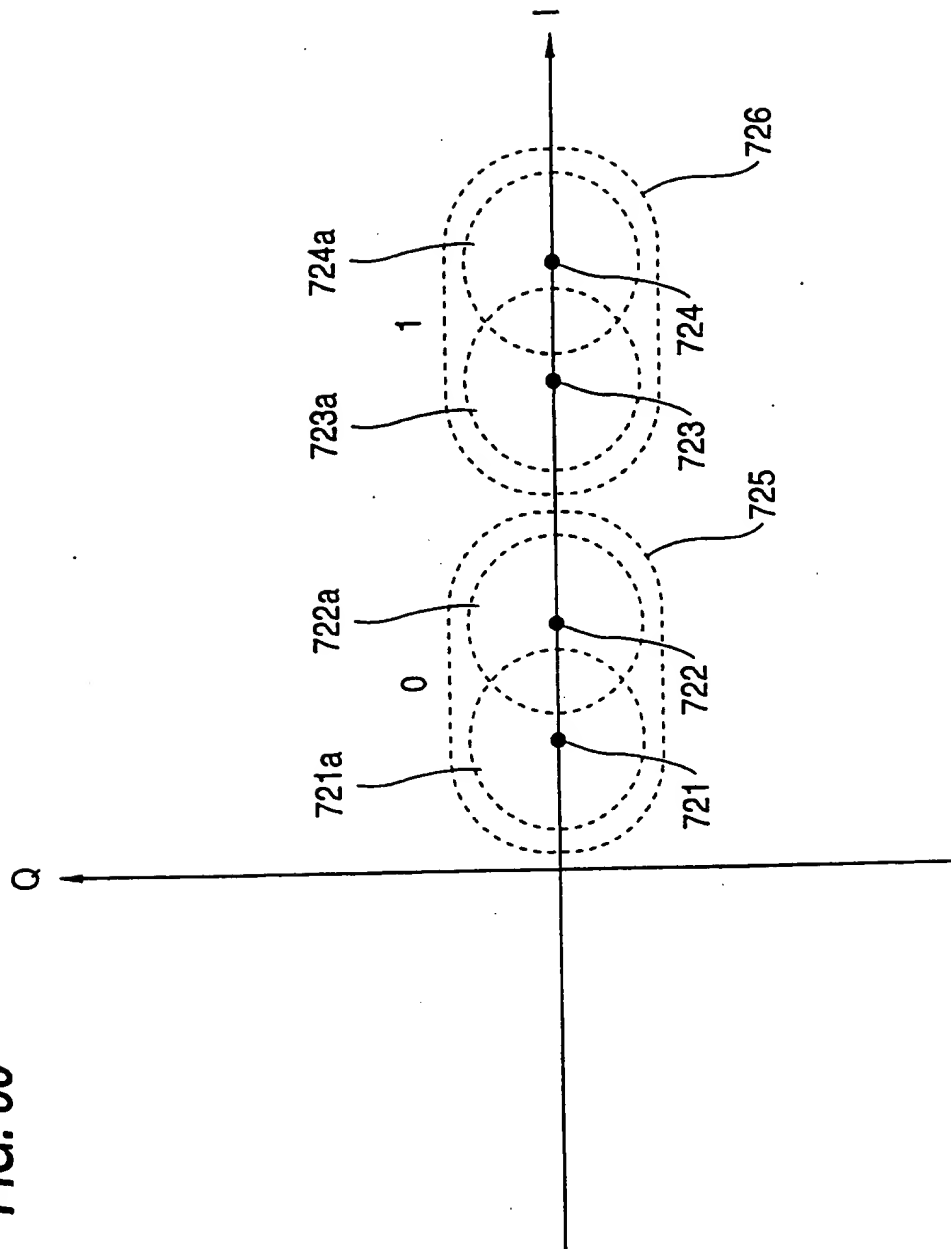


FIG. 61

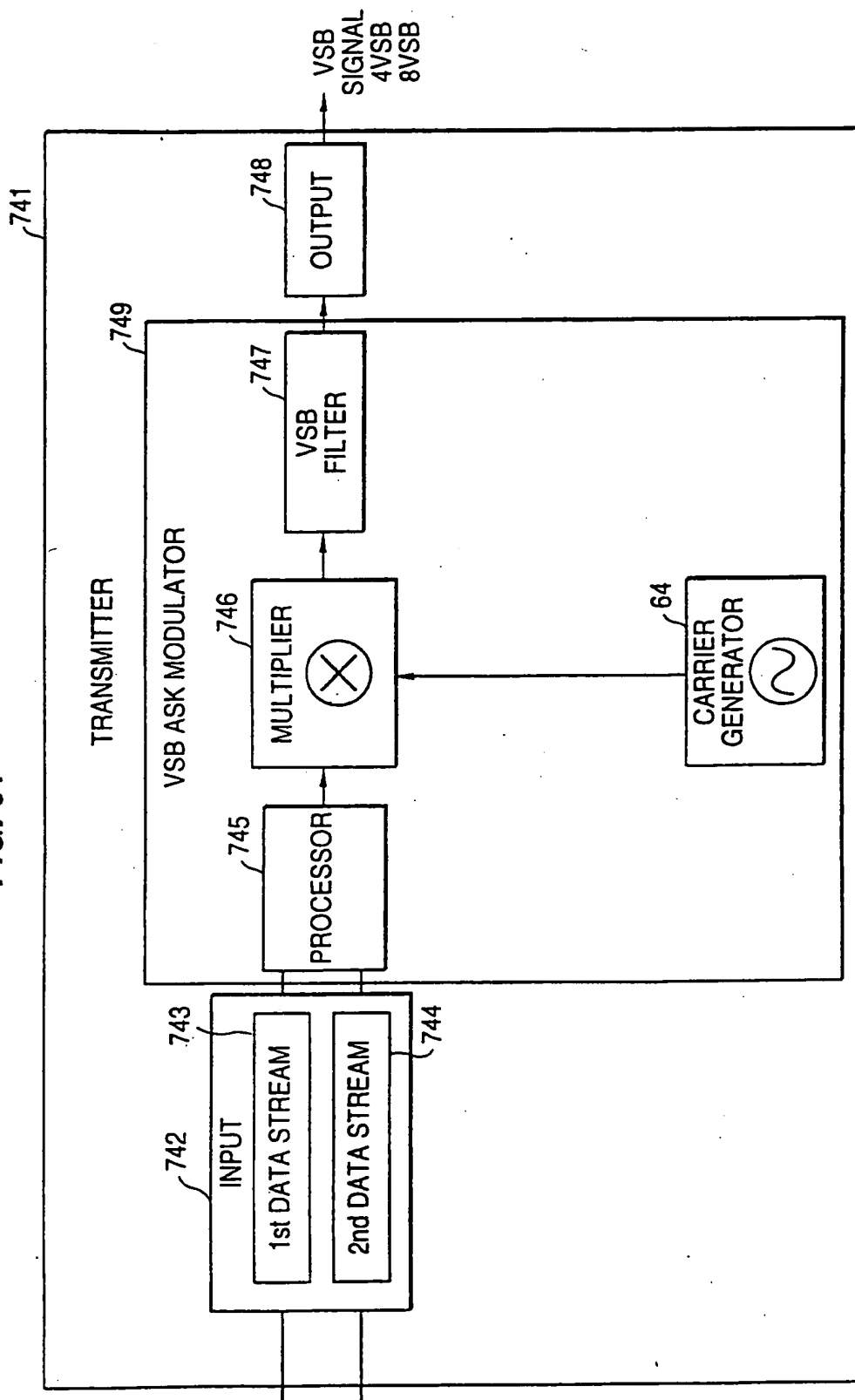


FIG. 62(a)

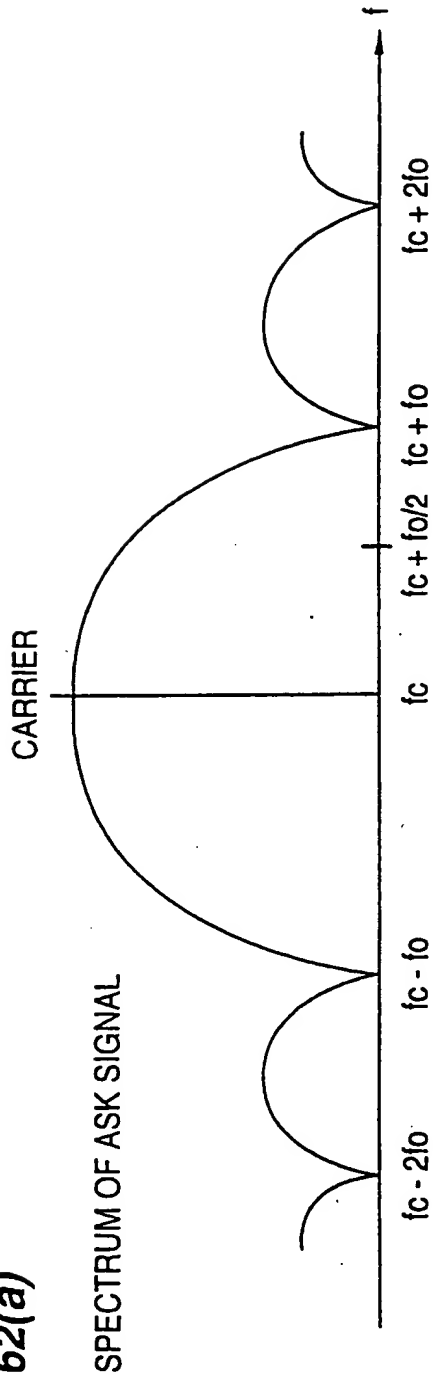


FIG. 62(b)

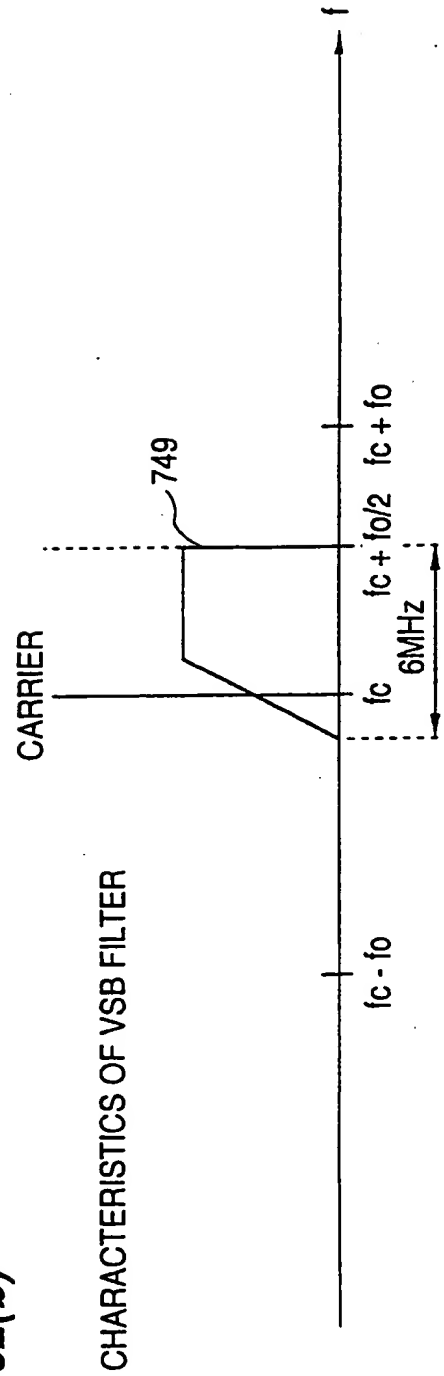


FIG. 63

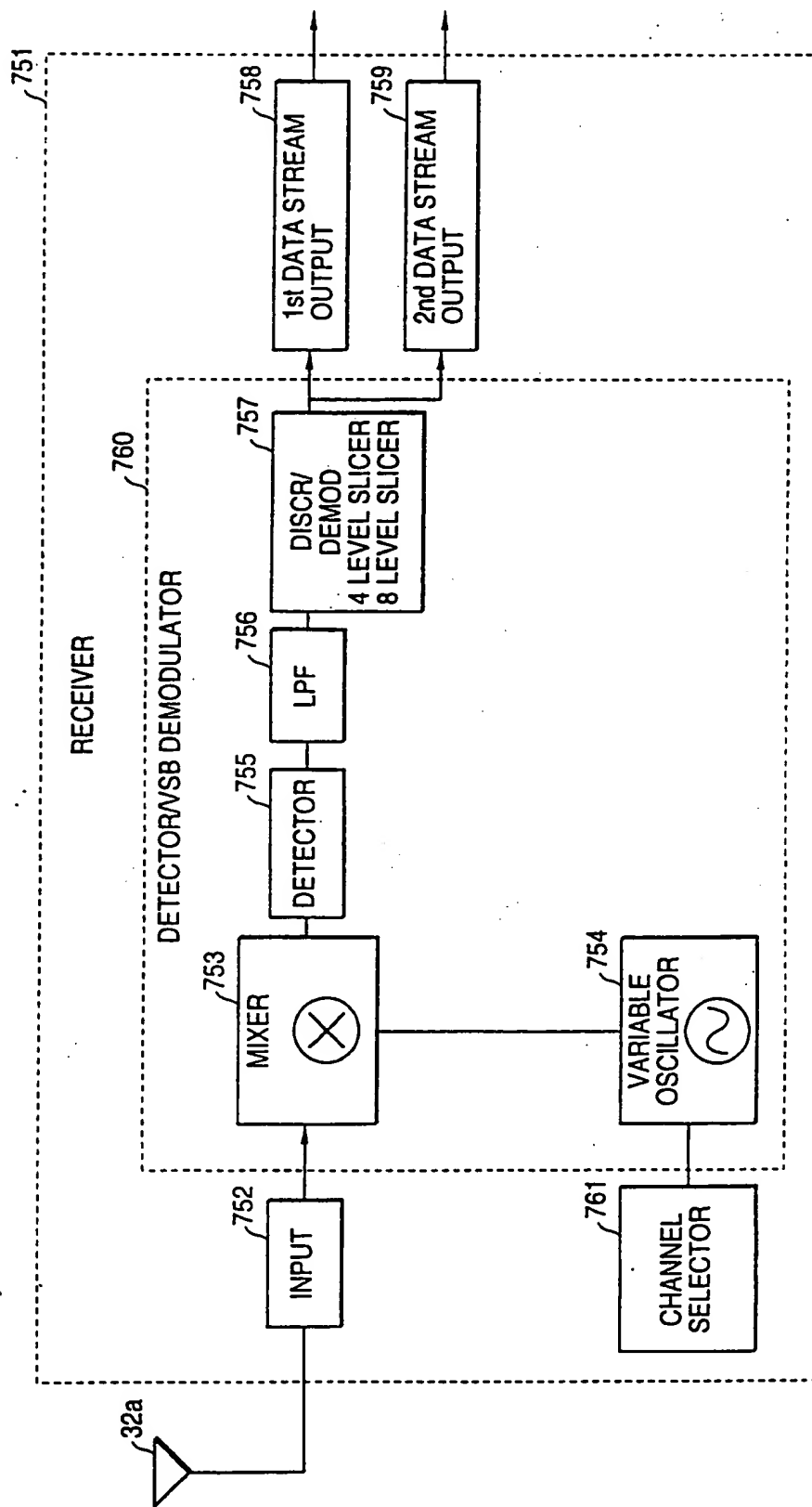


FIG. 64

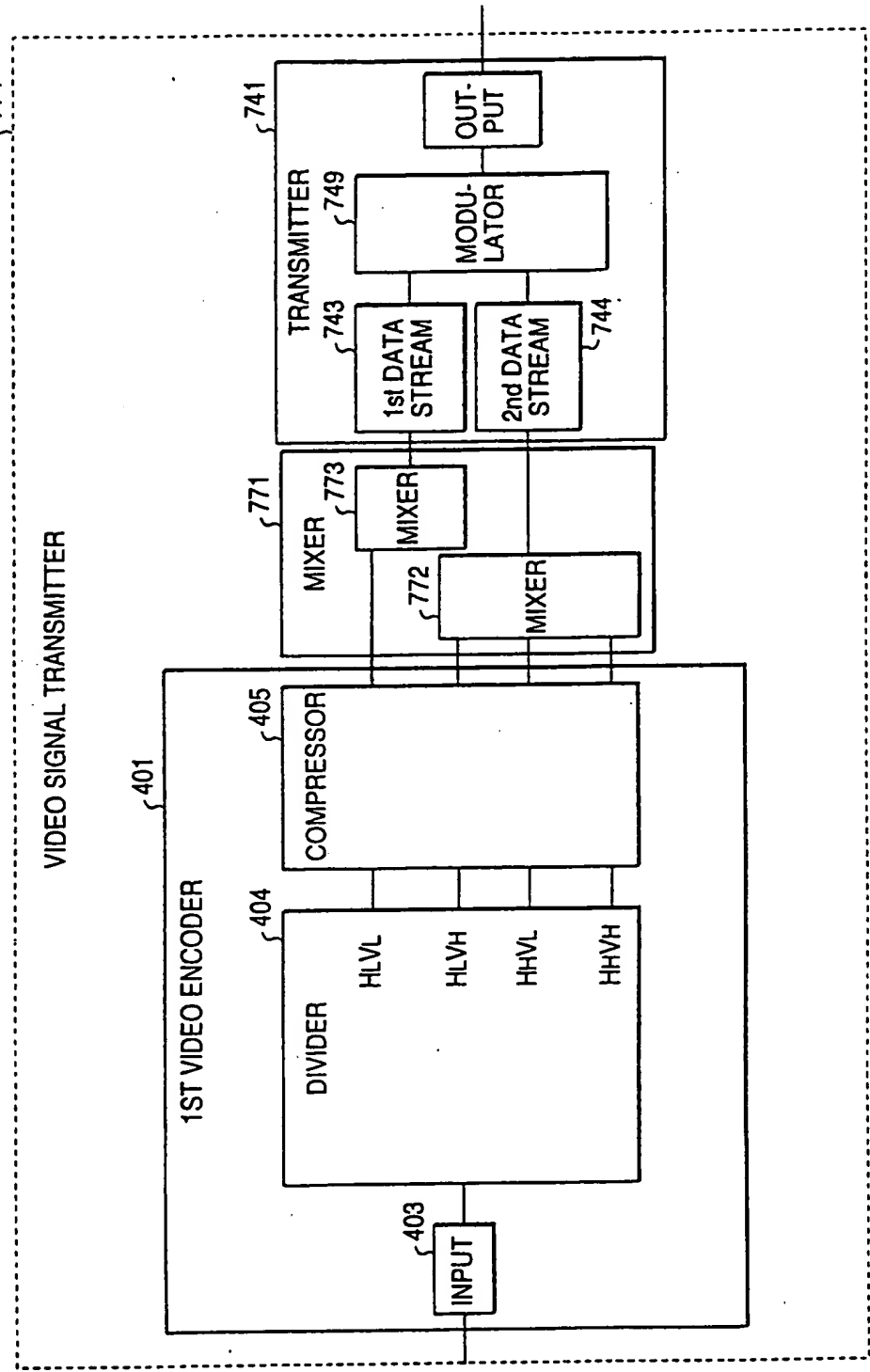


FIG. 65

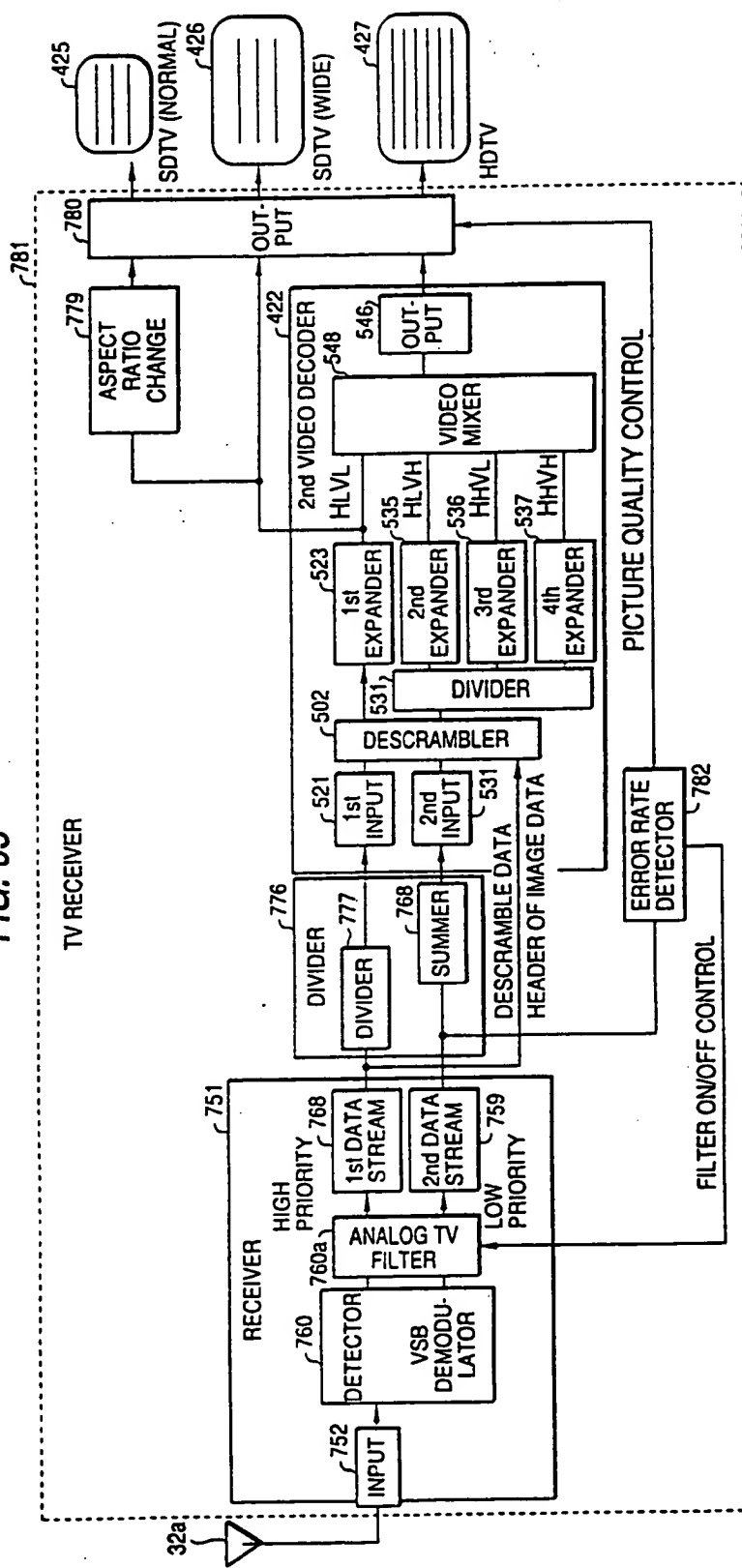


FIG. 66

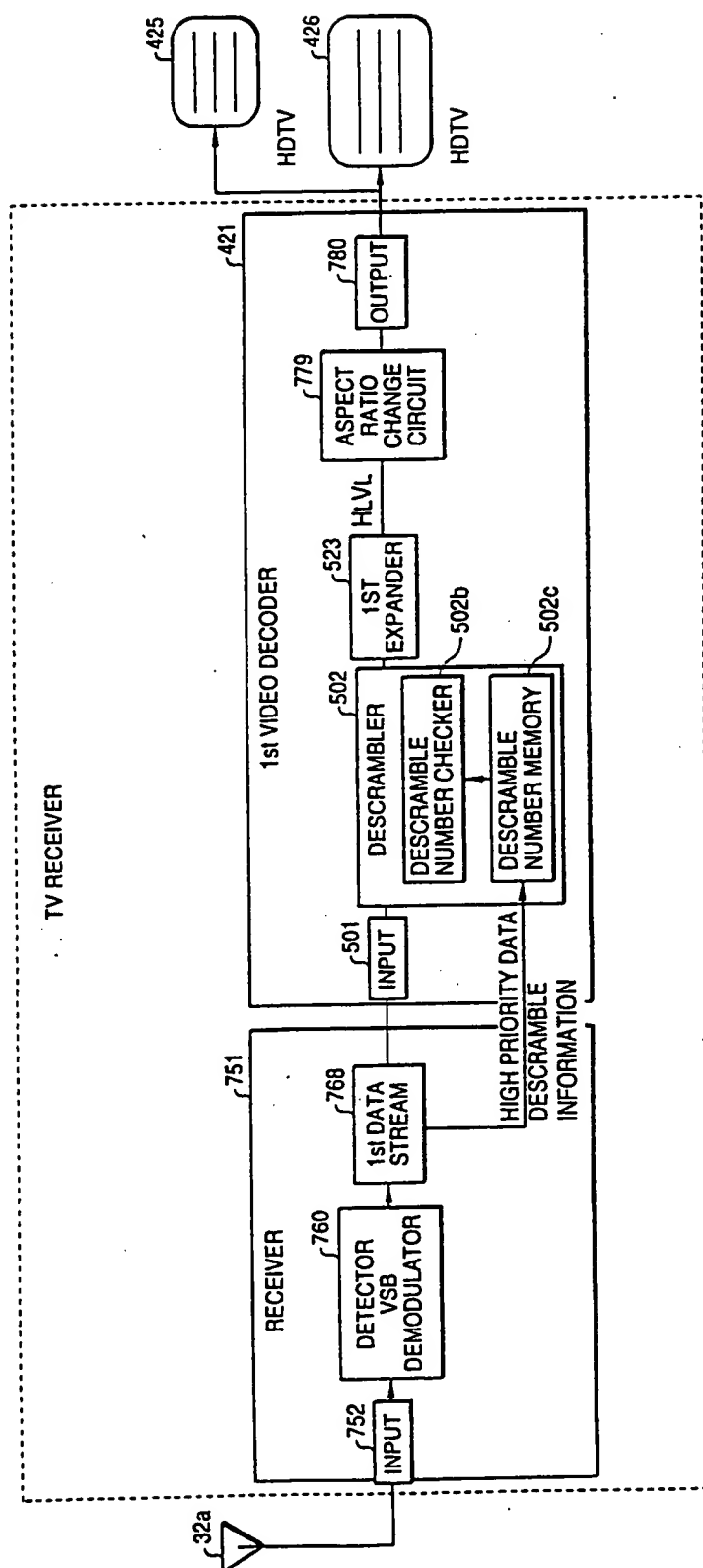
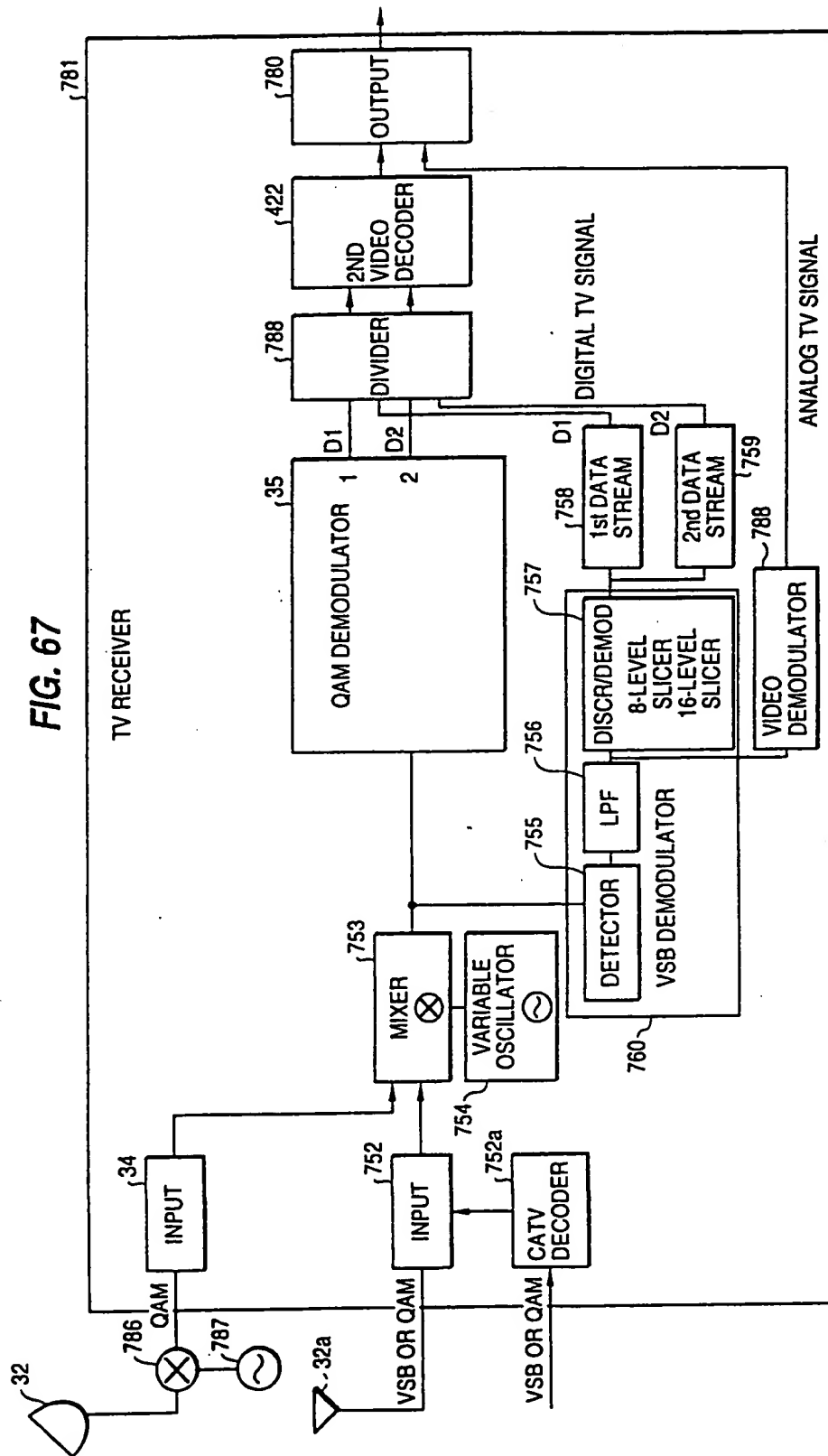


FIG. 67



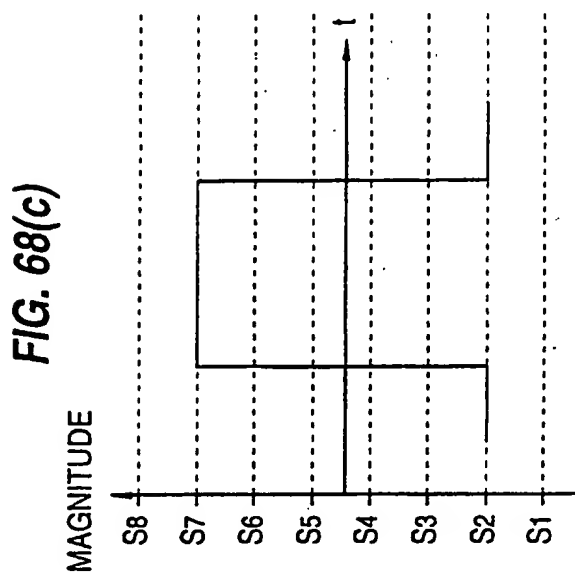
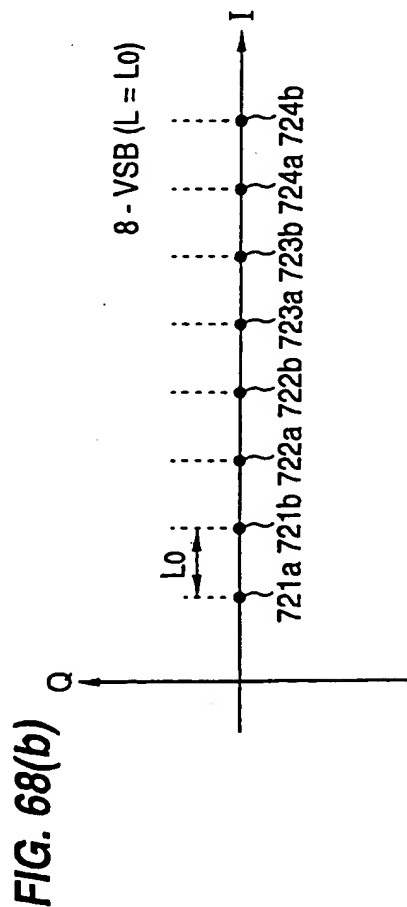
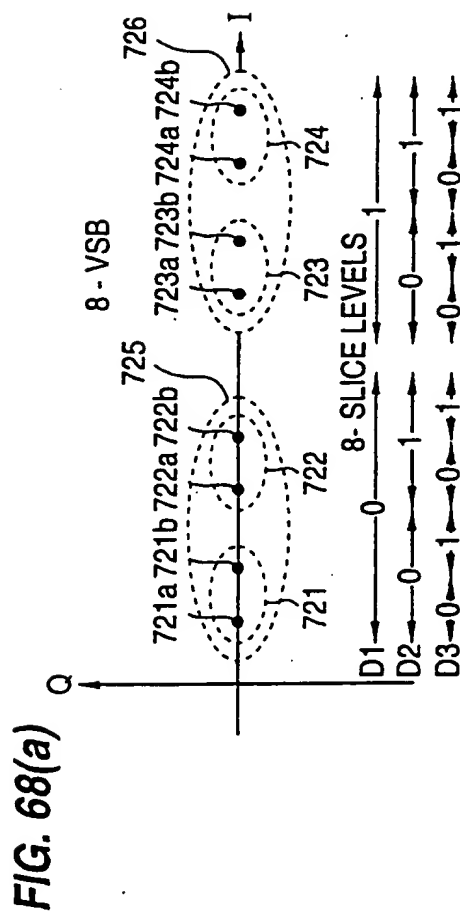


FIG. 69

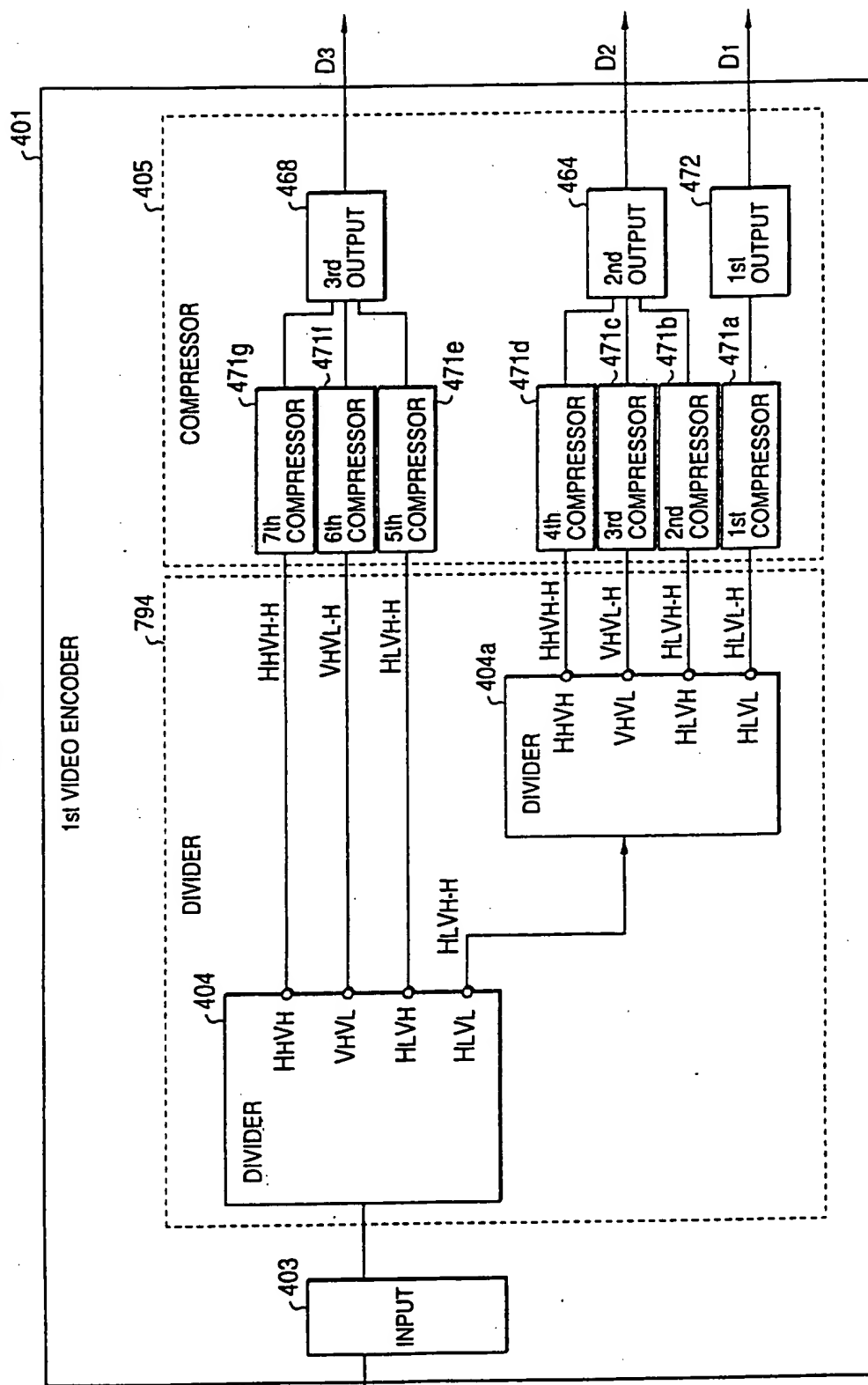


FIG. 70

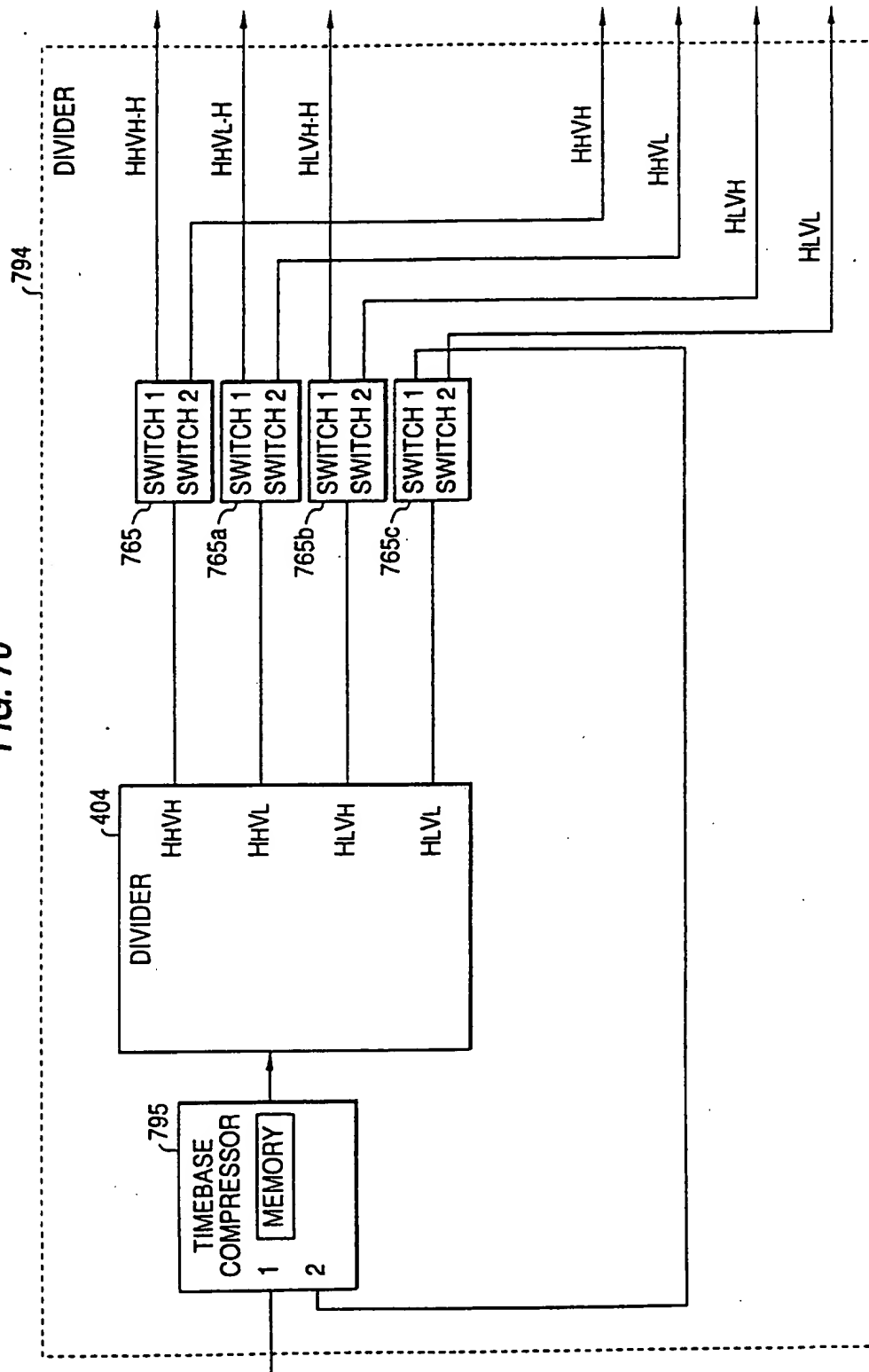


FIG. 71

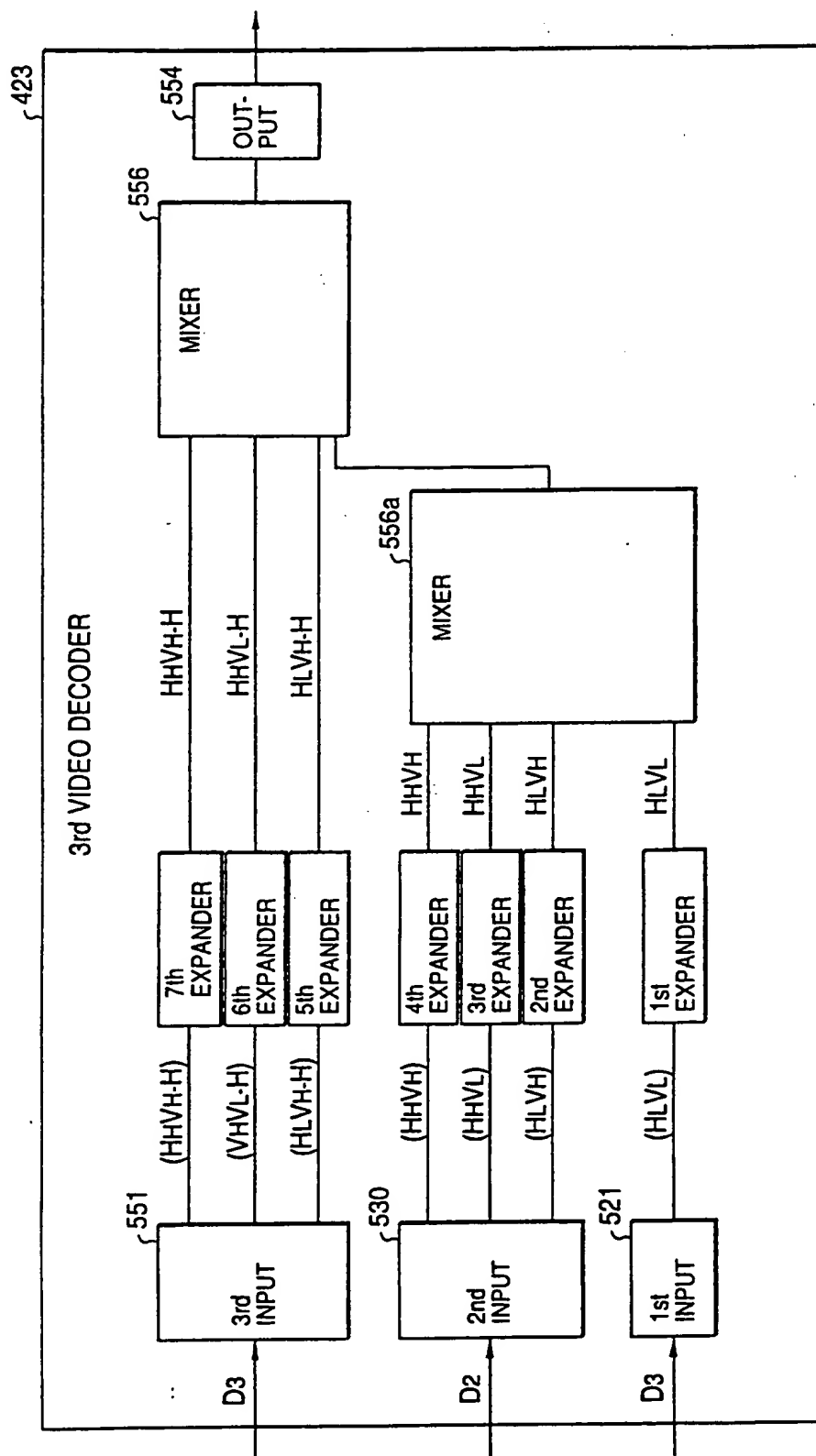


FIG. 72

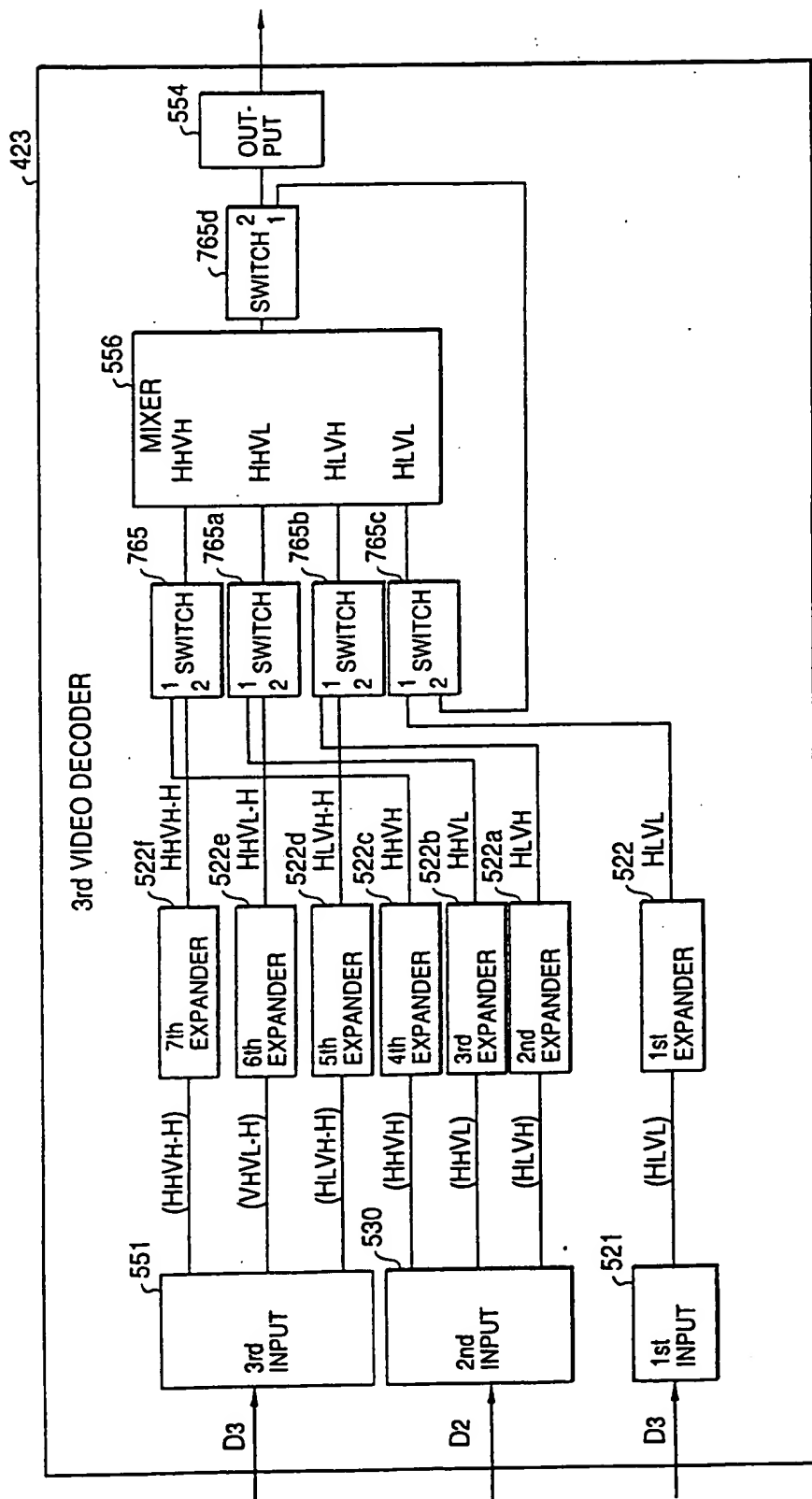


FIG. 73

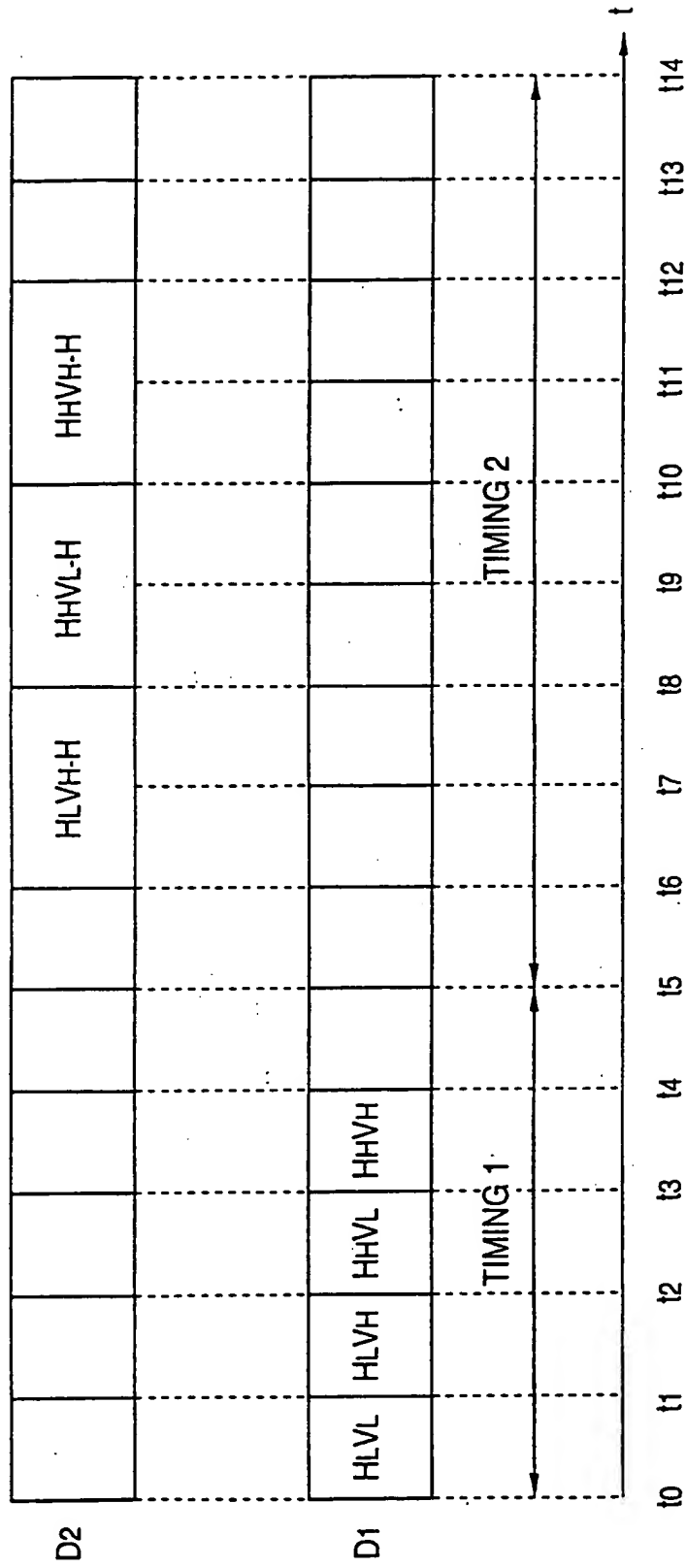


FIG. 74(a)

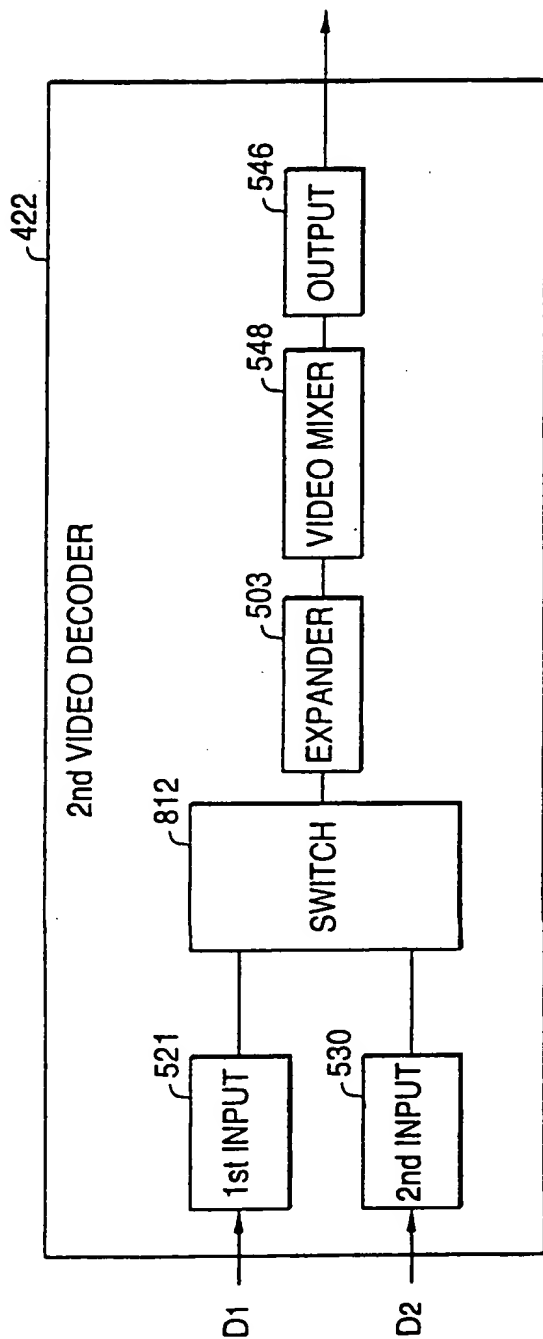
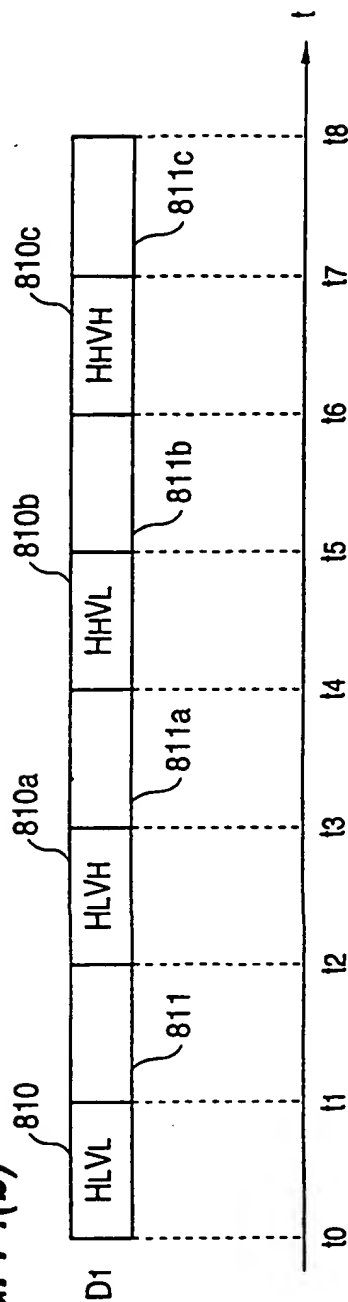


FIG. 74(b)



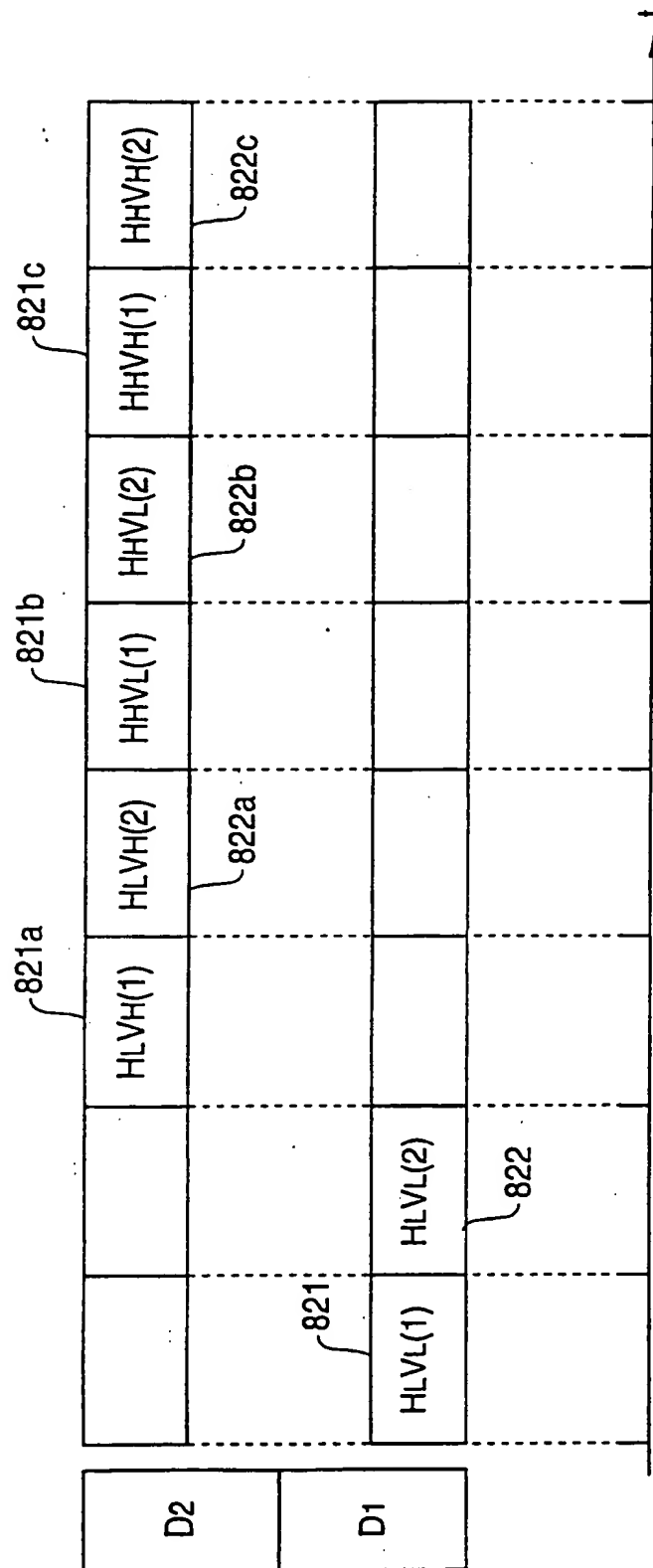


FIG. 76

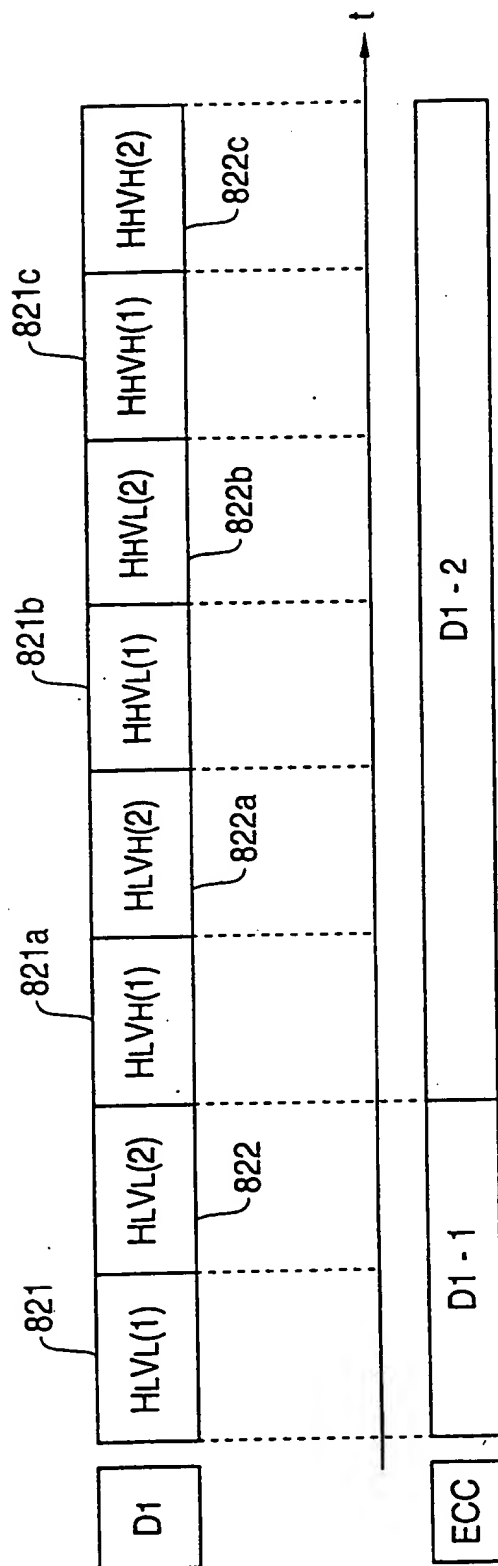


FIG. 77

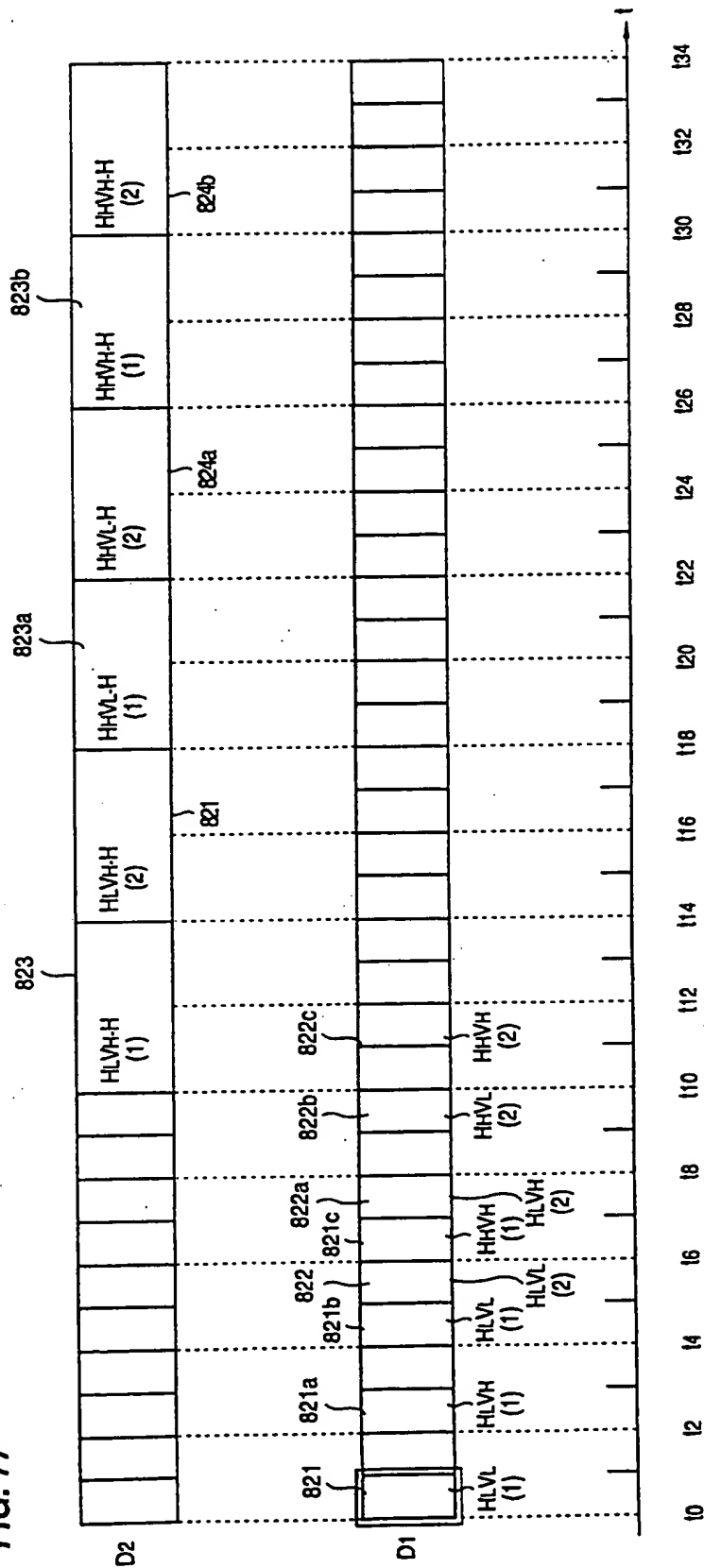


FIG. 78

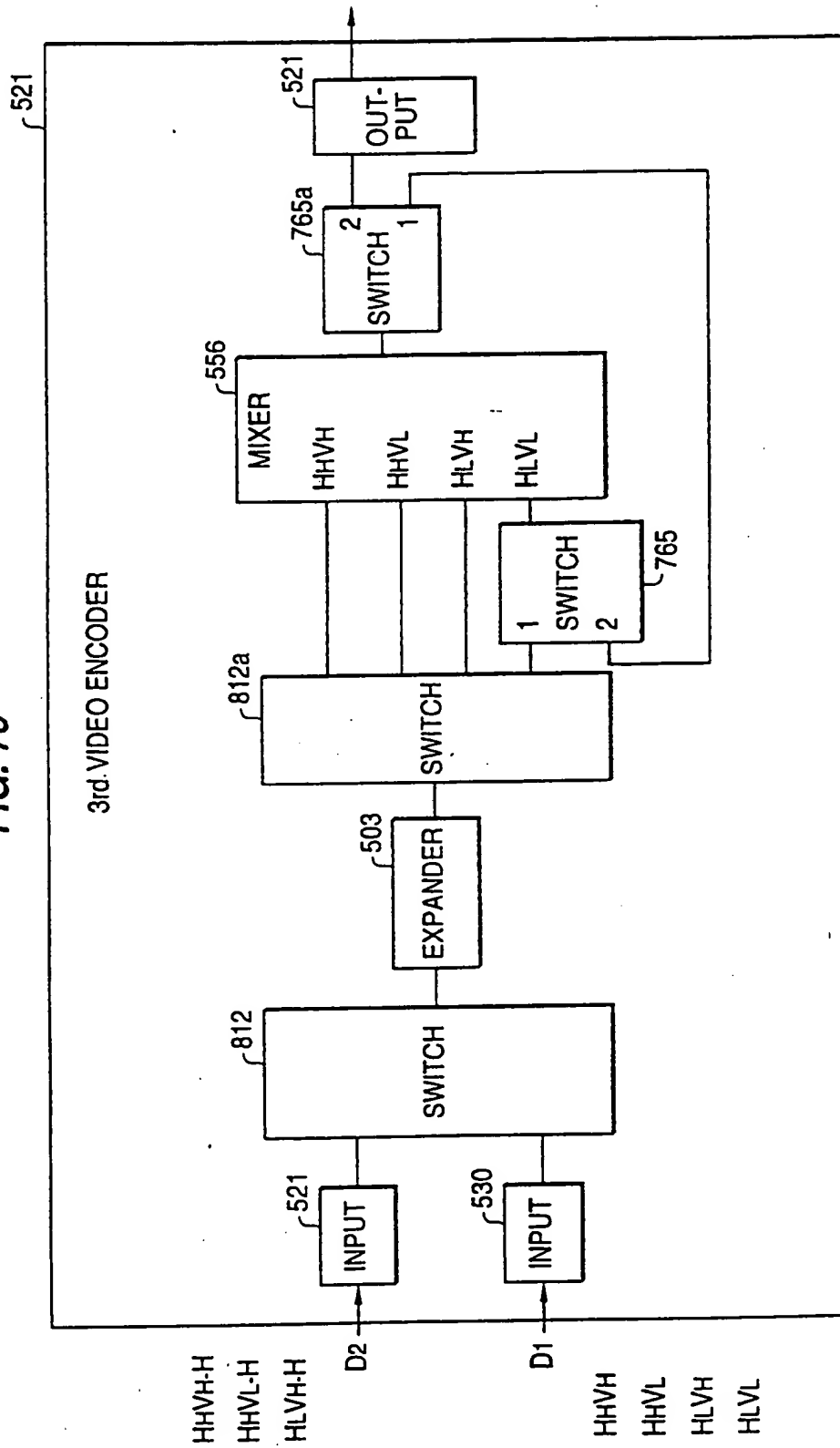


FIG. 79

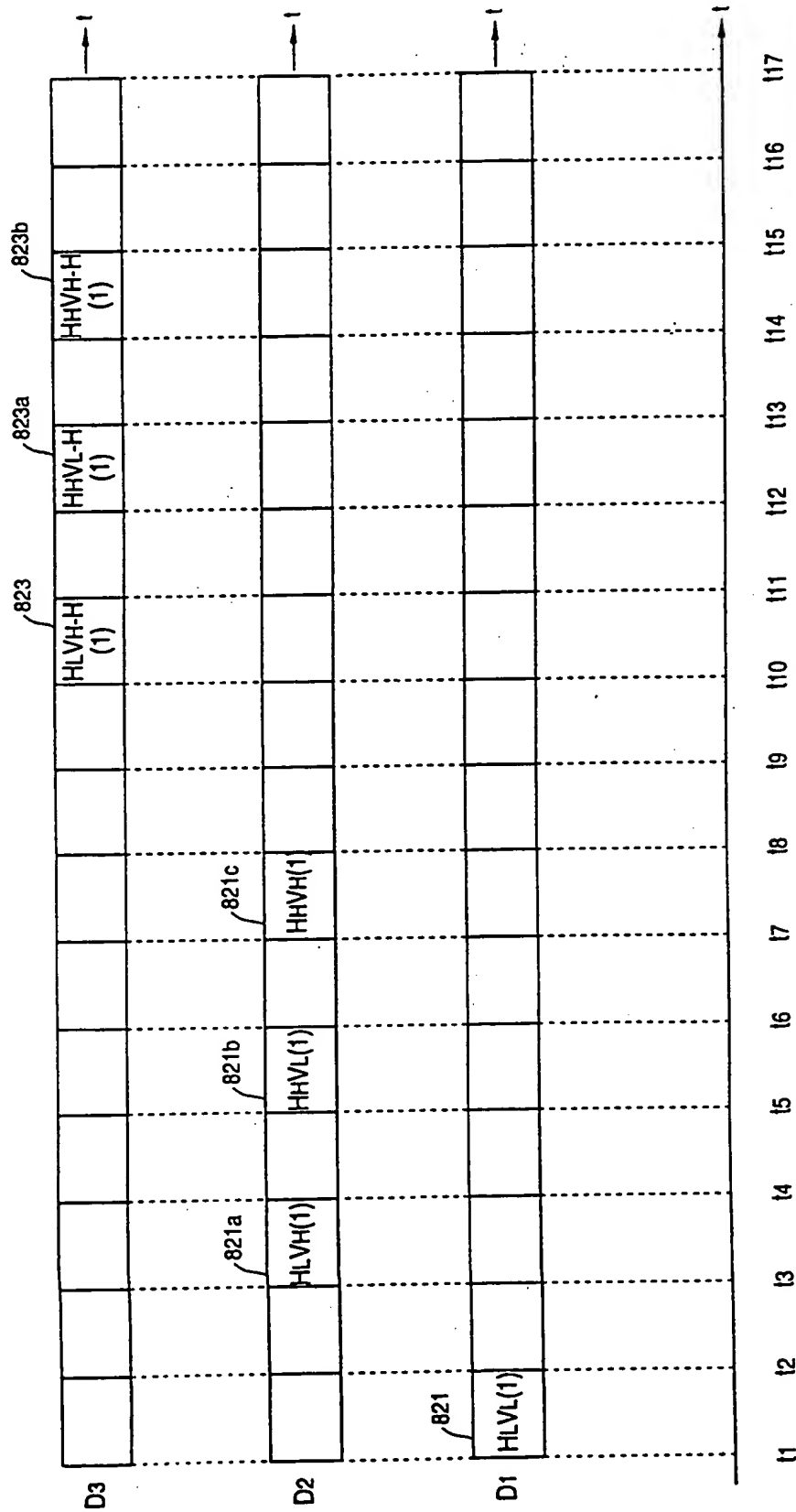


FIG. 80

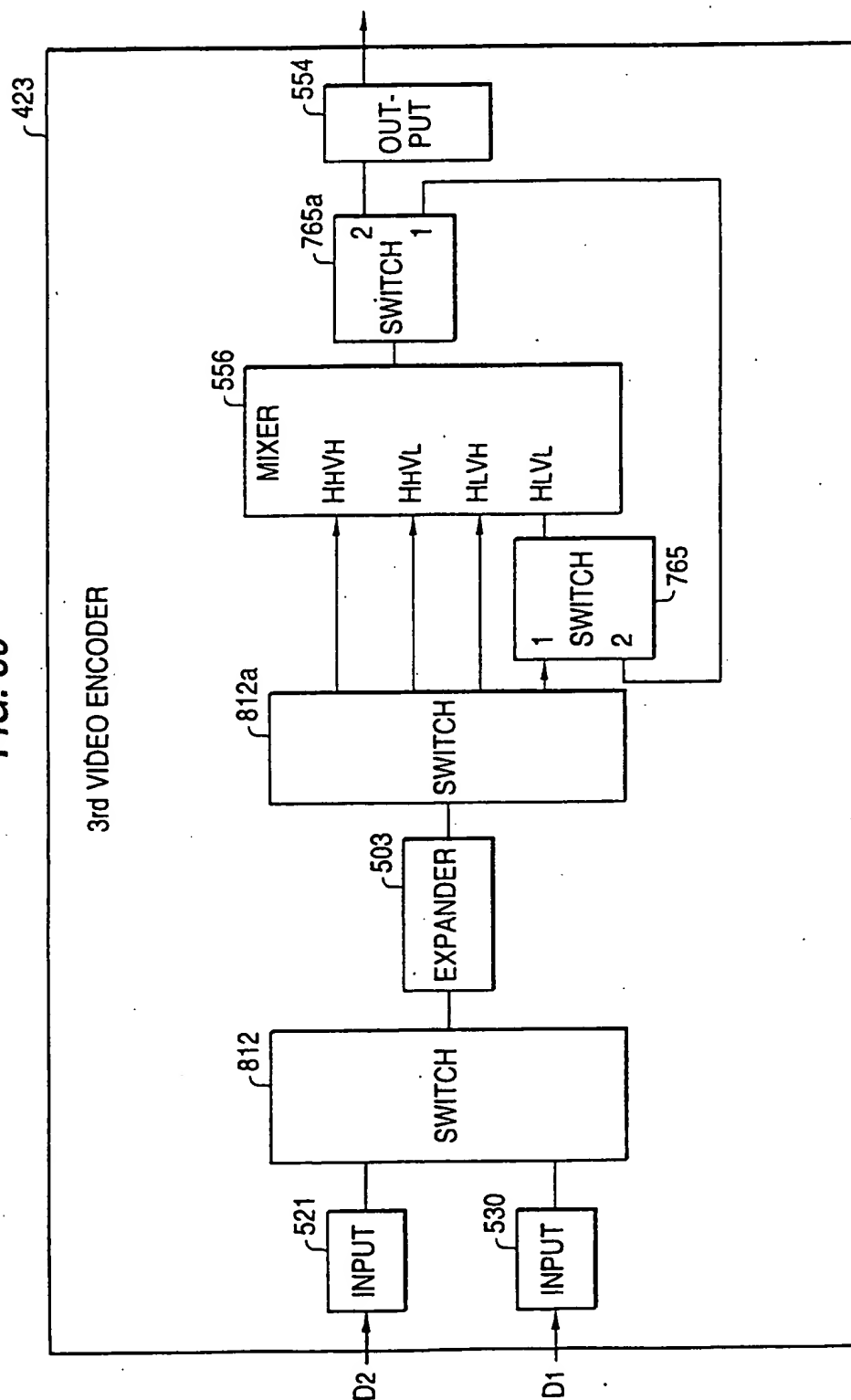


Figure 1 illustrates a sequence of 12 time slots (t0 to t11) for a system D1. The slots are organized into four groups, each containing three slots:

- Group 821 (t0-t2):**
 - 821a (t0): HLVL (1)
 - 821b (t1): HVLH (2)
 - 821c (t2): HVLH (1)
- Group 822 (t3-t5):**
 - 822 (t3): HLVL (2) (shaded)
 - 822a (t4): HVLH (2)
 - 822b (t5): HHVL (2)
- Group 823 (t6-t8):**
 - 823 (t6): HVLH (1)
 - 823a (t7): HHVL-H (1)
 - 823b (t8): HHVH-H (1)
- Group 824 (t9-t11):**
 - 824 (t9): HVLH (2)
 - 824a (t10): HHVL-H (2)
 - 824b (t11): HHVH-H (1)

The diagram shows a timeline from t0 to t11, with each slot labeled with its group number and the specific H/V configuration and count.

FIG. 82

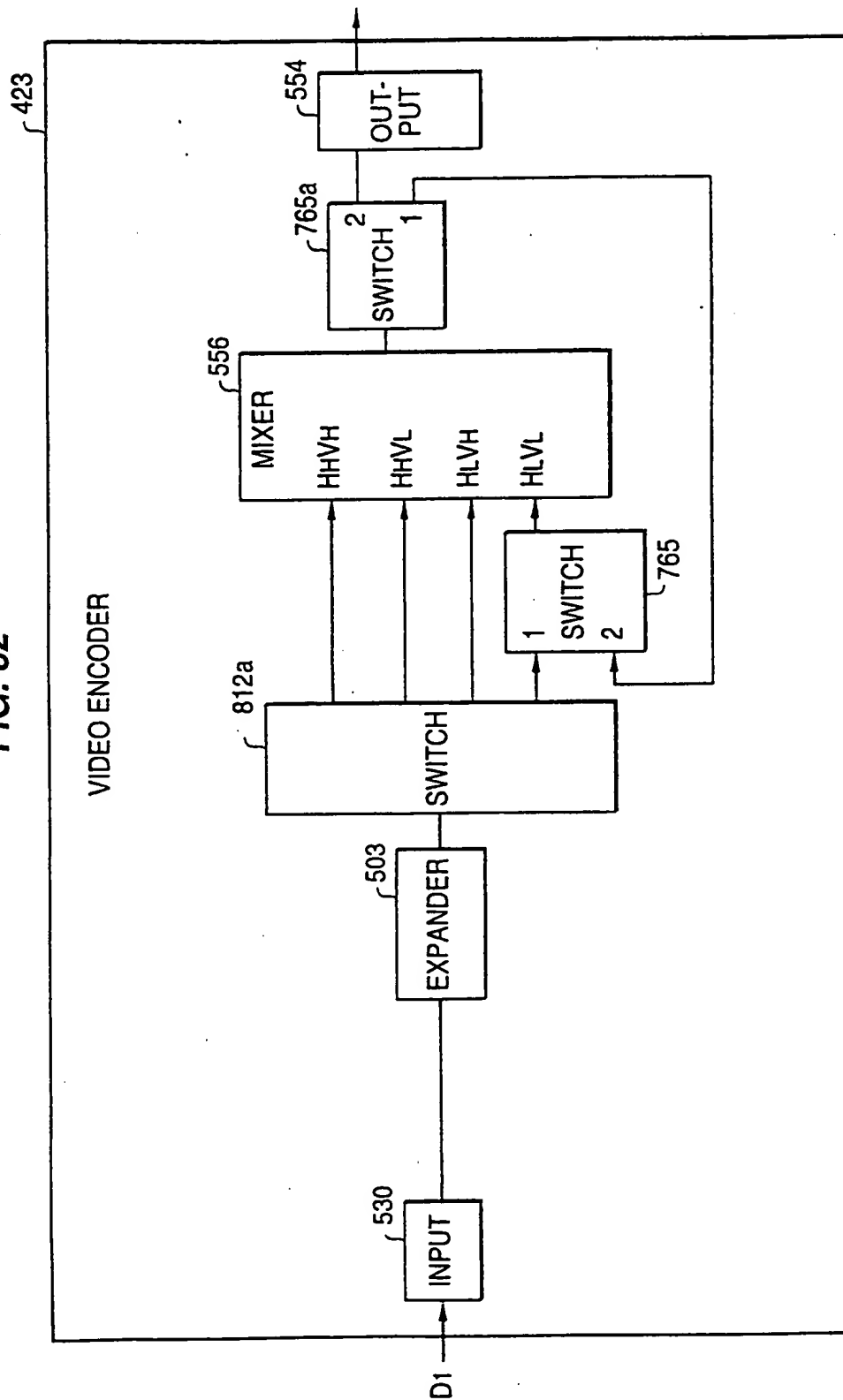


FIG. 83

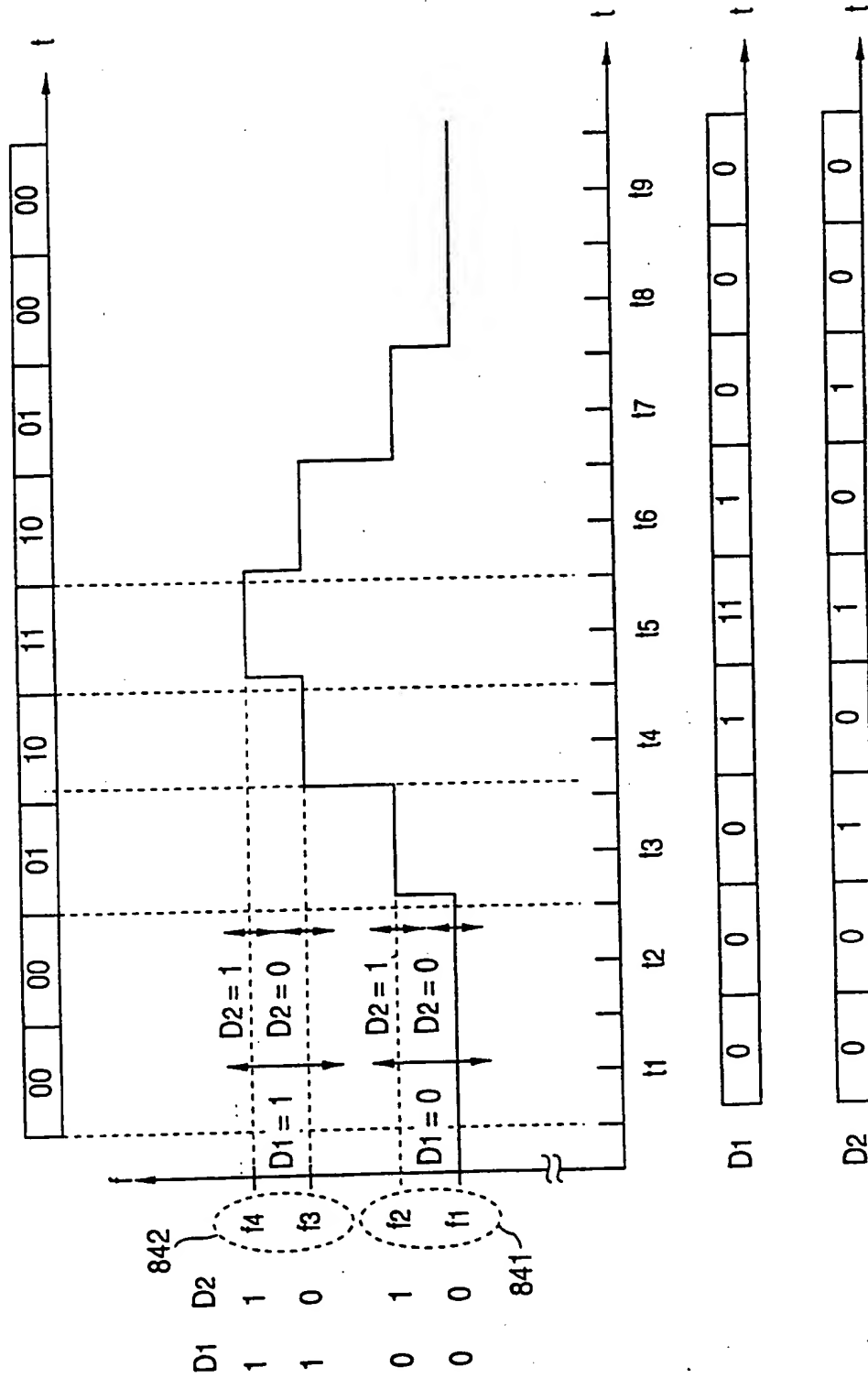


FIG. 84

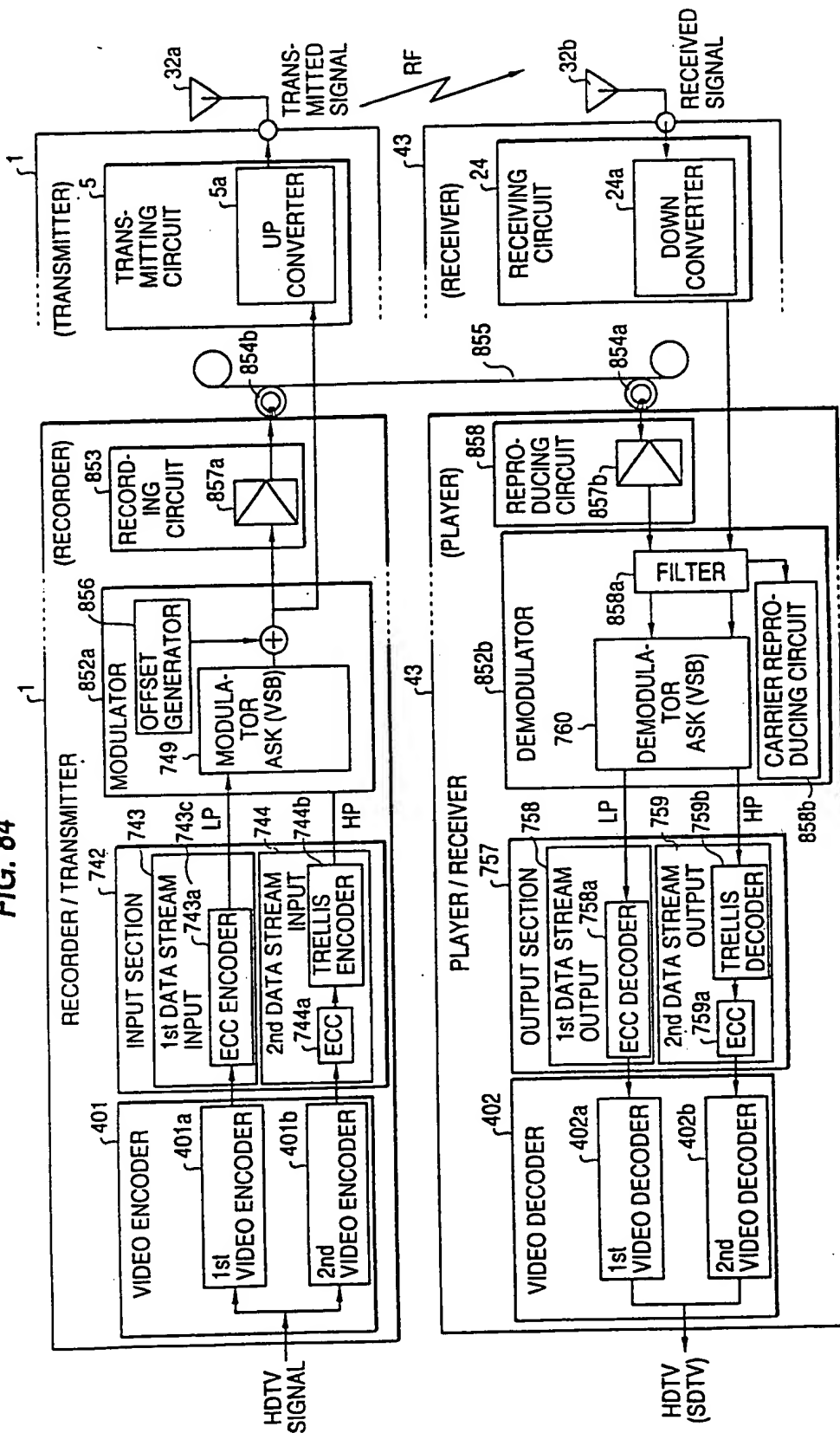


FIG. 85

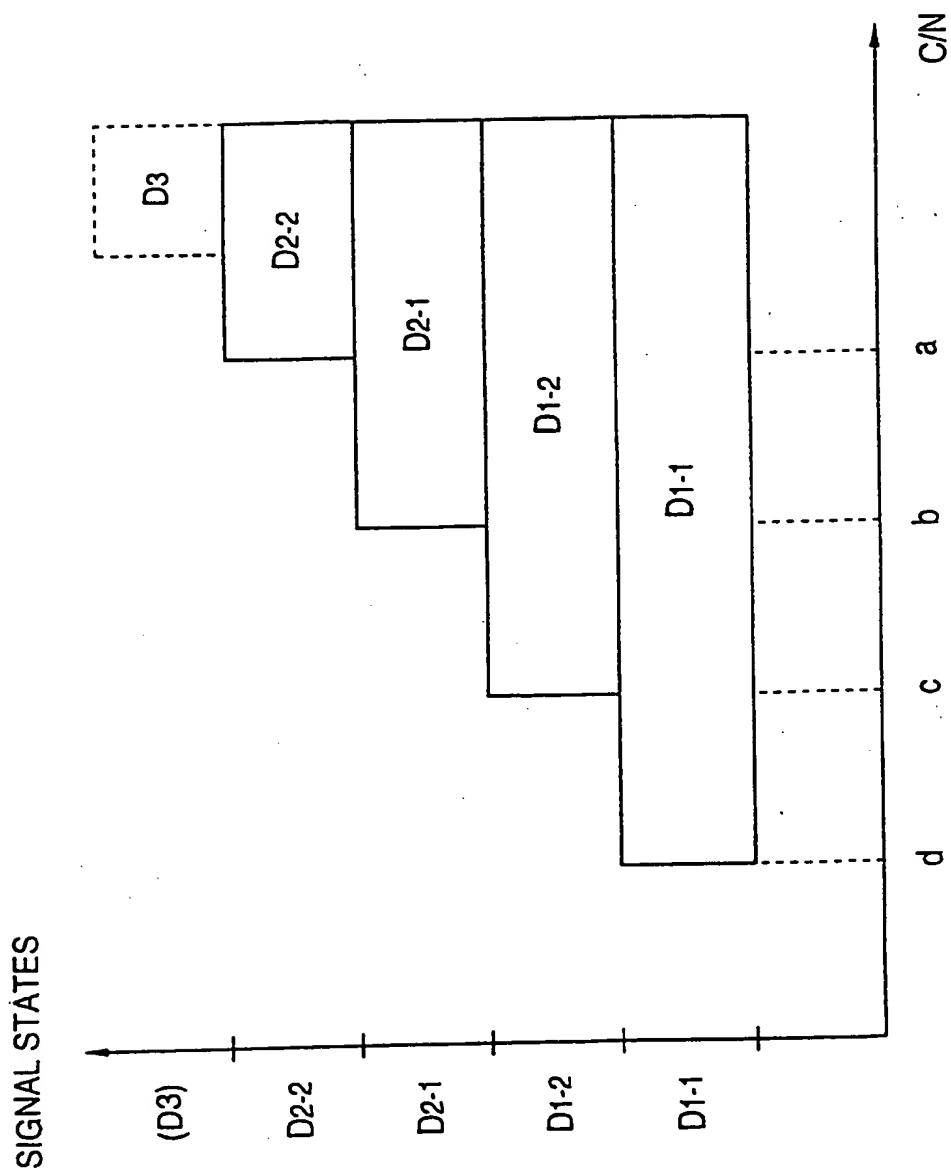


FIG. 86

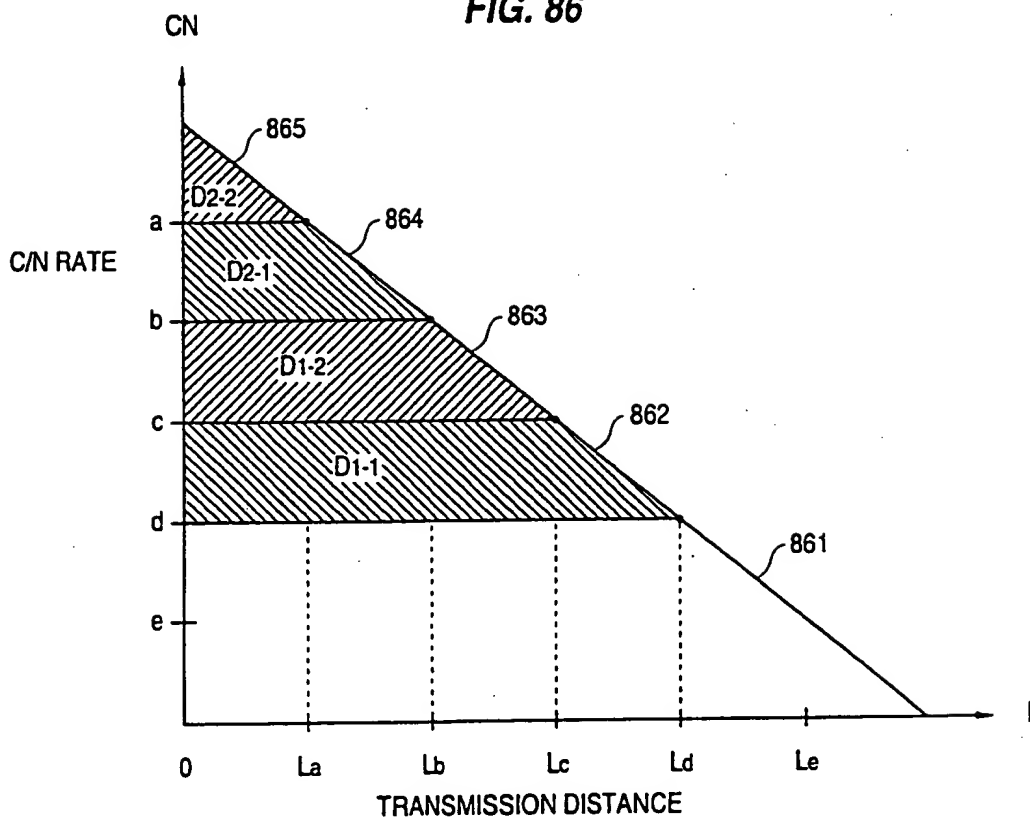


FIG. 87

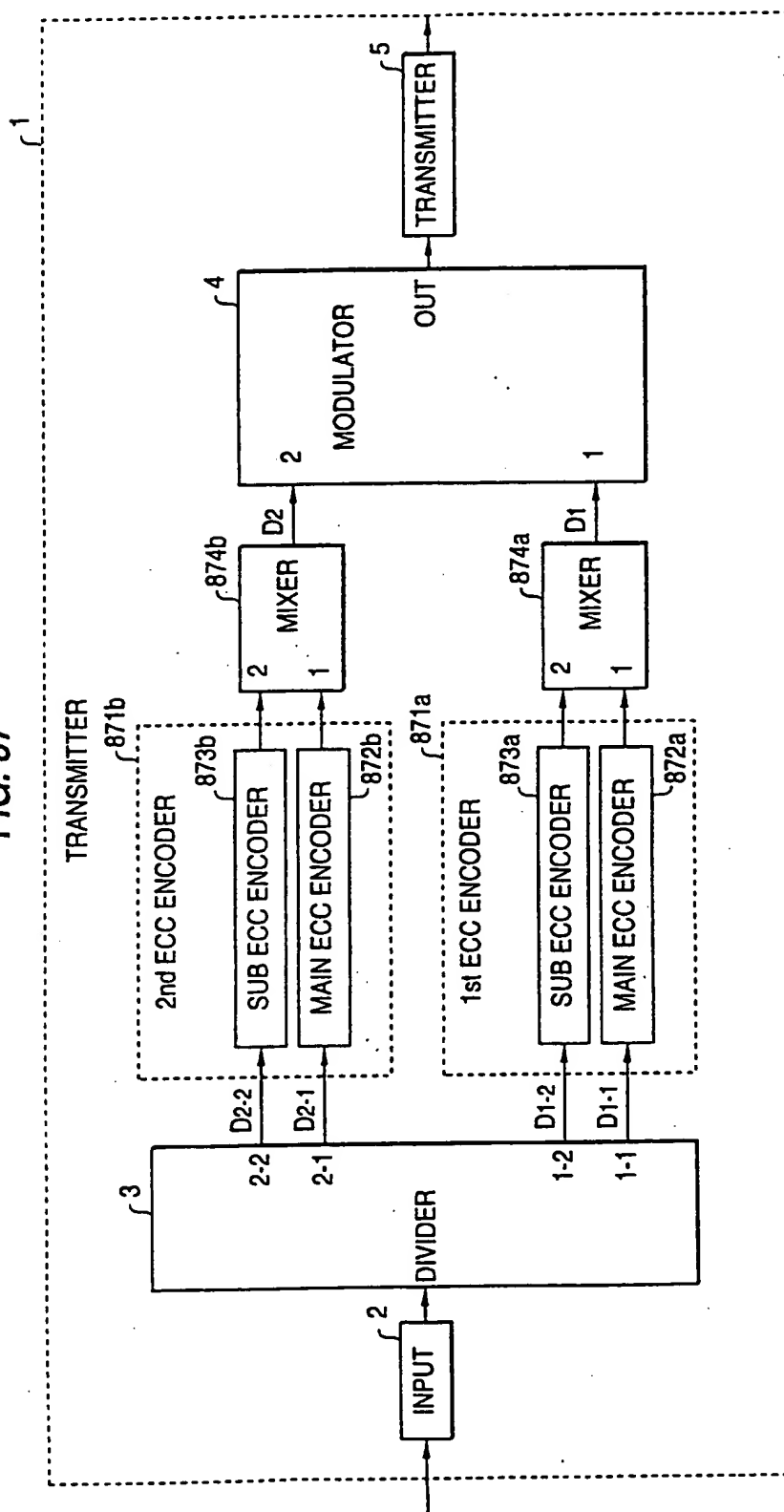


FIG. 88

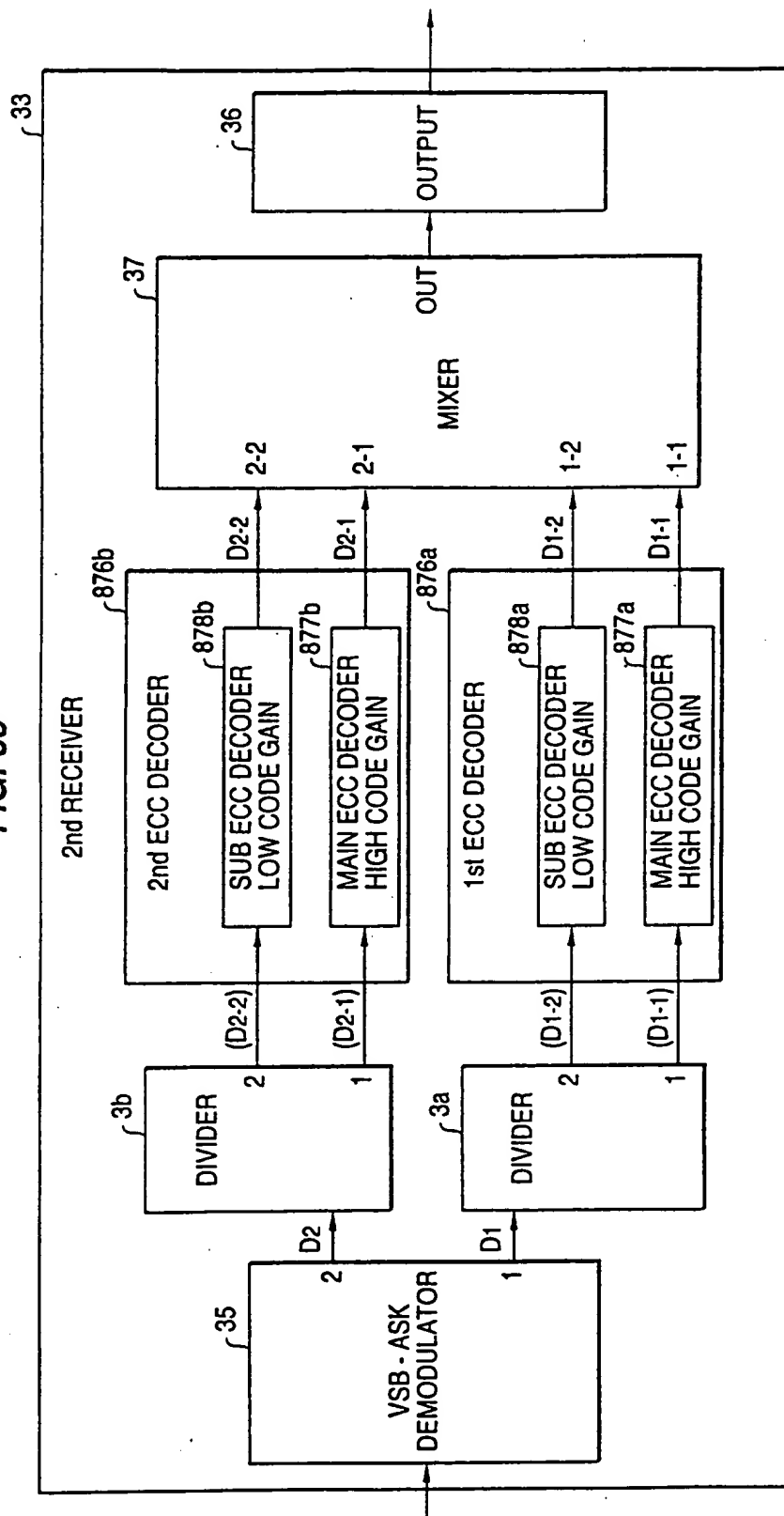


FIG. 89

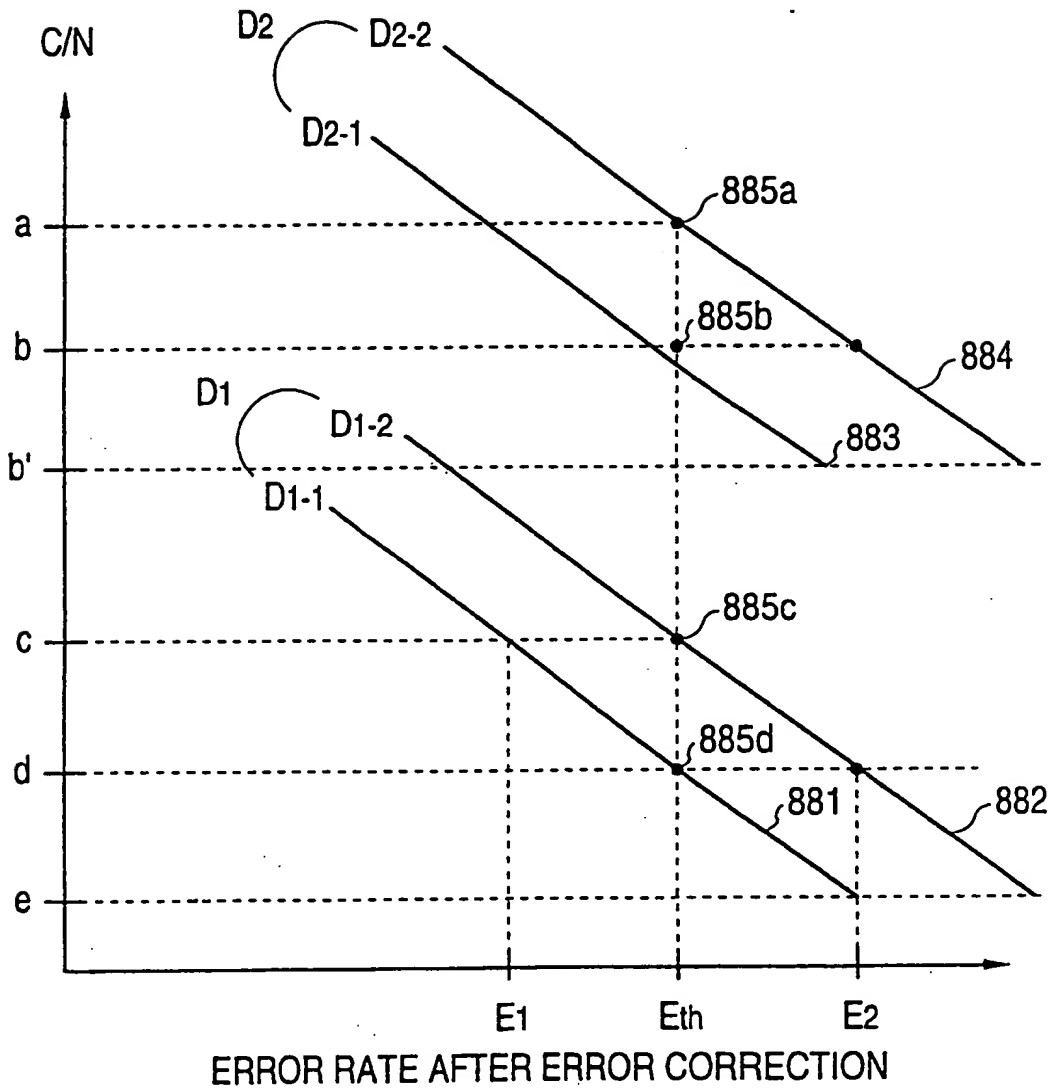


FIG. 90

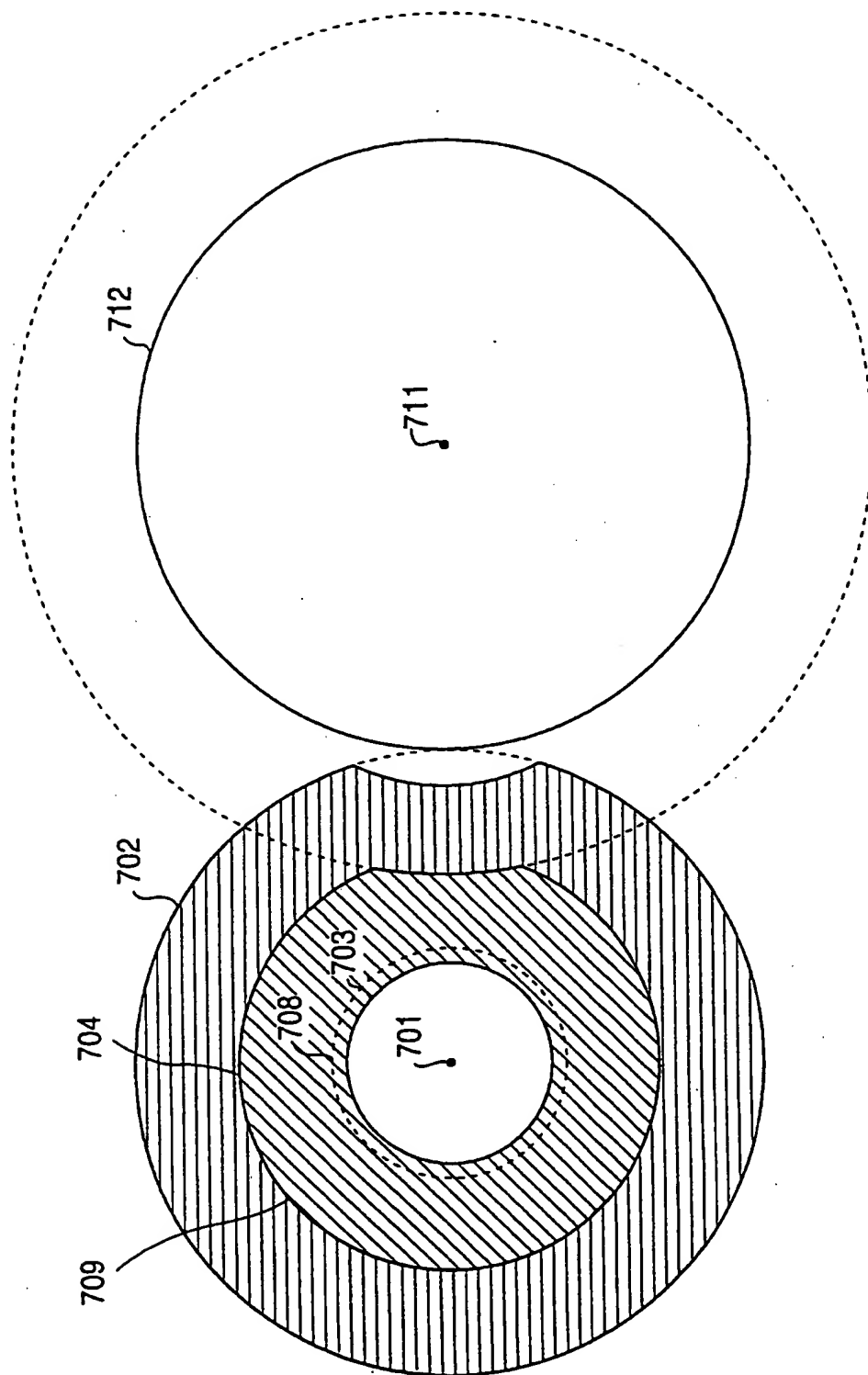


FIG. 91

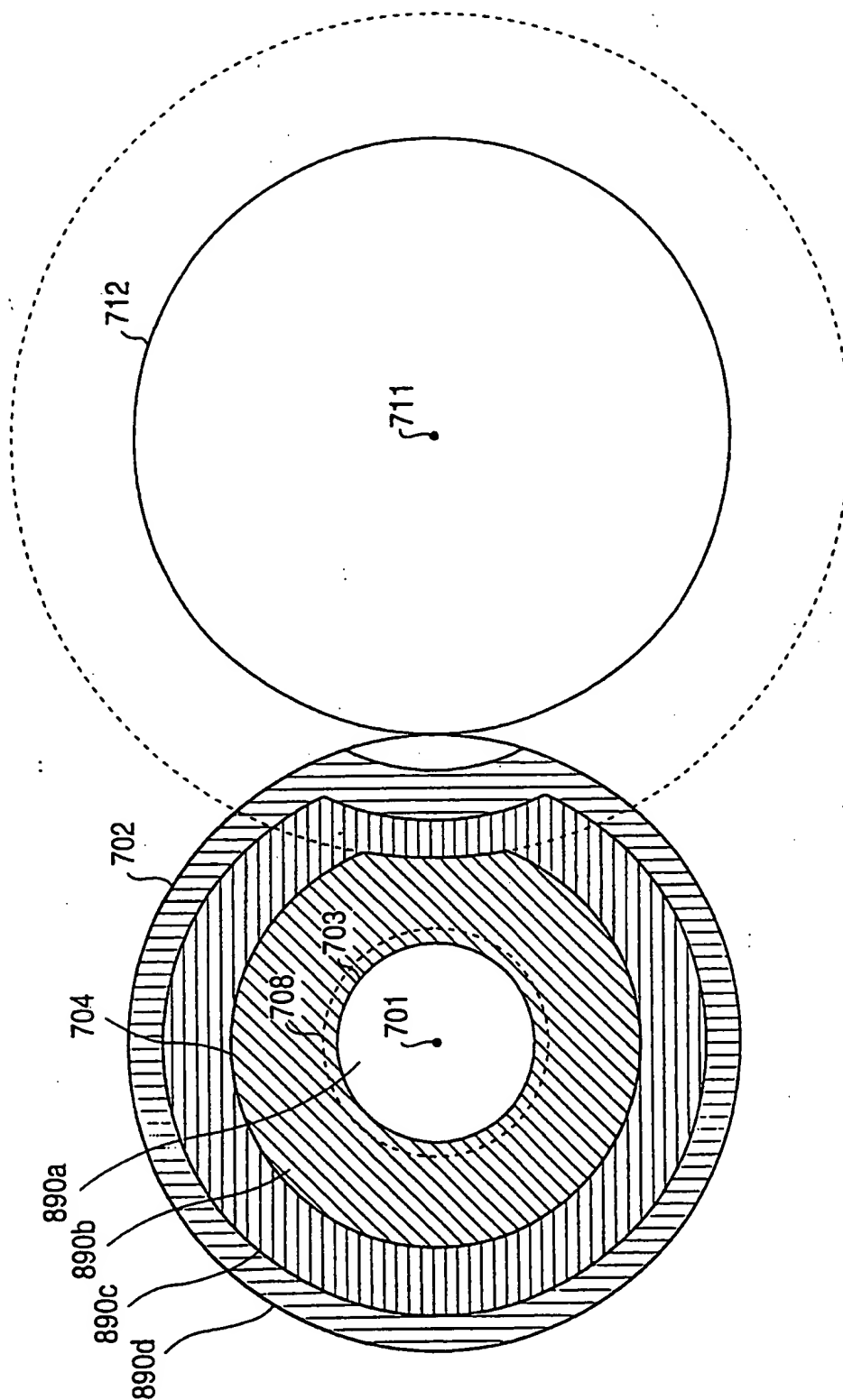


FIG. 92

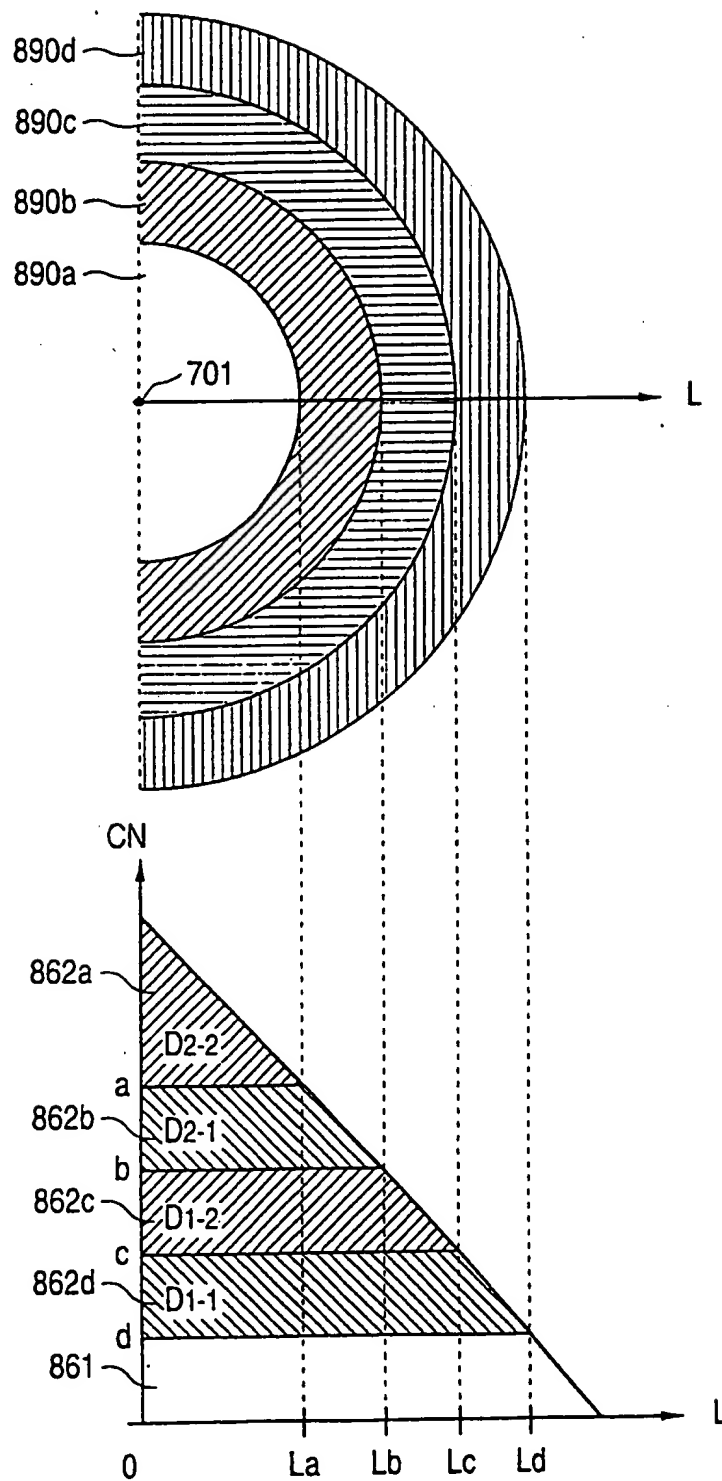


FIG. 93

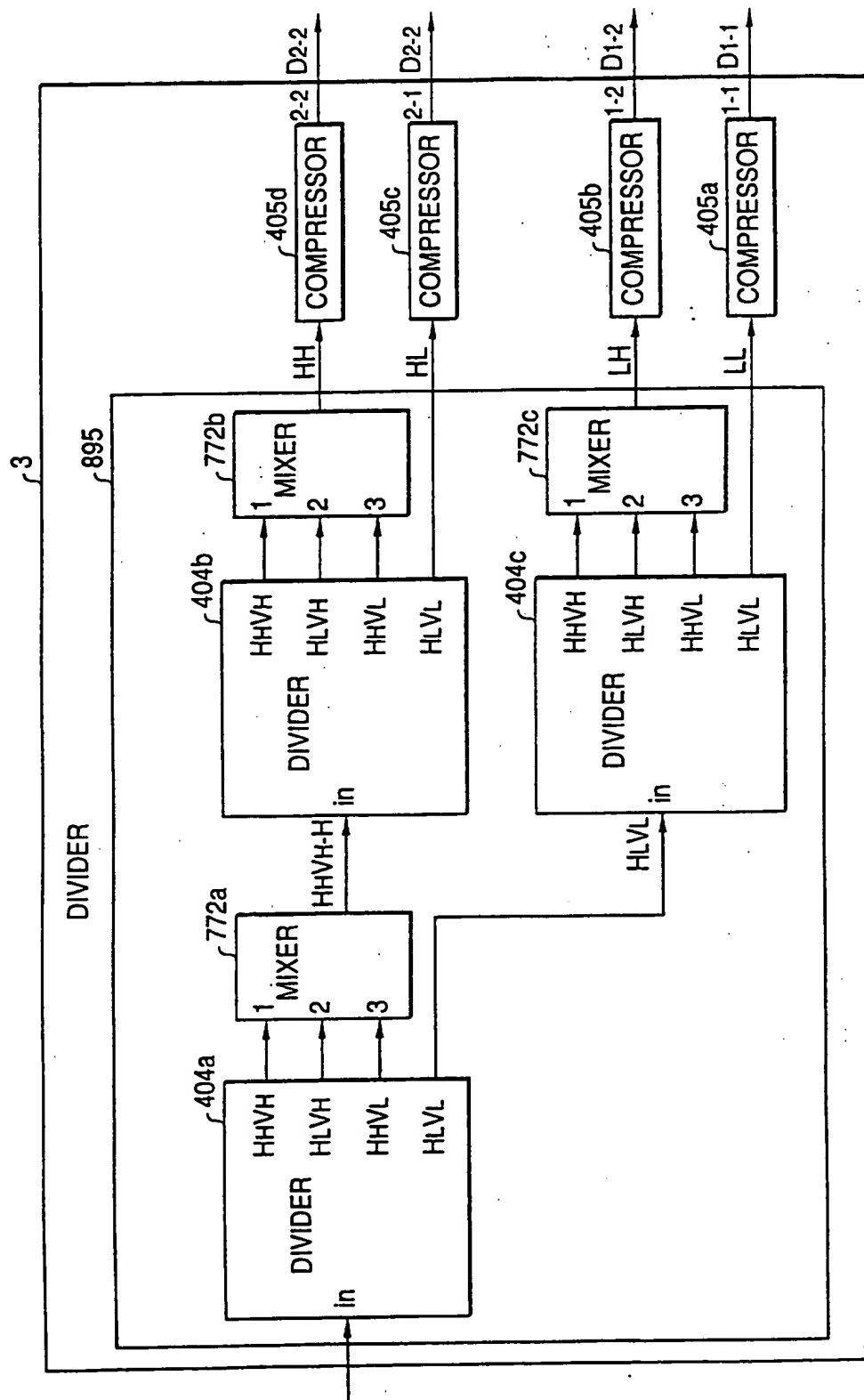


FIG. 94

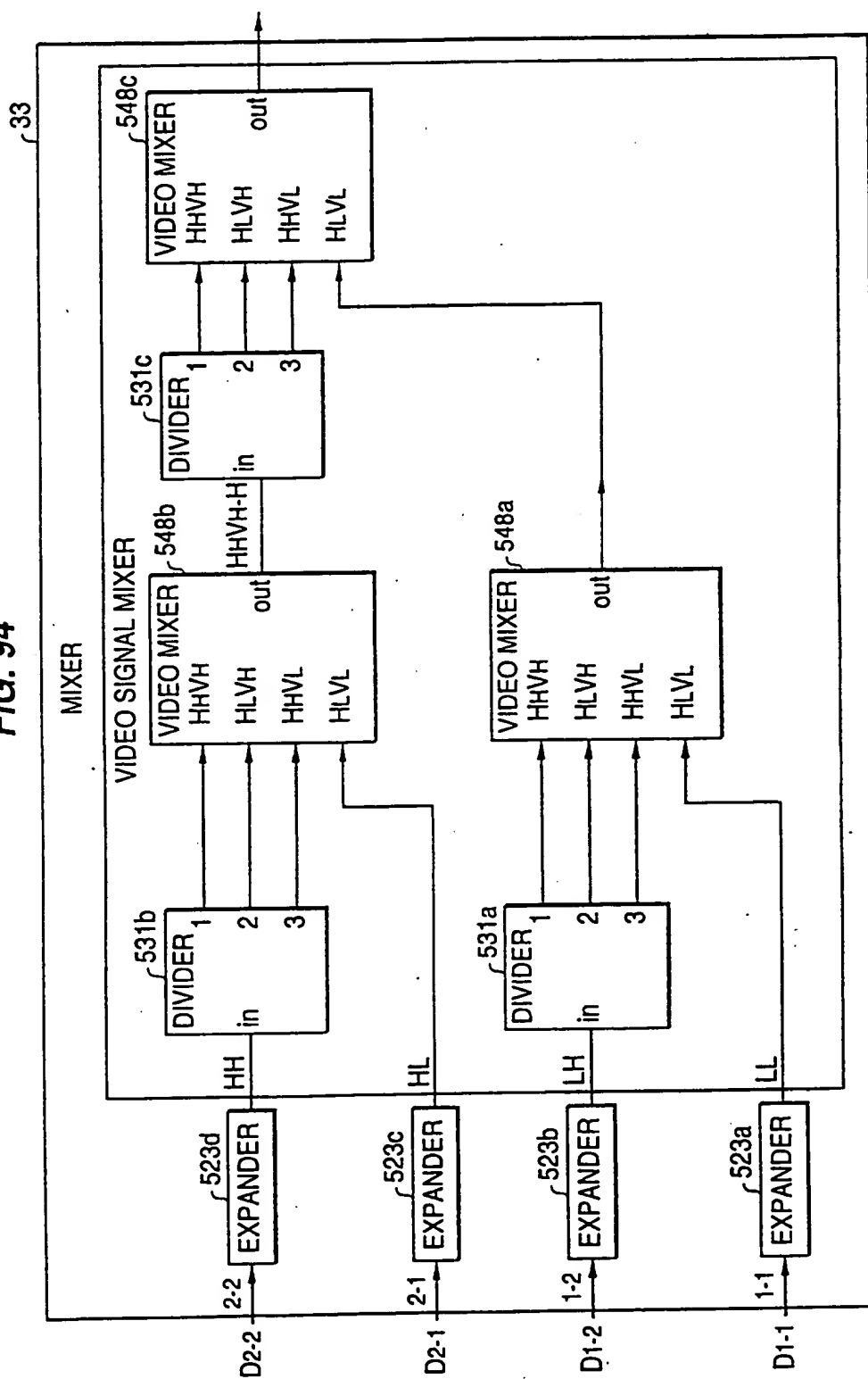


FIG. 95

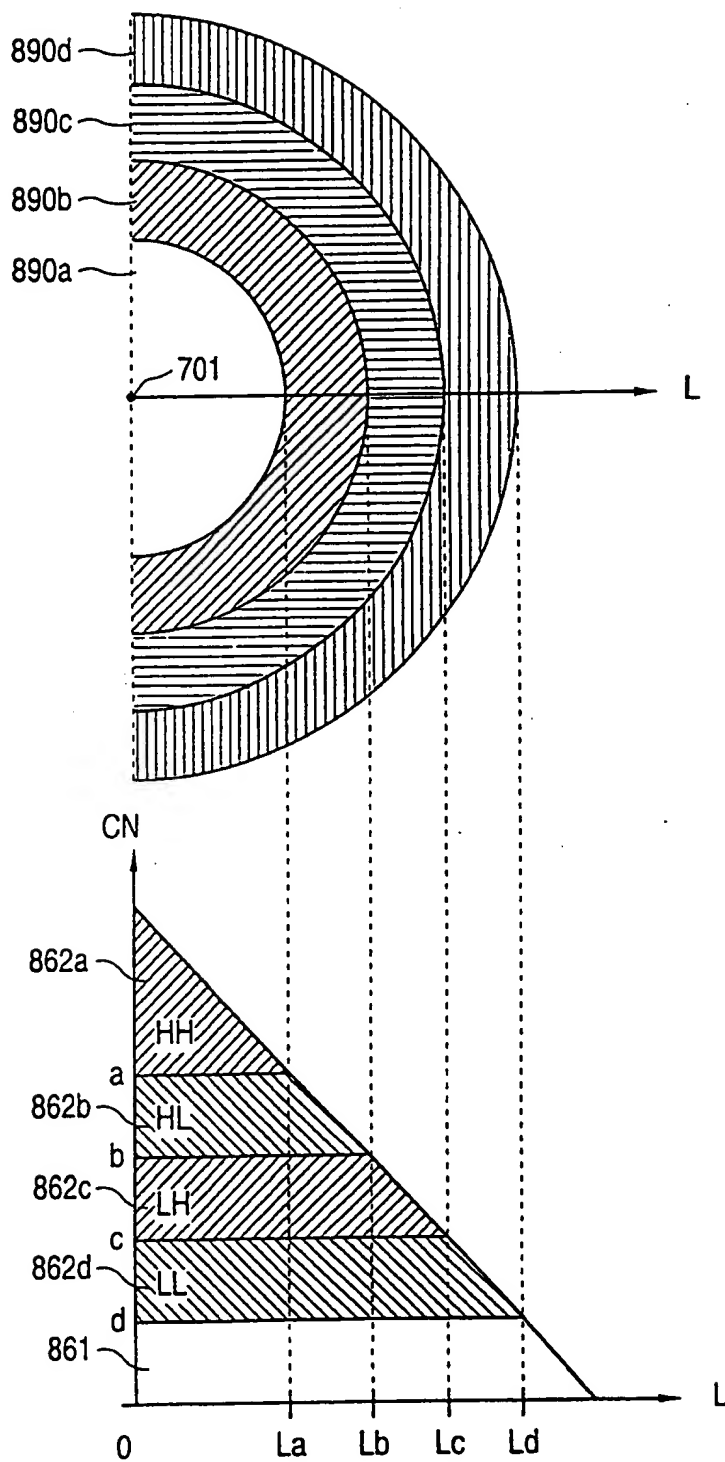


FIG. 96

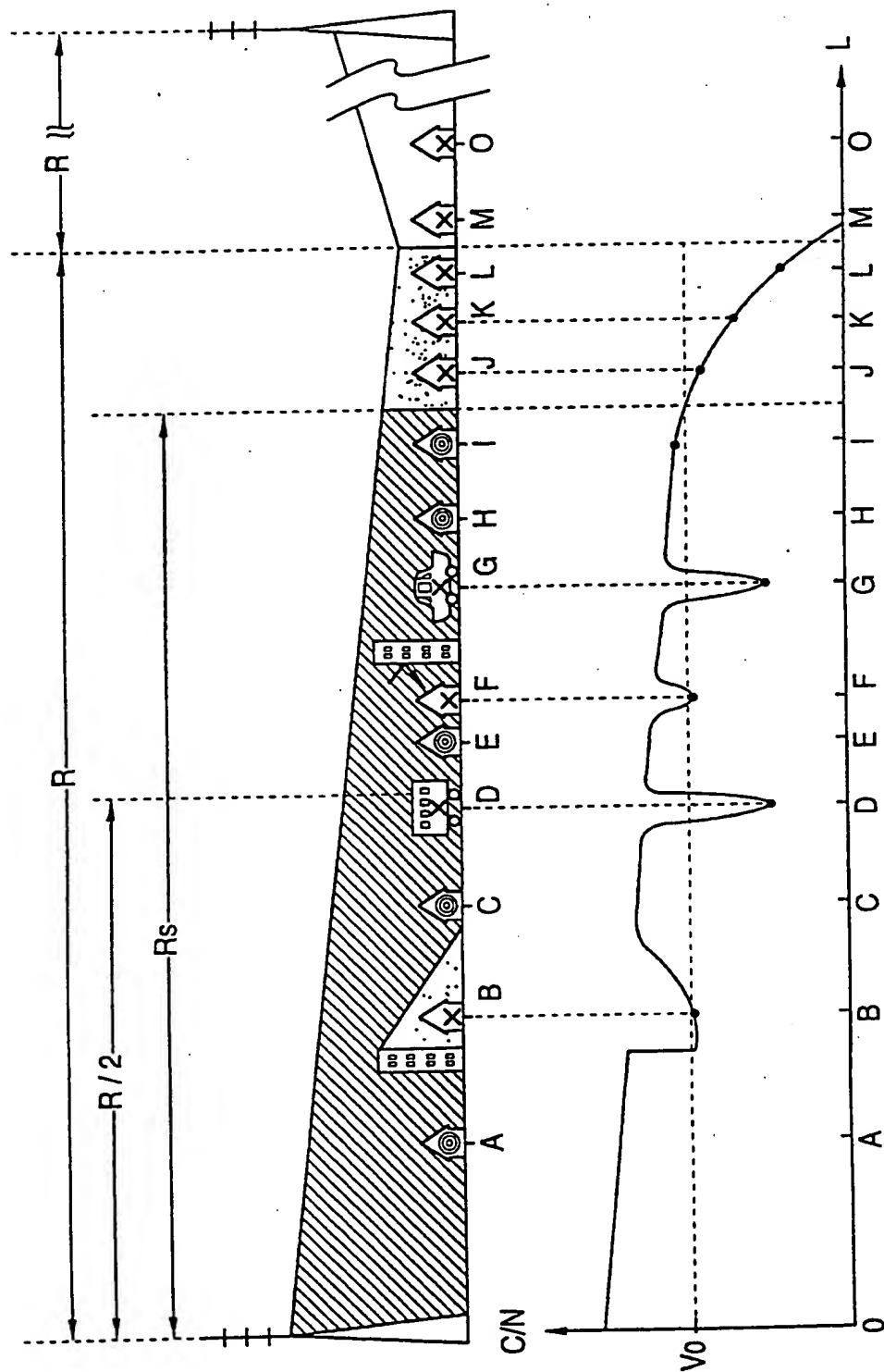


FIG. 97

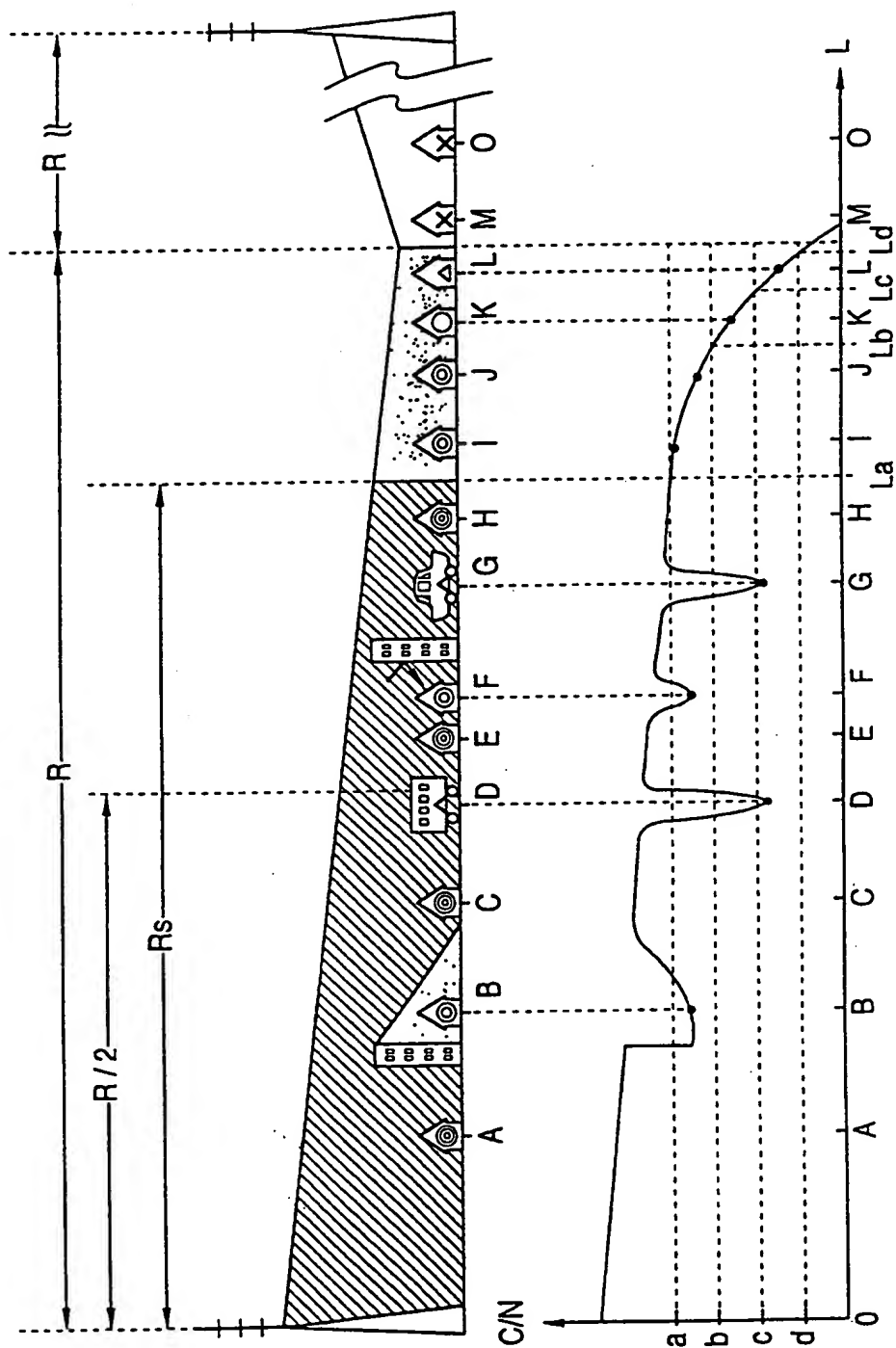


FIG. 98

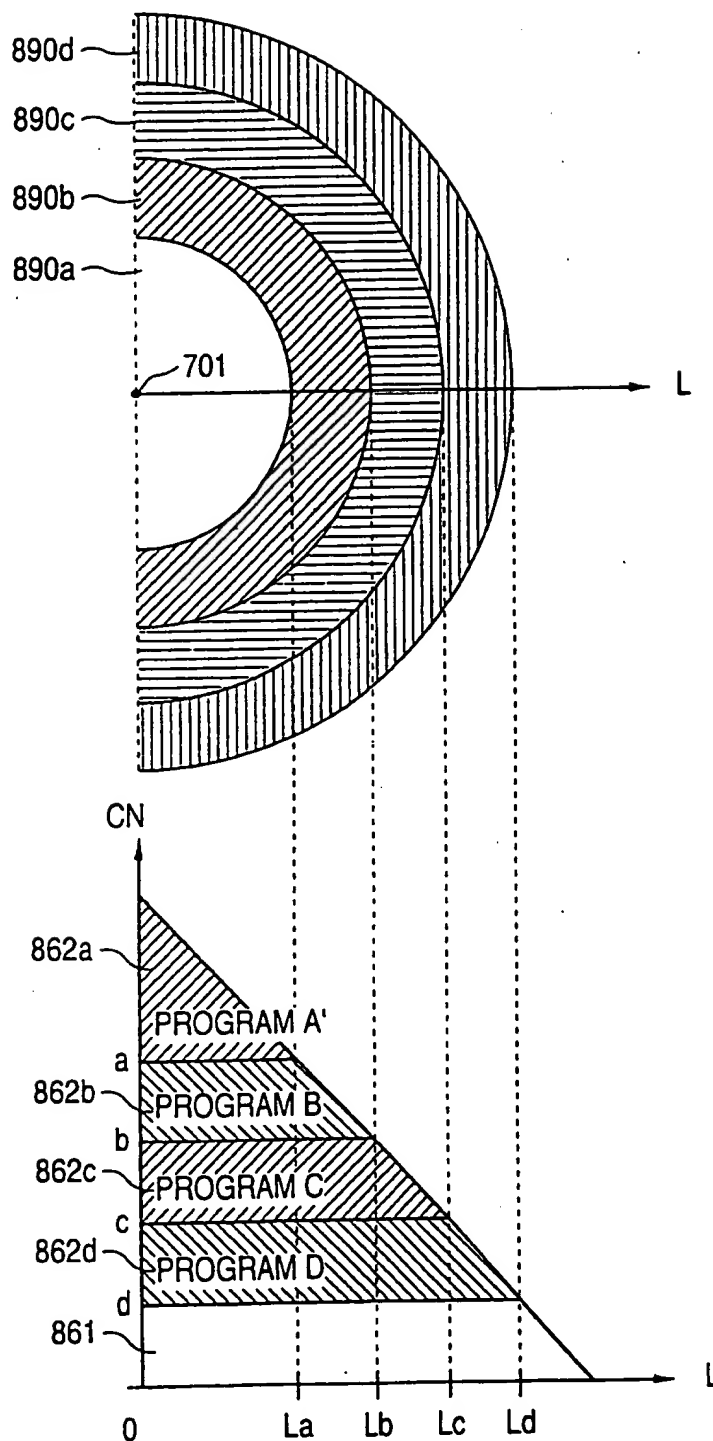


FIG. 99

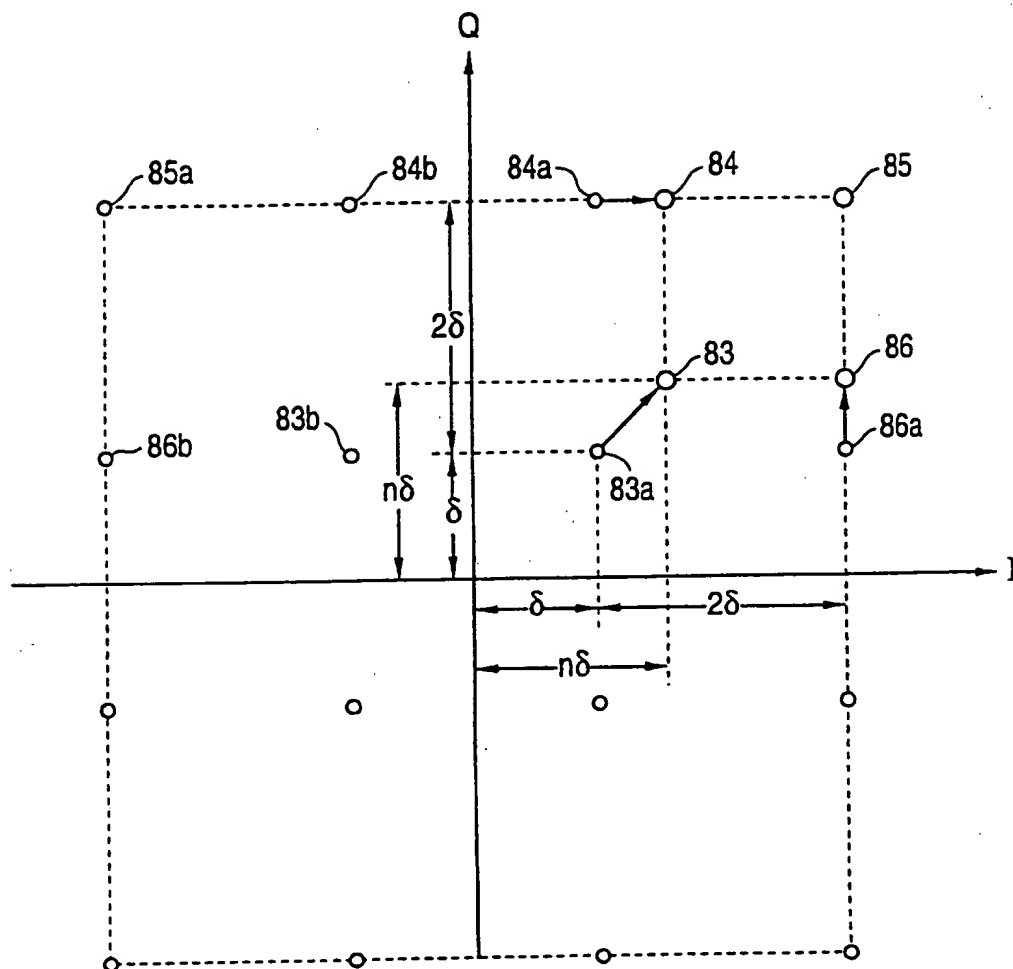


FIG. 100

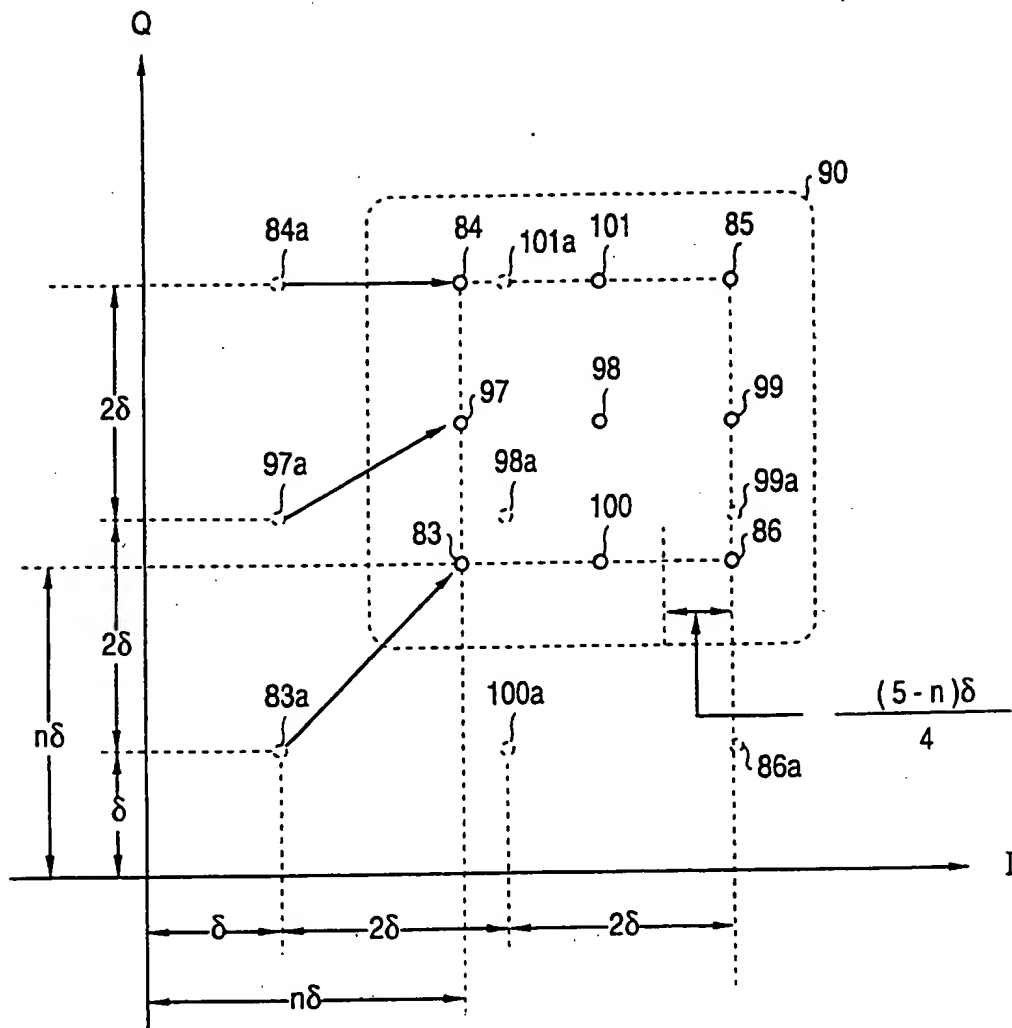


FIG. 101

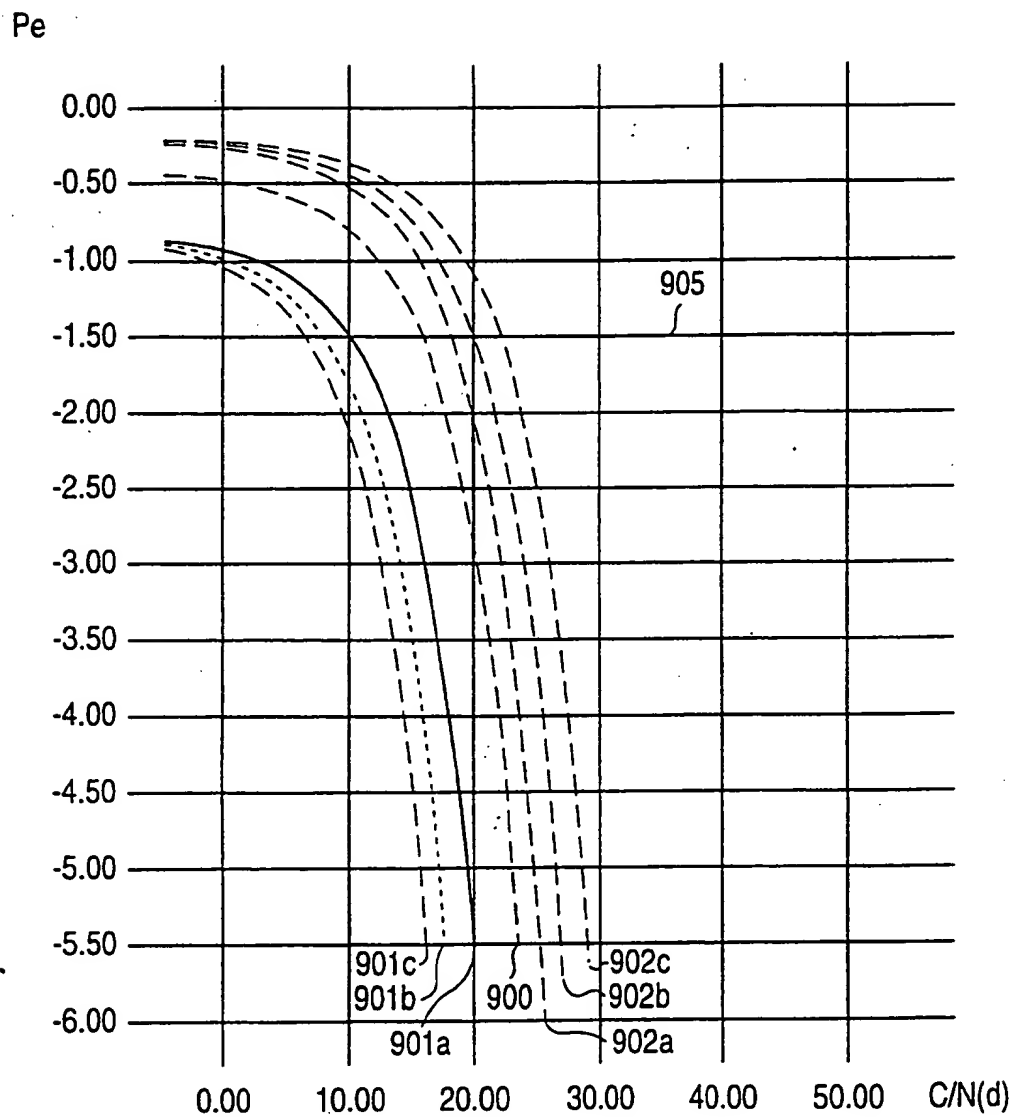


FIG. 102

Pe

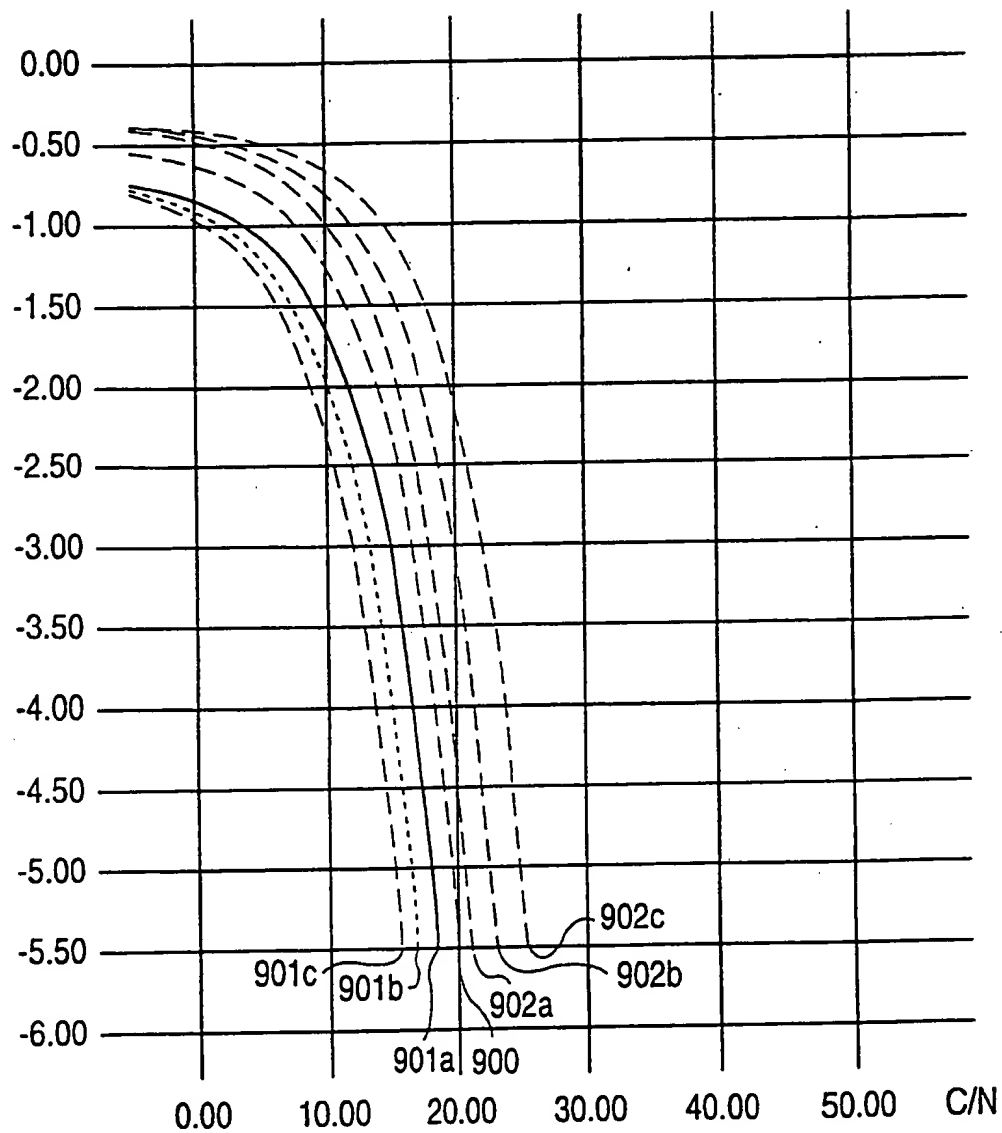


FIG. 103

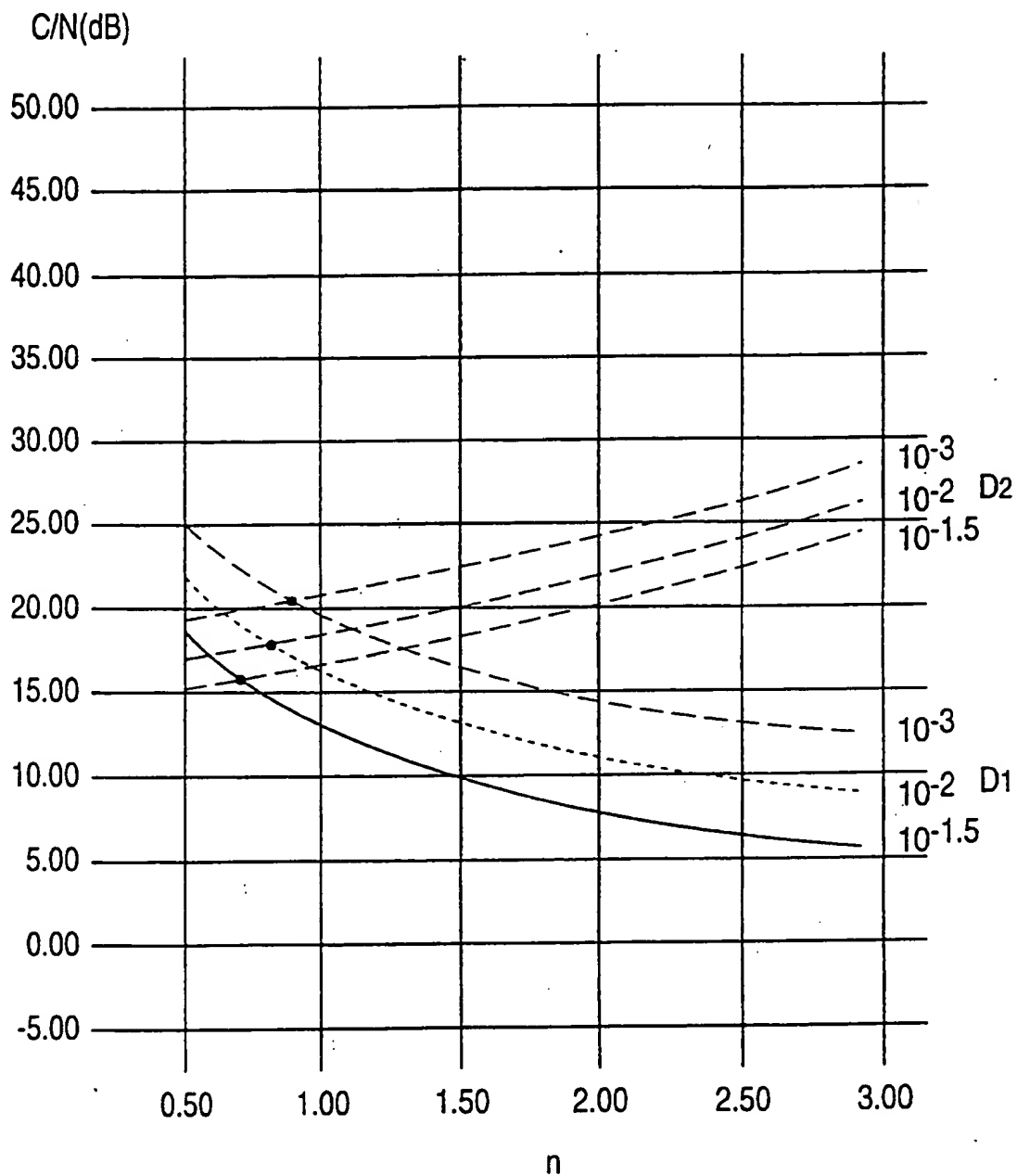


FIG. 104

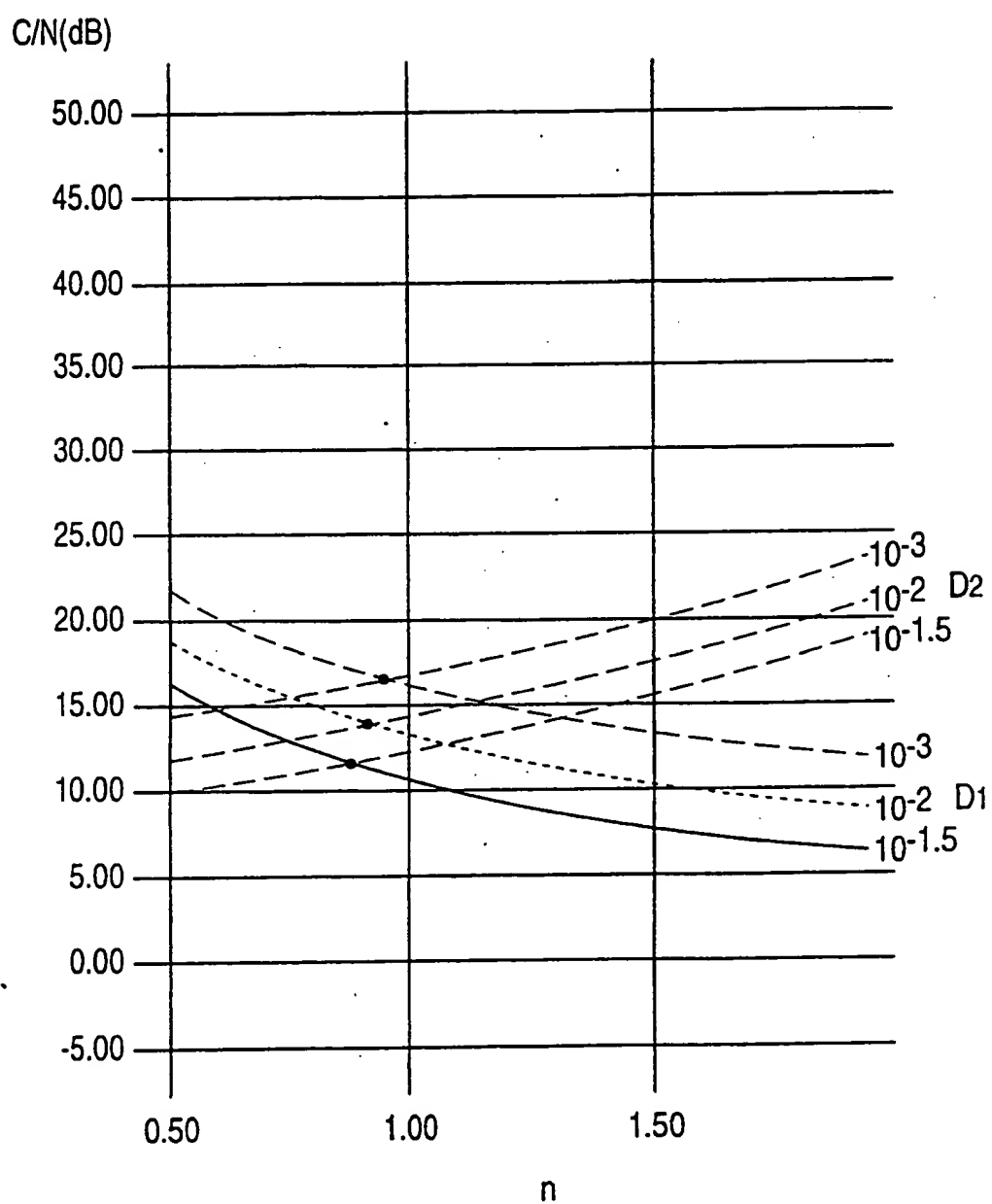


FIG. 105

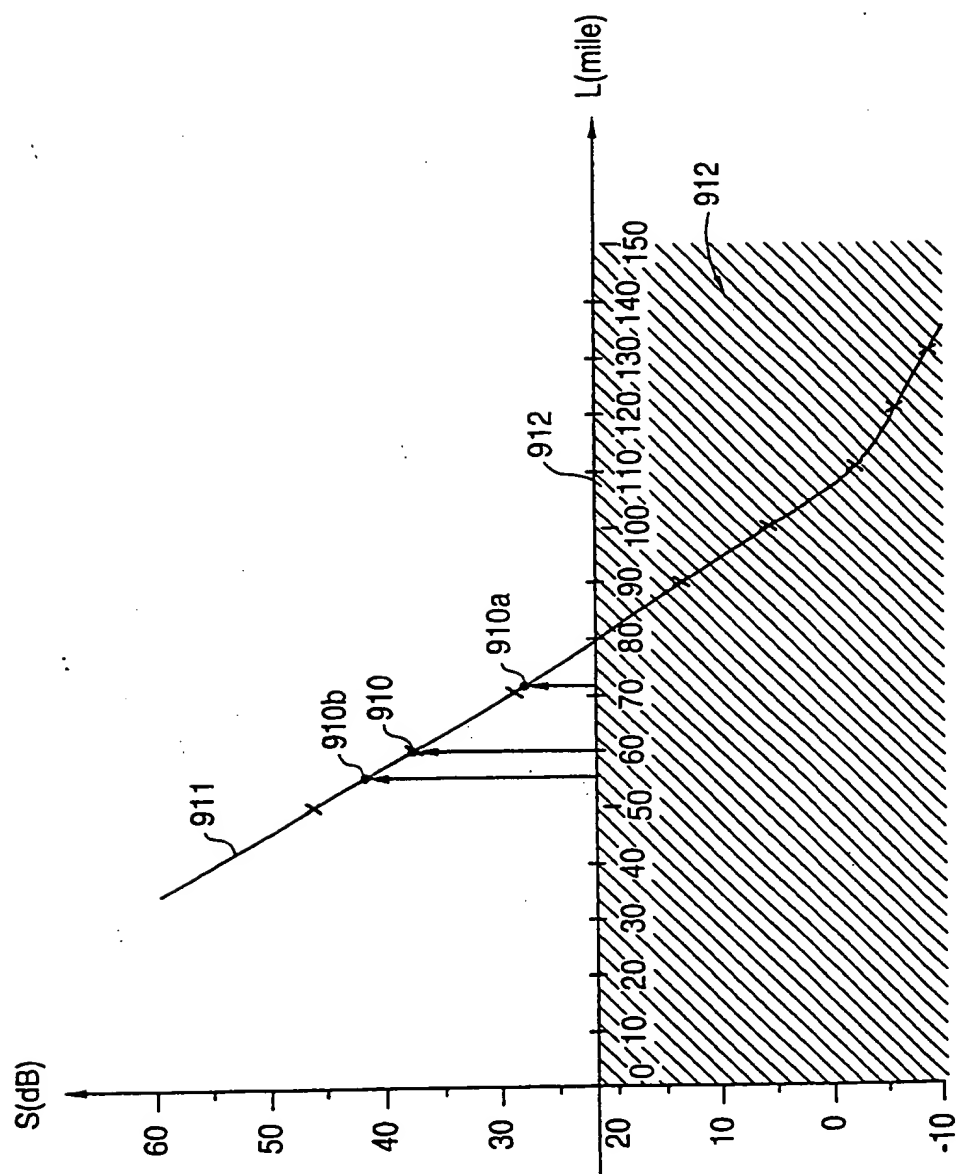


FIG. 106

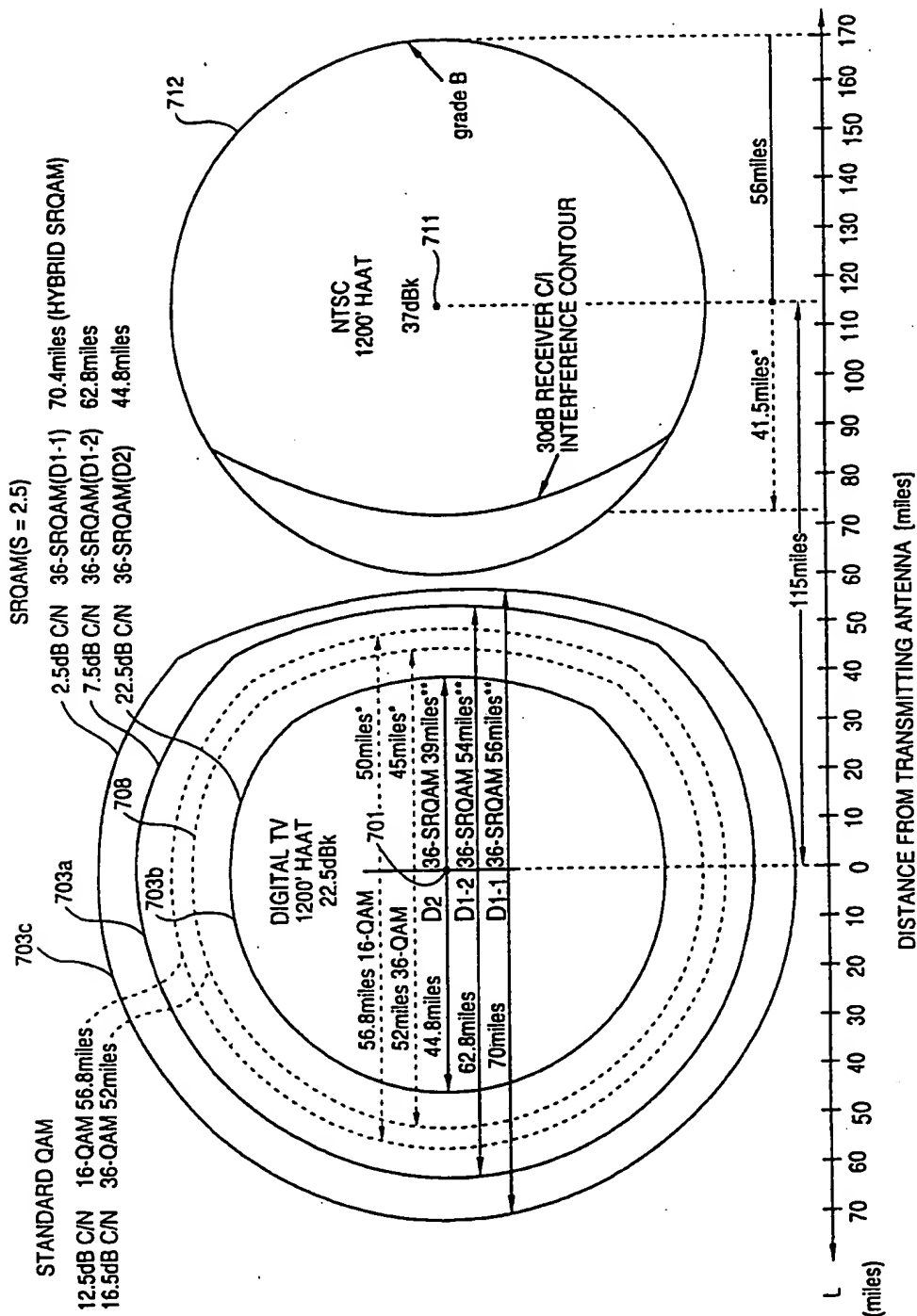


FIG. 107

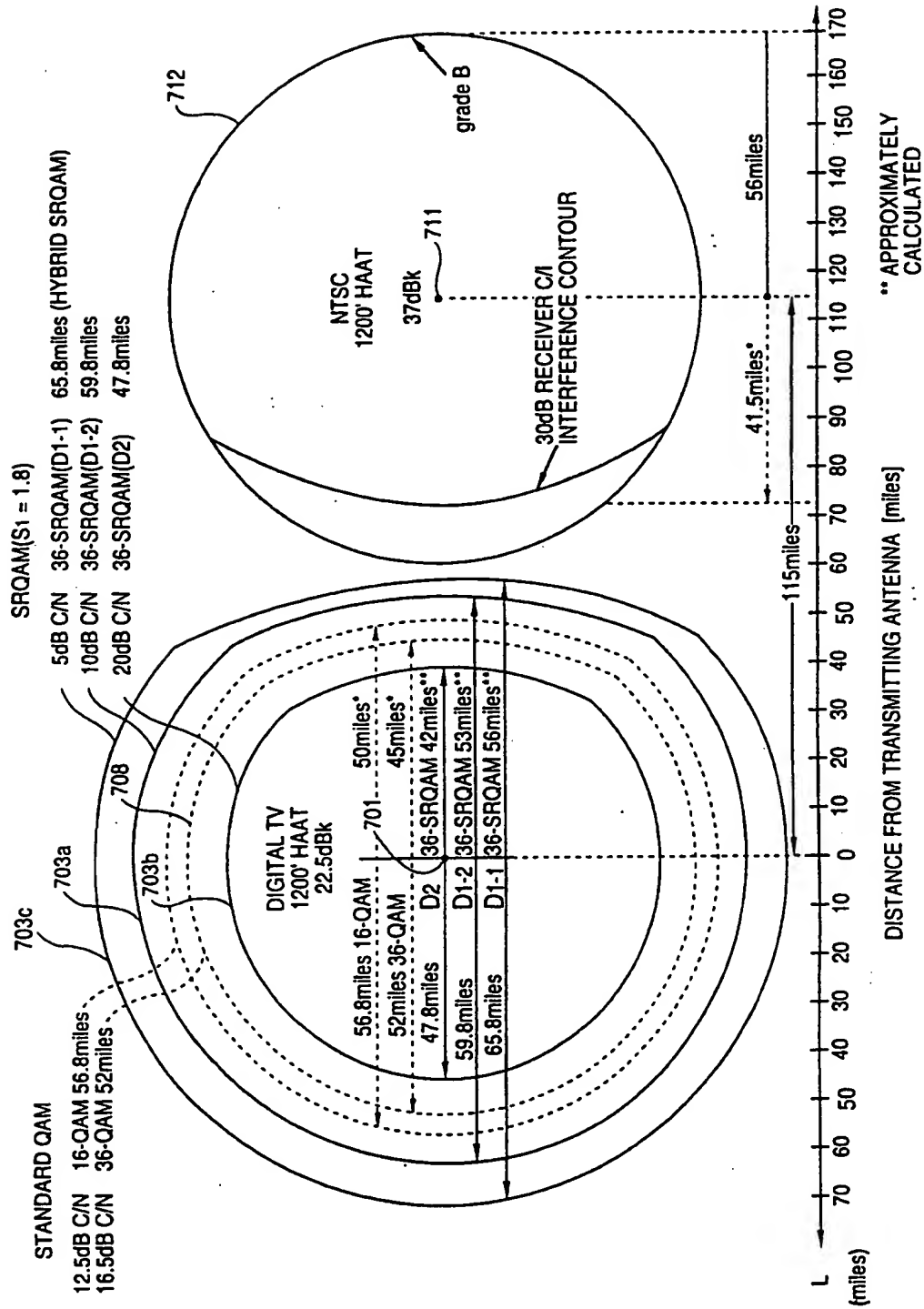


FIG. 108(a)

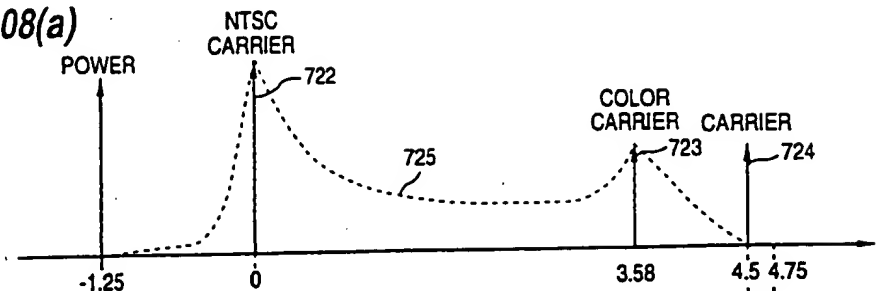


FIG. 108(b)

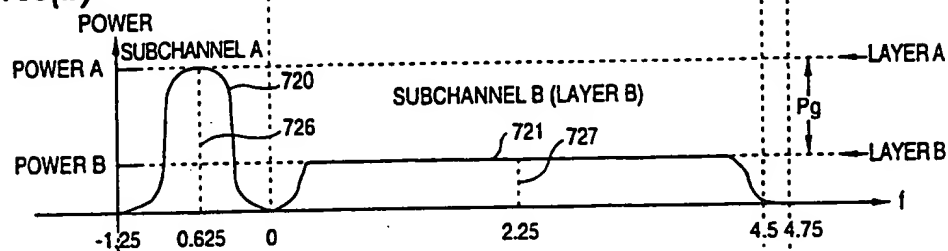


FIG. 108(c)

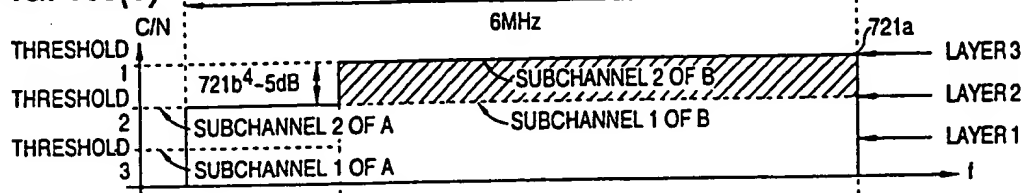


FIG. 108(d)

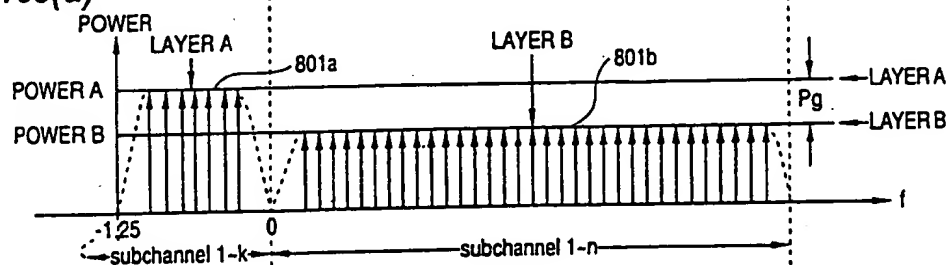


FIG. 108(e)

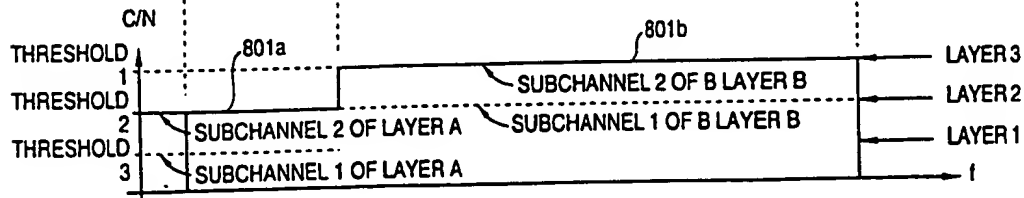


FIG. 109

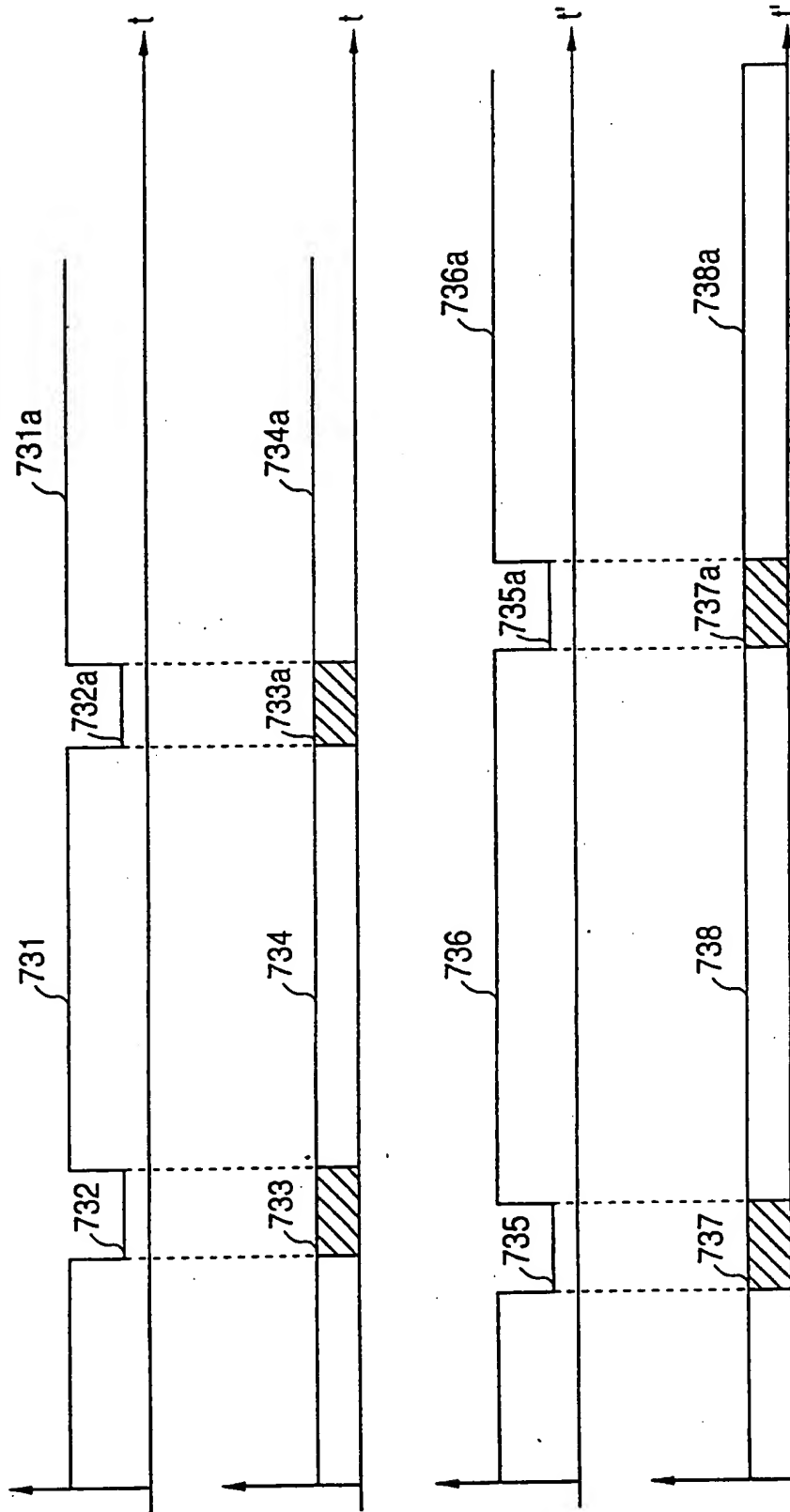
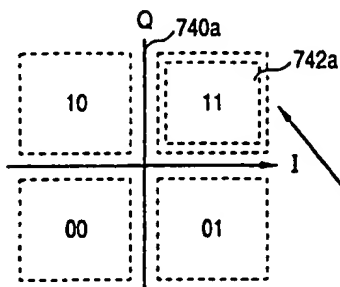


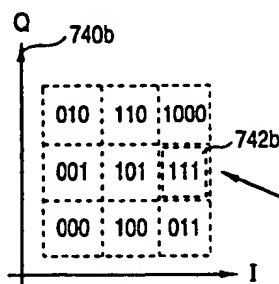
FIG. 111

SUBCHANNEL-1 (SRQAM:D1 = 2bit)



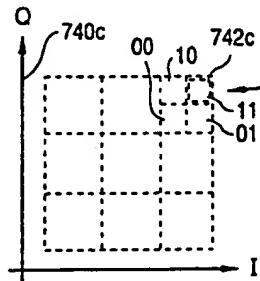
CODE WORD-1

SUBCHANNEL-2 (36-SRQAM:D2 = 3bit + 1/8bit)



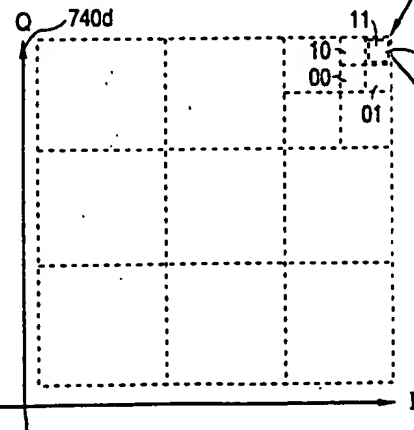
CODE WORD-2

SUBCHANNEL-3 (144-SRQAM:D3 = 2bit)



CODE WORD-3

SUBCHANNEL-4 (576-SRQAM:D4 = 2bit)



CODE WORD-4

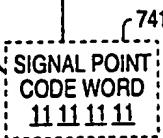
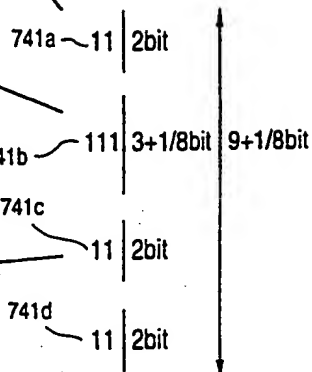
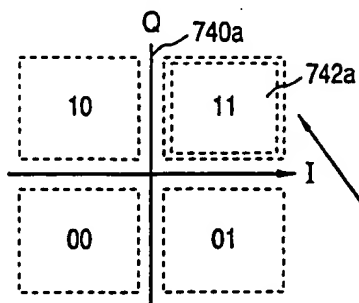
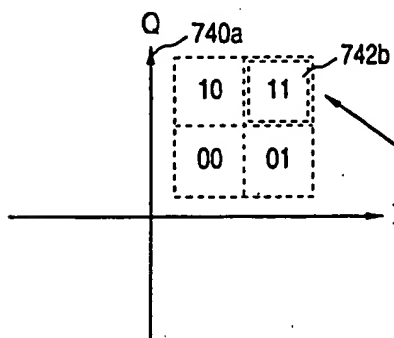


FIG. 112

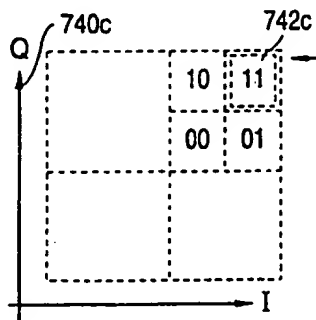
SUBCHANNEL-1 (SRQAM:D1 = 2bit)



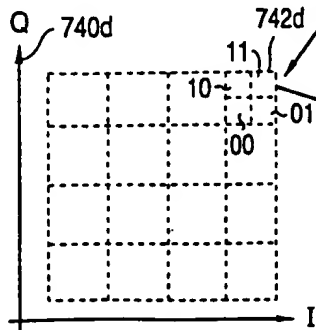
SUBCHANNEL-2 (16-SRQAM:D2 = 2bit)



SUBCHANNEL-3 (64-SRQAM:D3 = 2bit)



SUBCHANNEL-4 (256-SRQAM:D4 = 2bit)



CODE WORD-1

CODE WORD-2

CODE WORD-3

CODE WORD-4

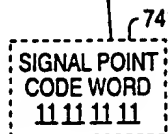
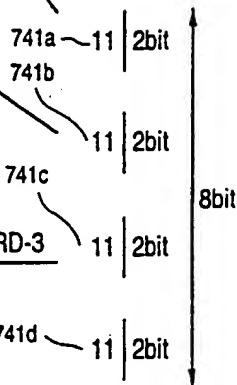


FIG. 113

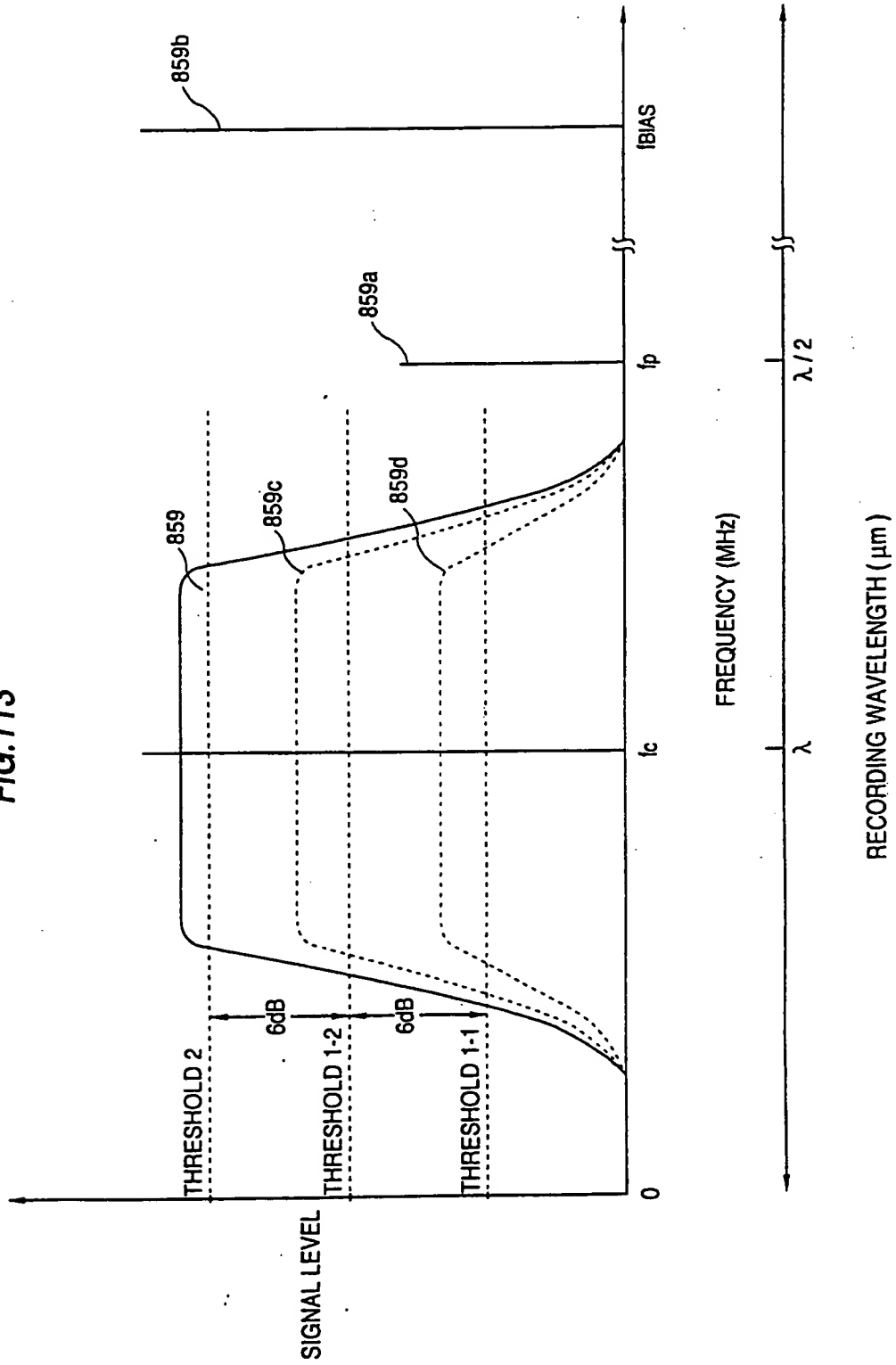


FIG. 114

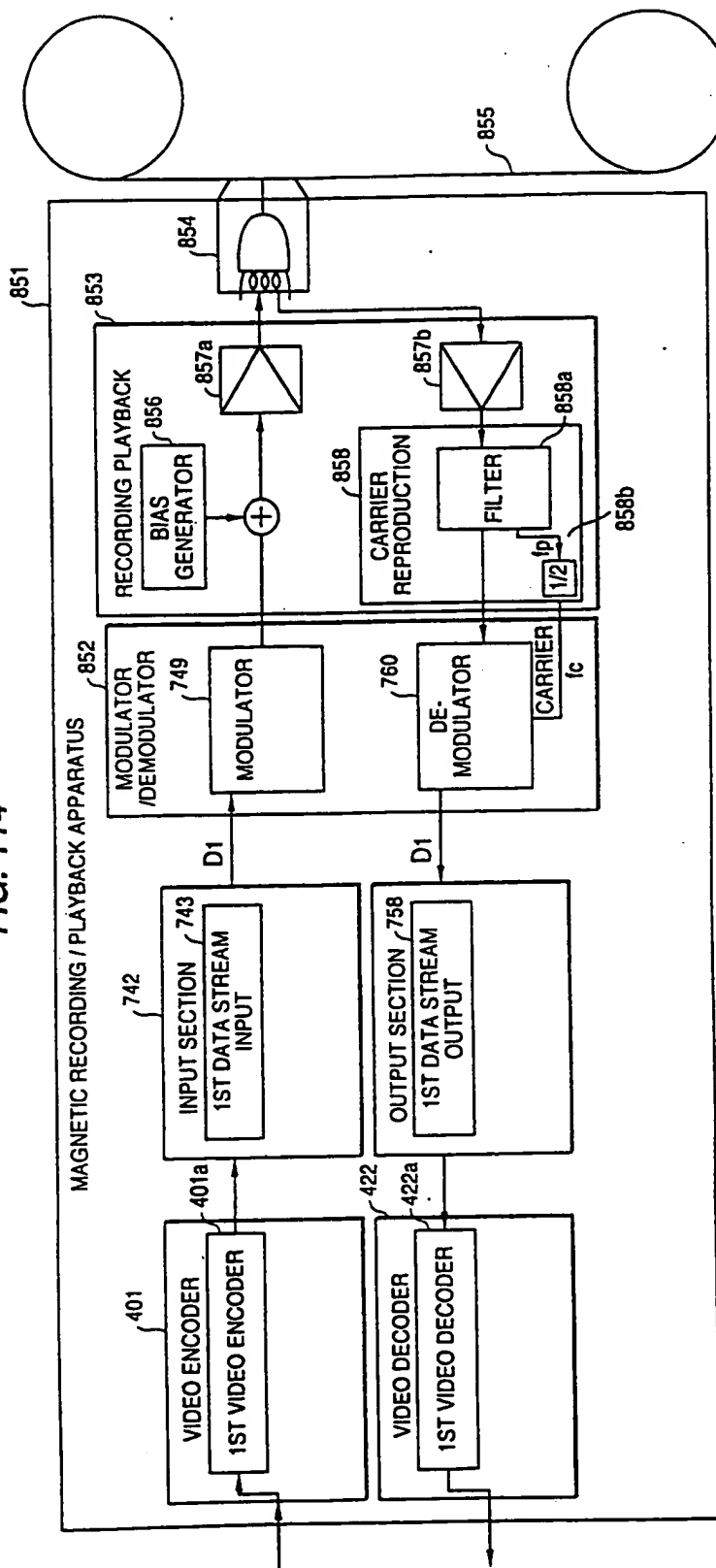


FIG. 115

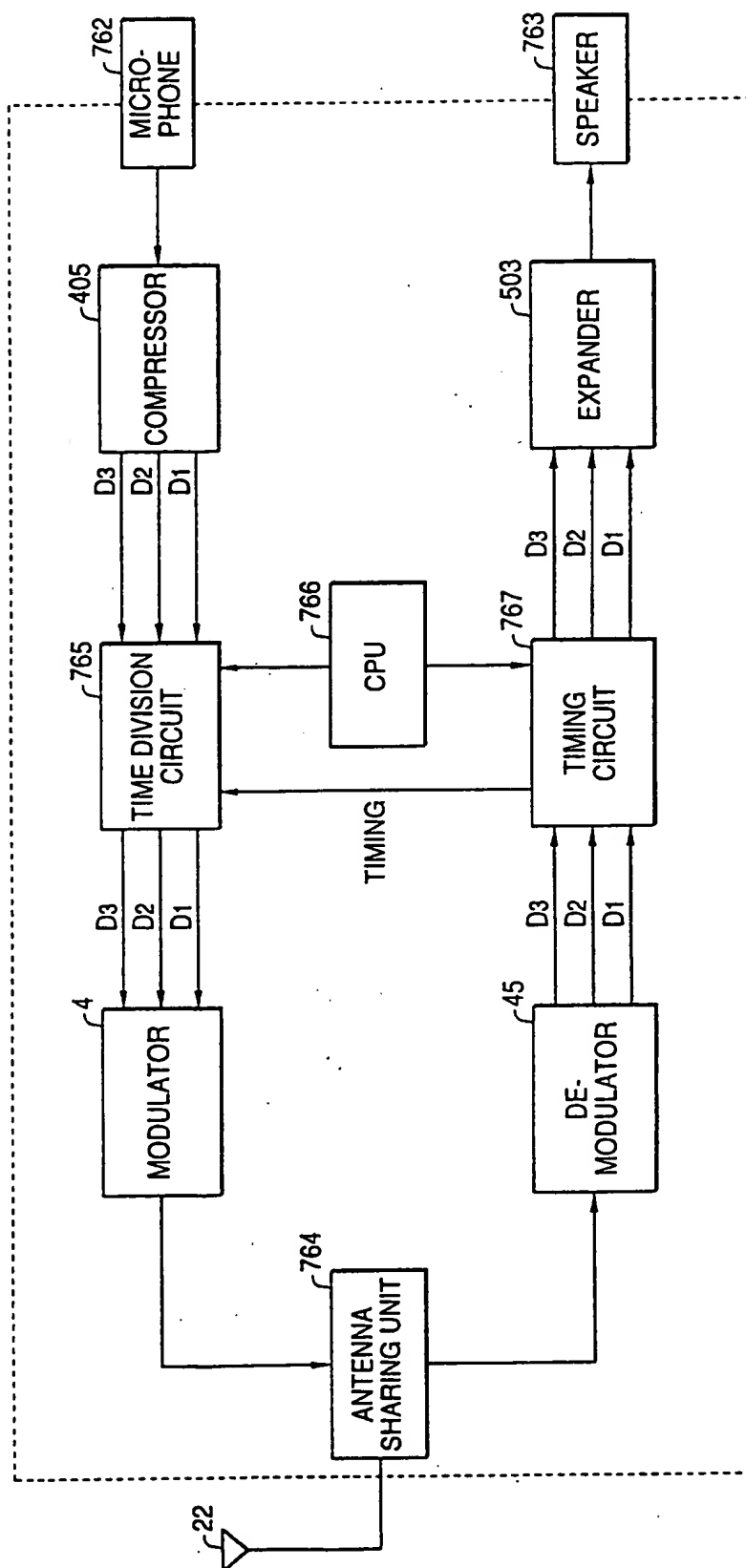


FIG. 116

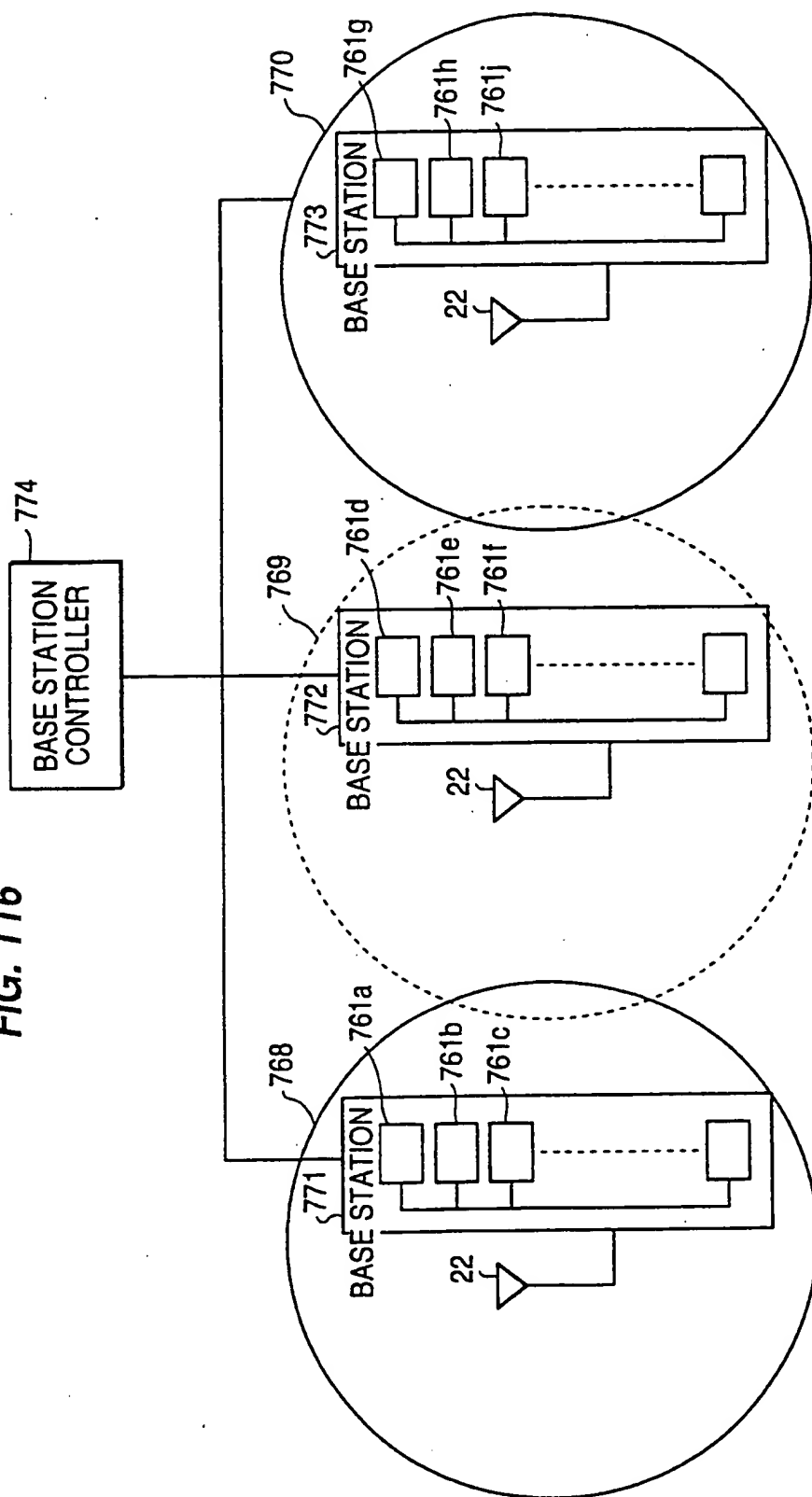
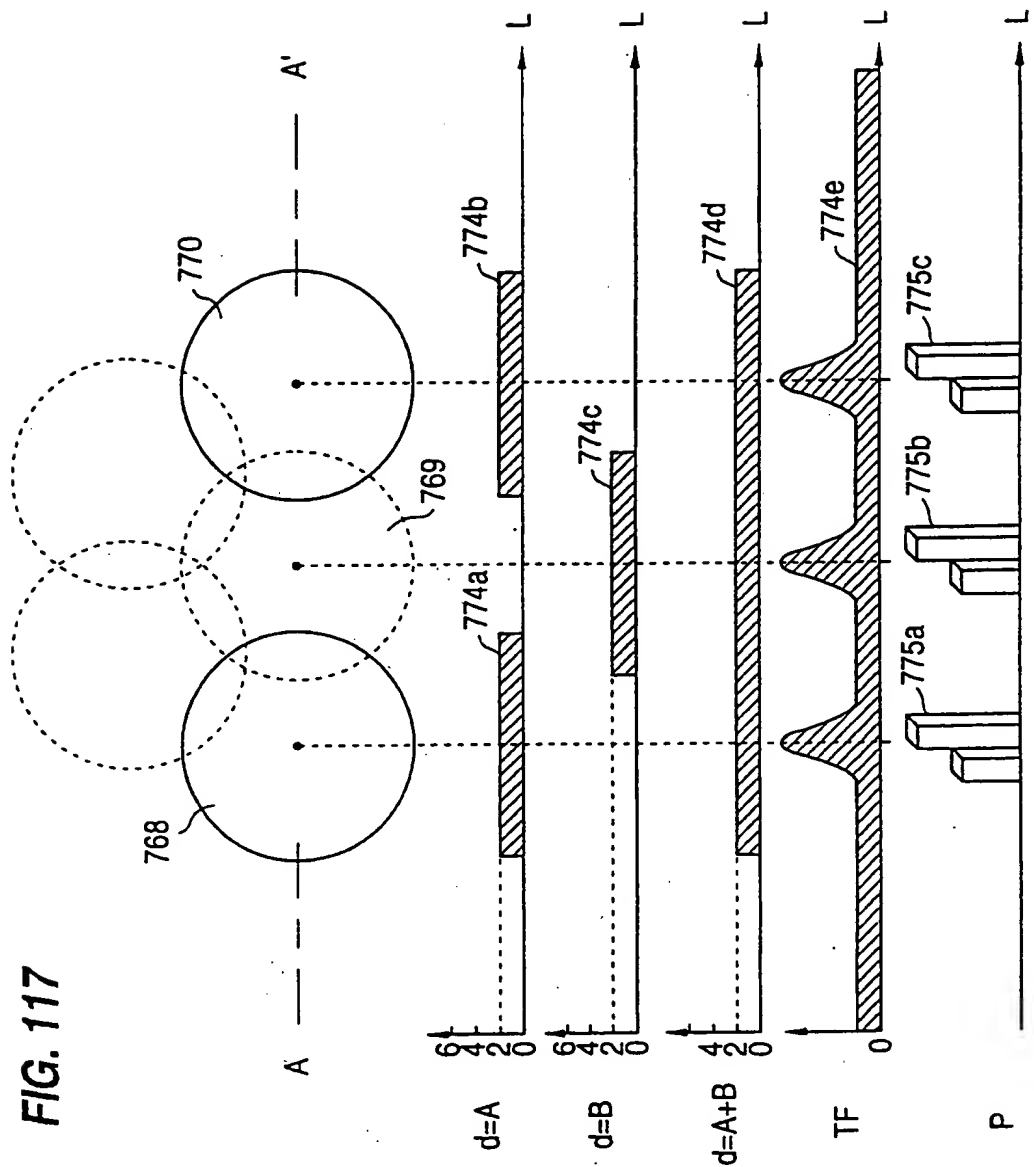
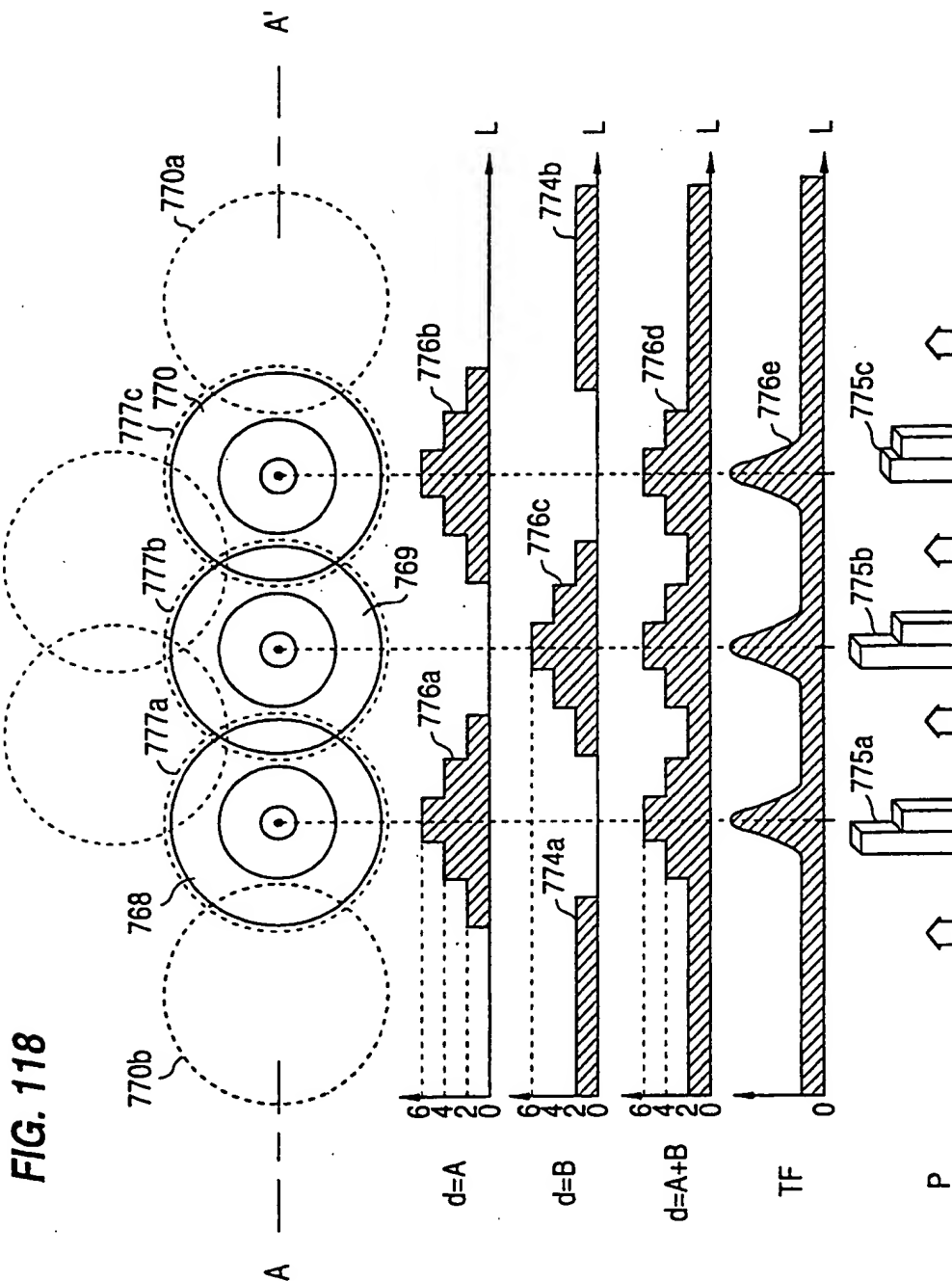
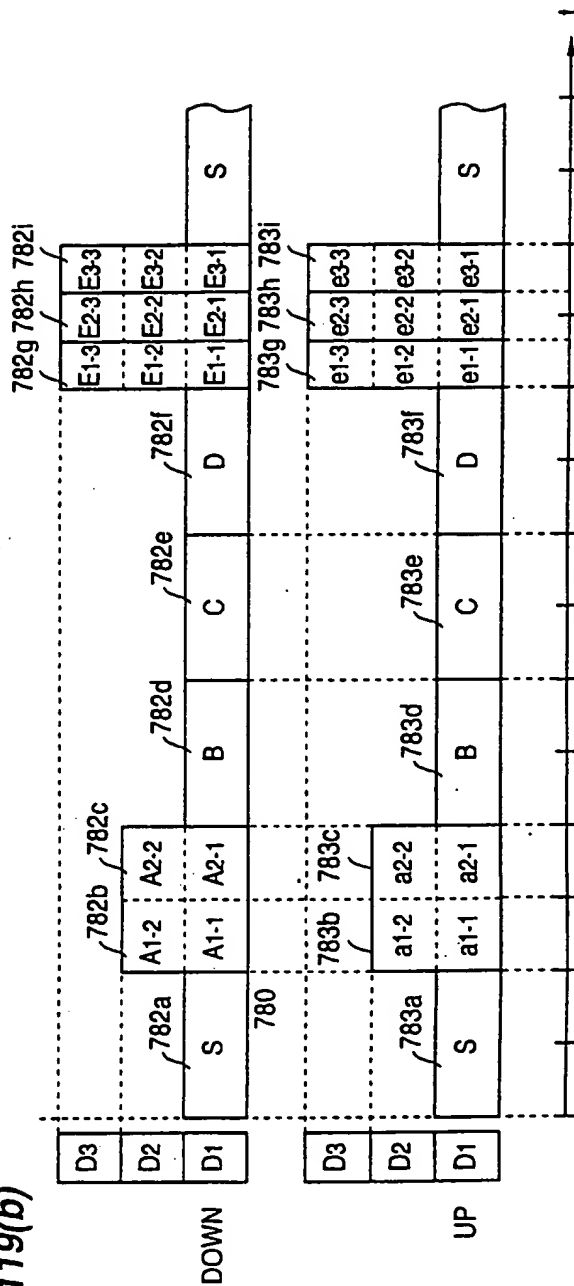


FIG. 117







785	S	786a	A	787a	a	786b	B	787b	b	786c	C	787c	c	786d	D	787d	d	786e	E	787e	e	786f	F	787f	f	S
-----	---	------	---	------	---	------	---	------	---	------	---	------	---	------	---	------	---	------	---	------	---	------	---	------	---	---

FIG. 120(b)

FIG. 121

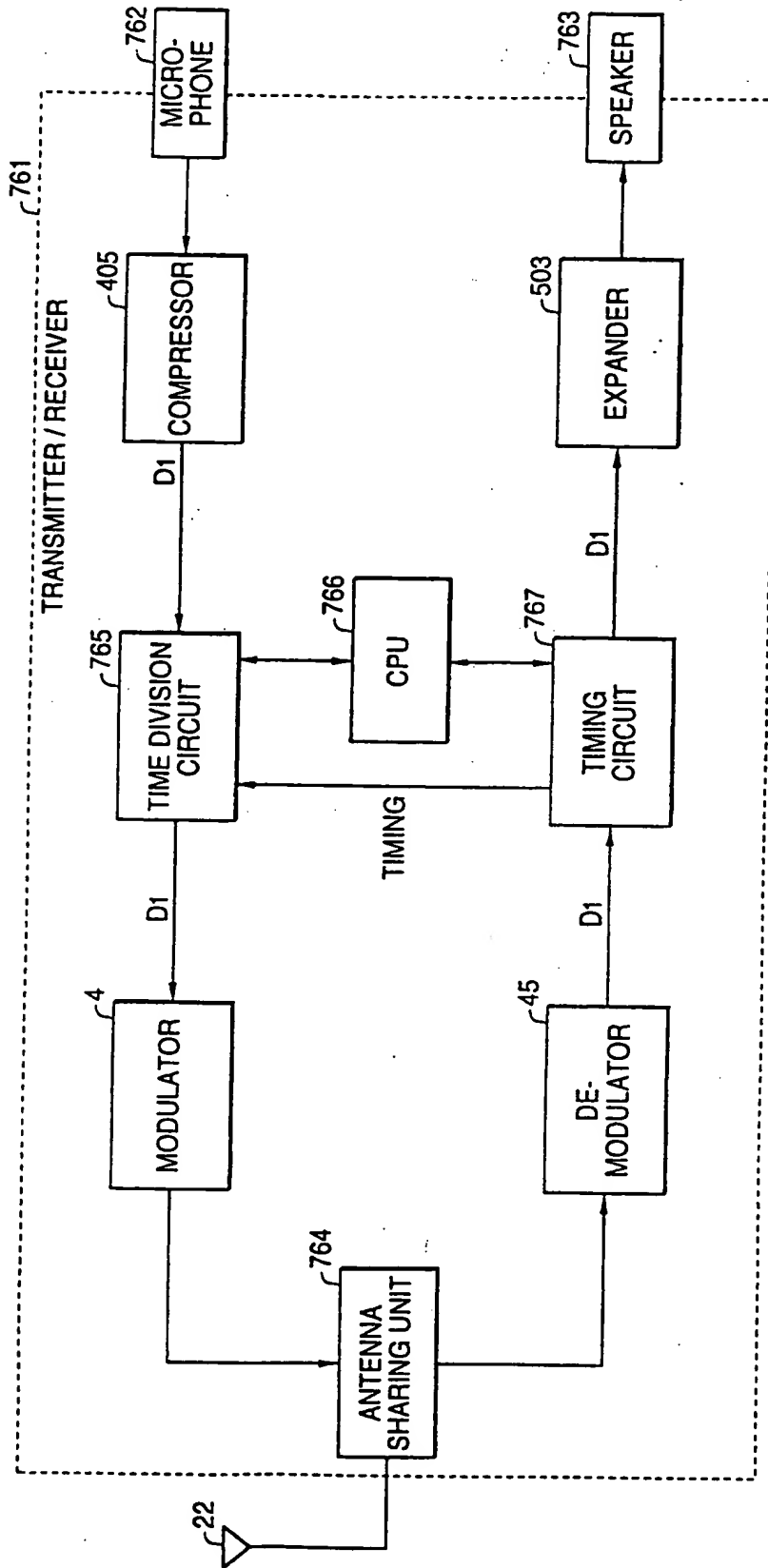


FIG. 122

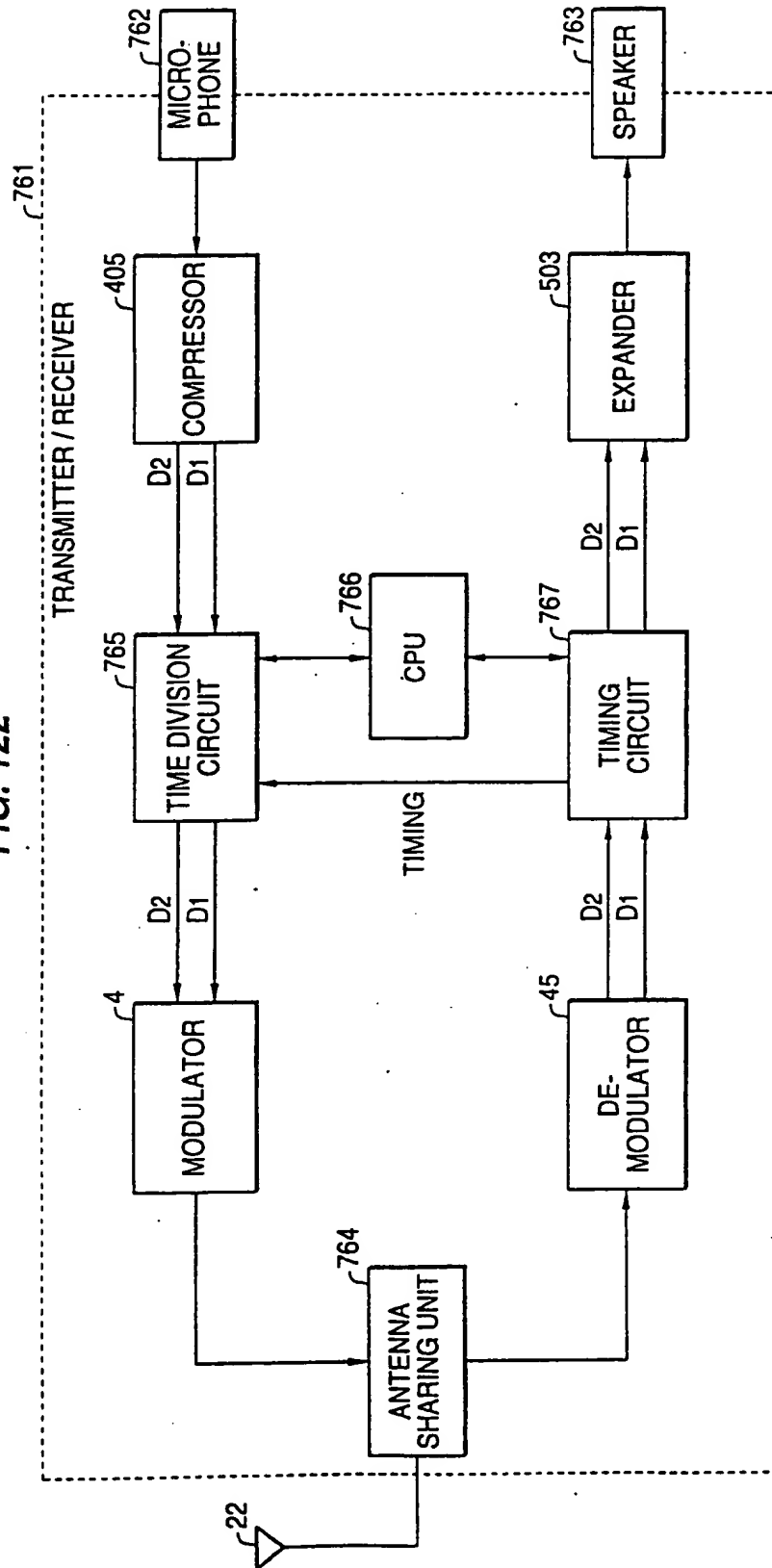
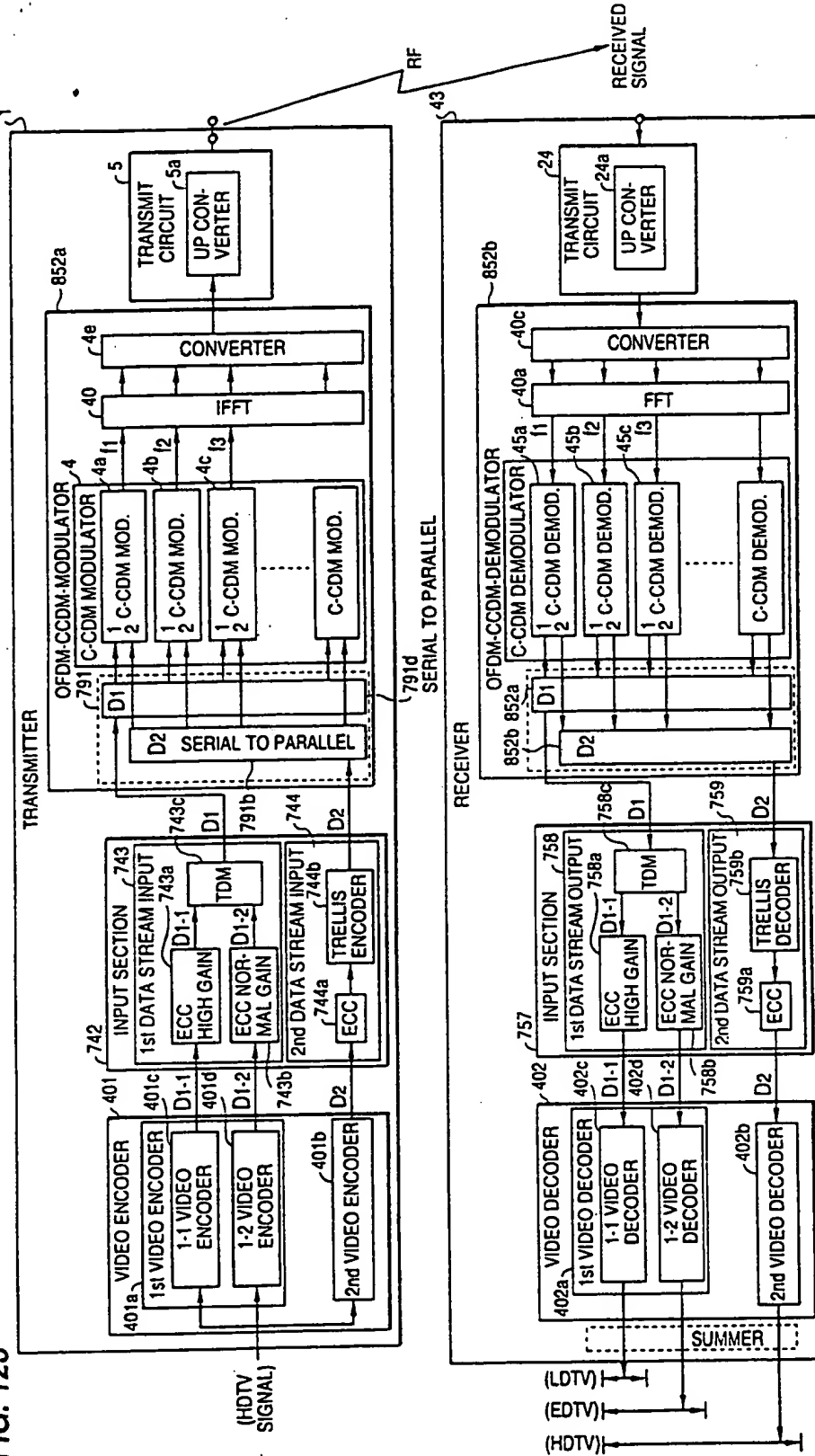


FIG. 123



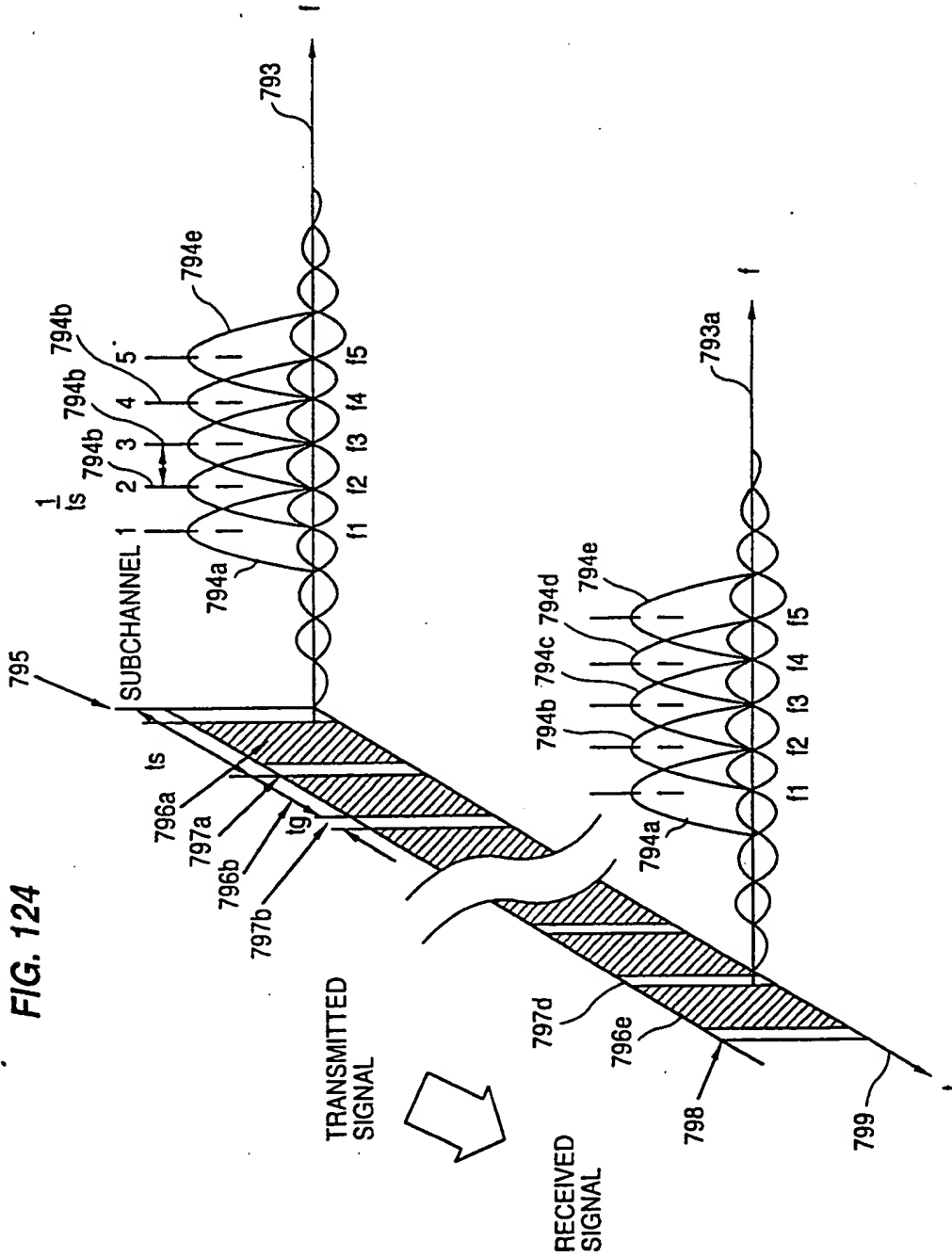


FIG. 125(a)

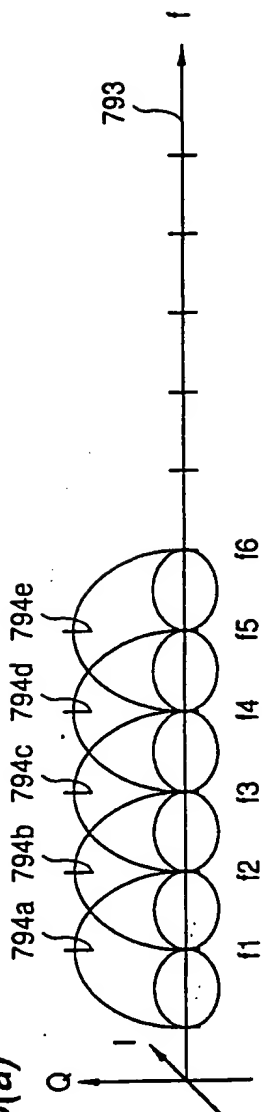
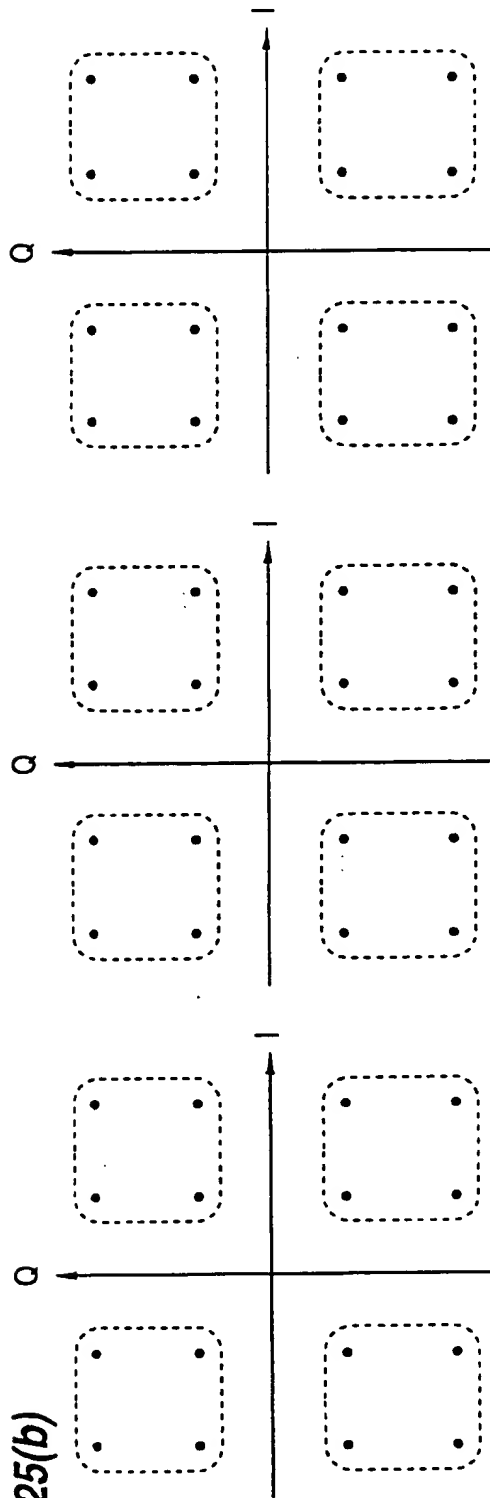


FIG. 125(b)



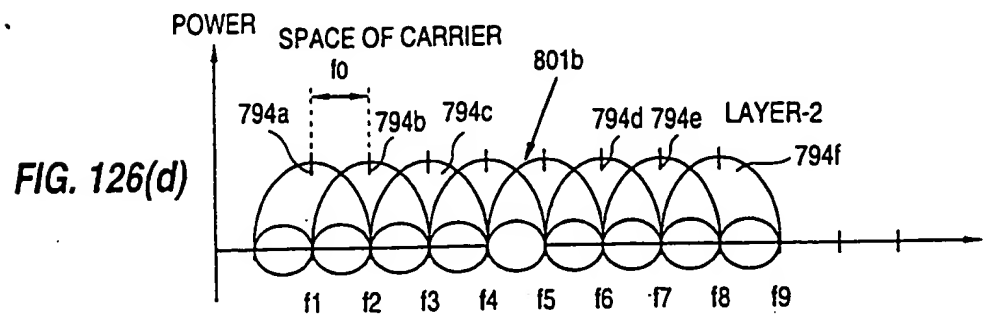
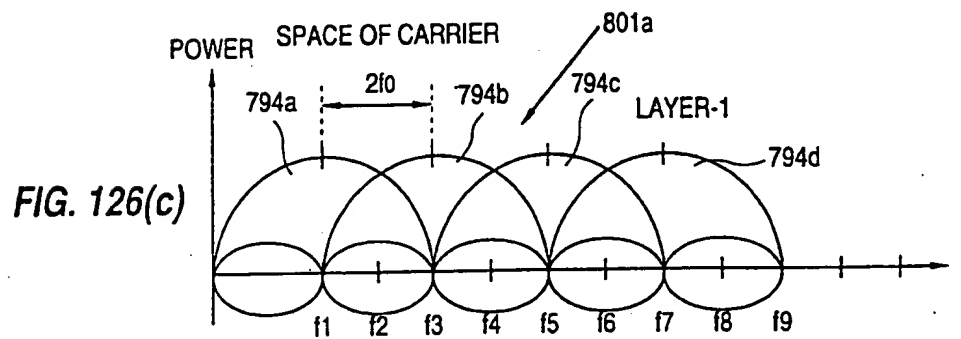
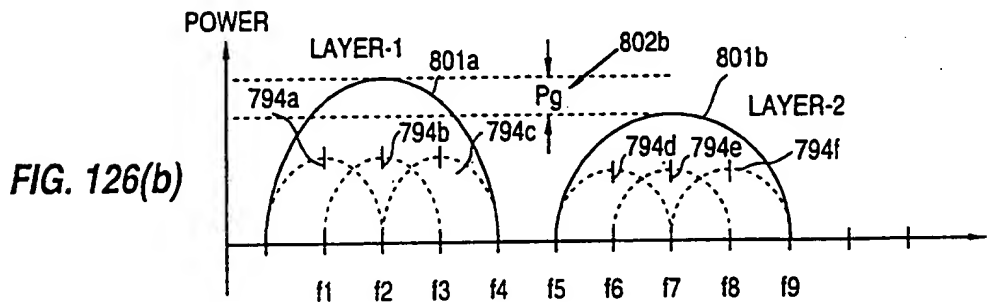
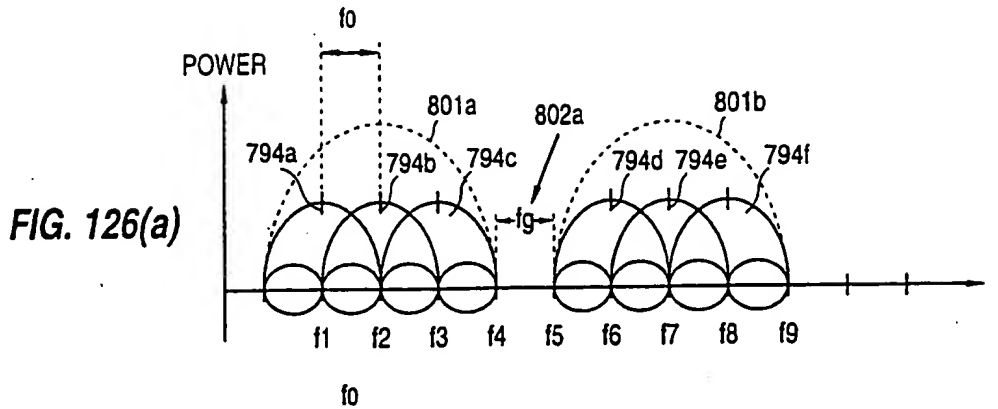


FIG. 127

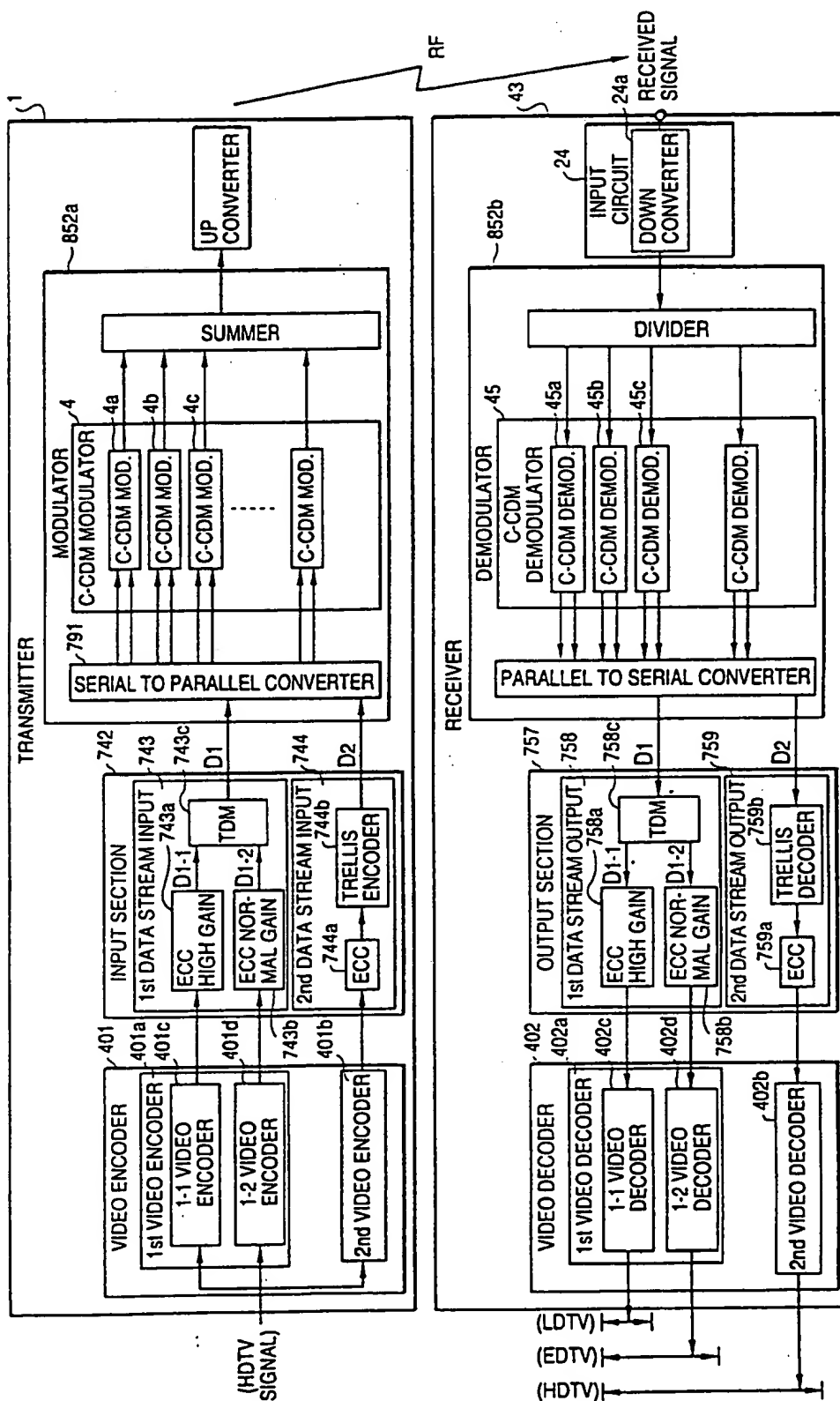


FIG. 128(a)

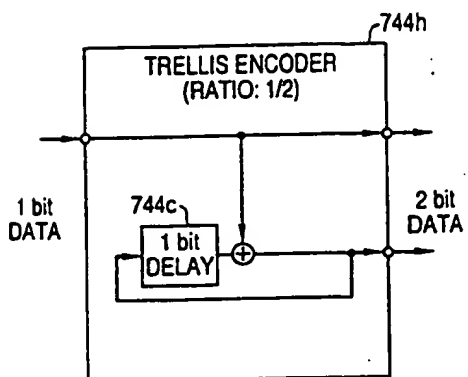


FIG. 128(d)

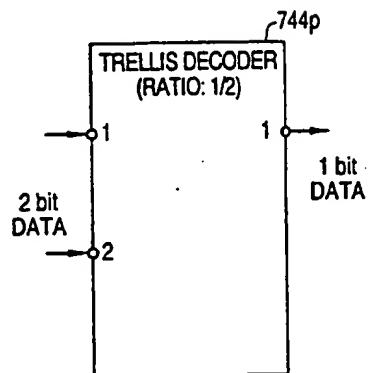


FIG. 128(b)

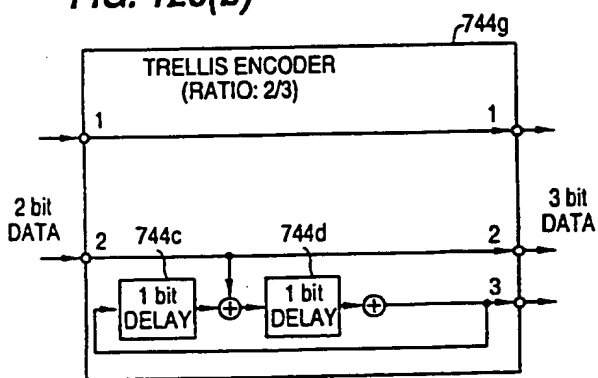


FIG. 128(e)

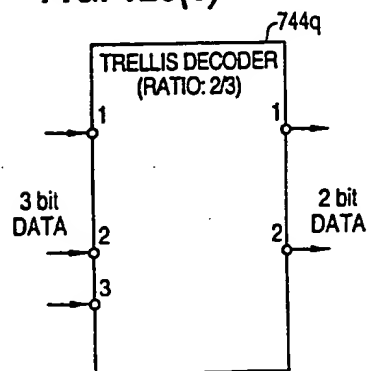


FIG. 128(c)

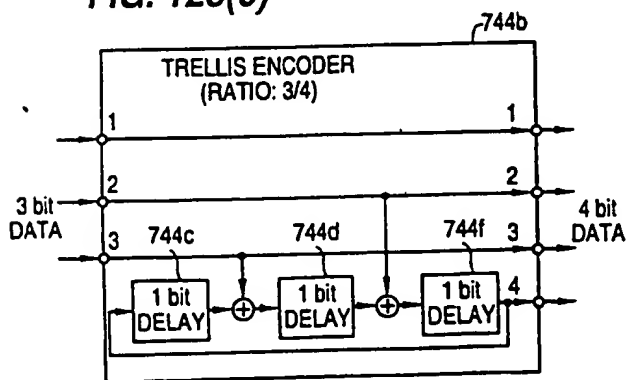


FIG. 128(f)

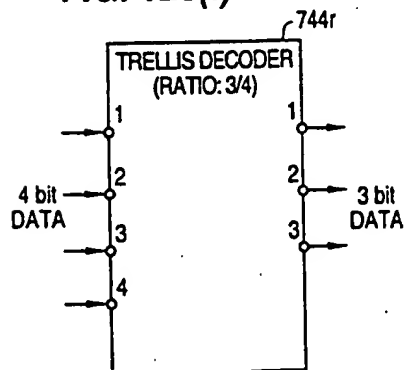


FIG. 129

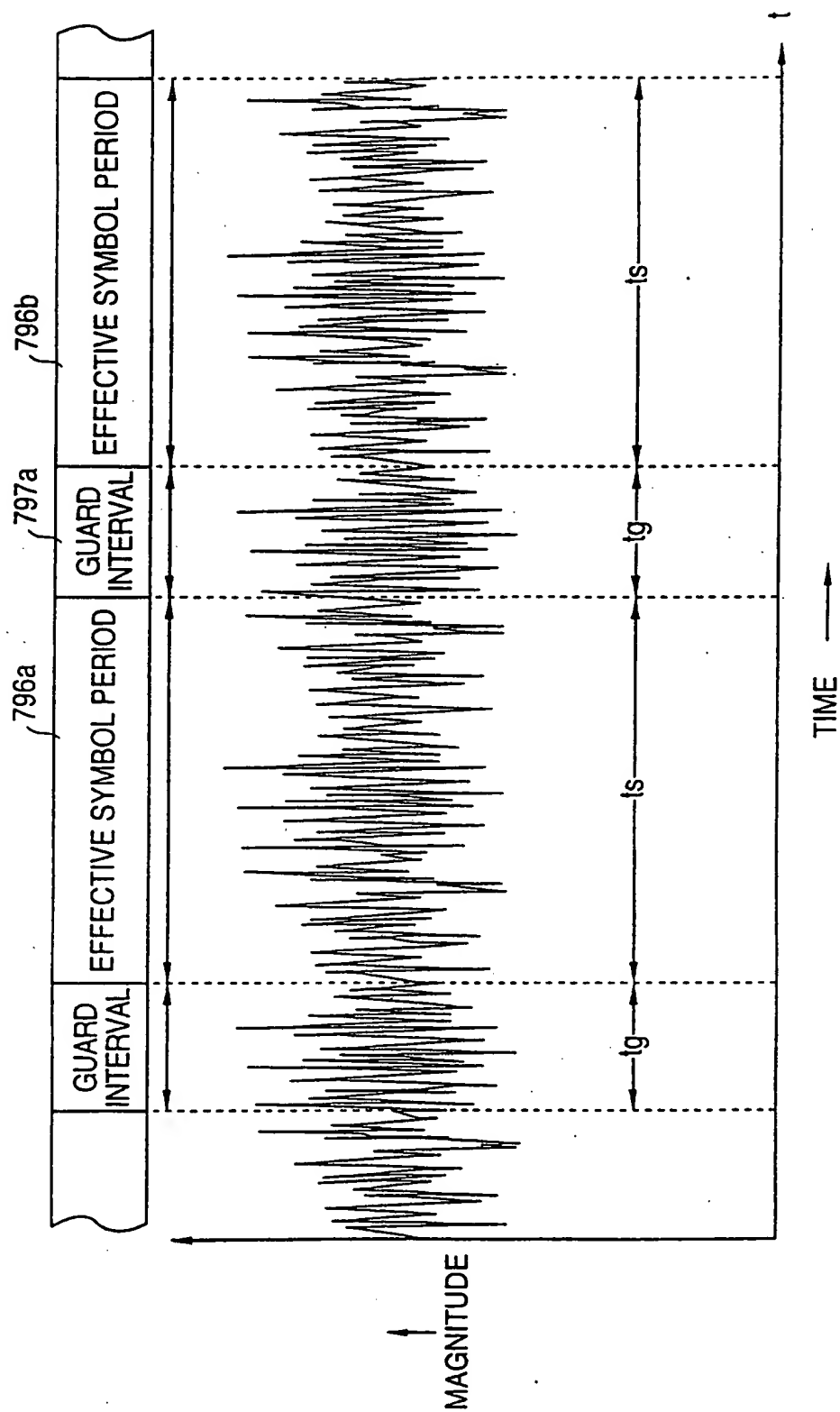


FIG. 130

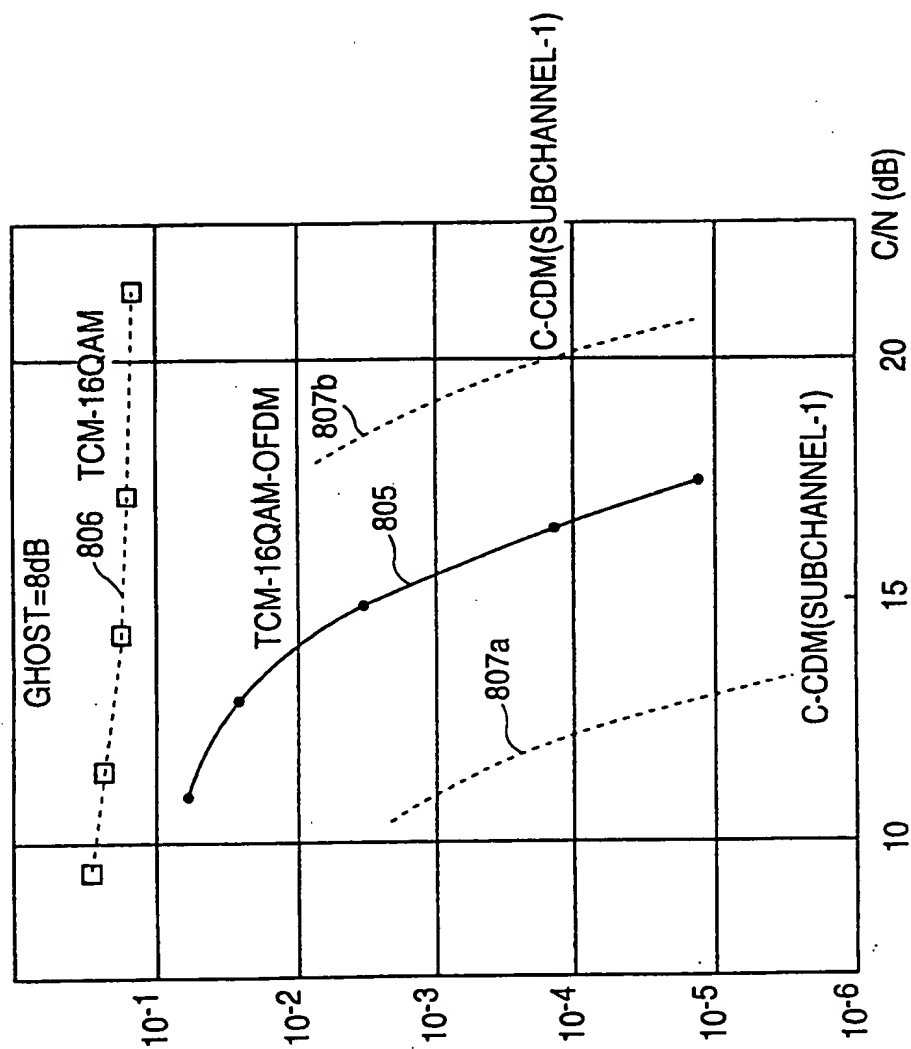
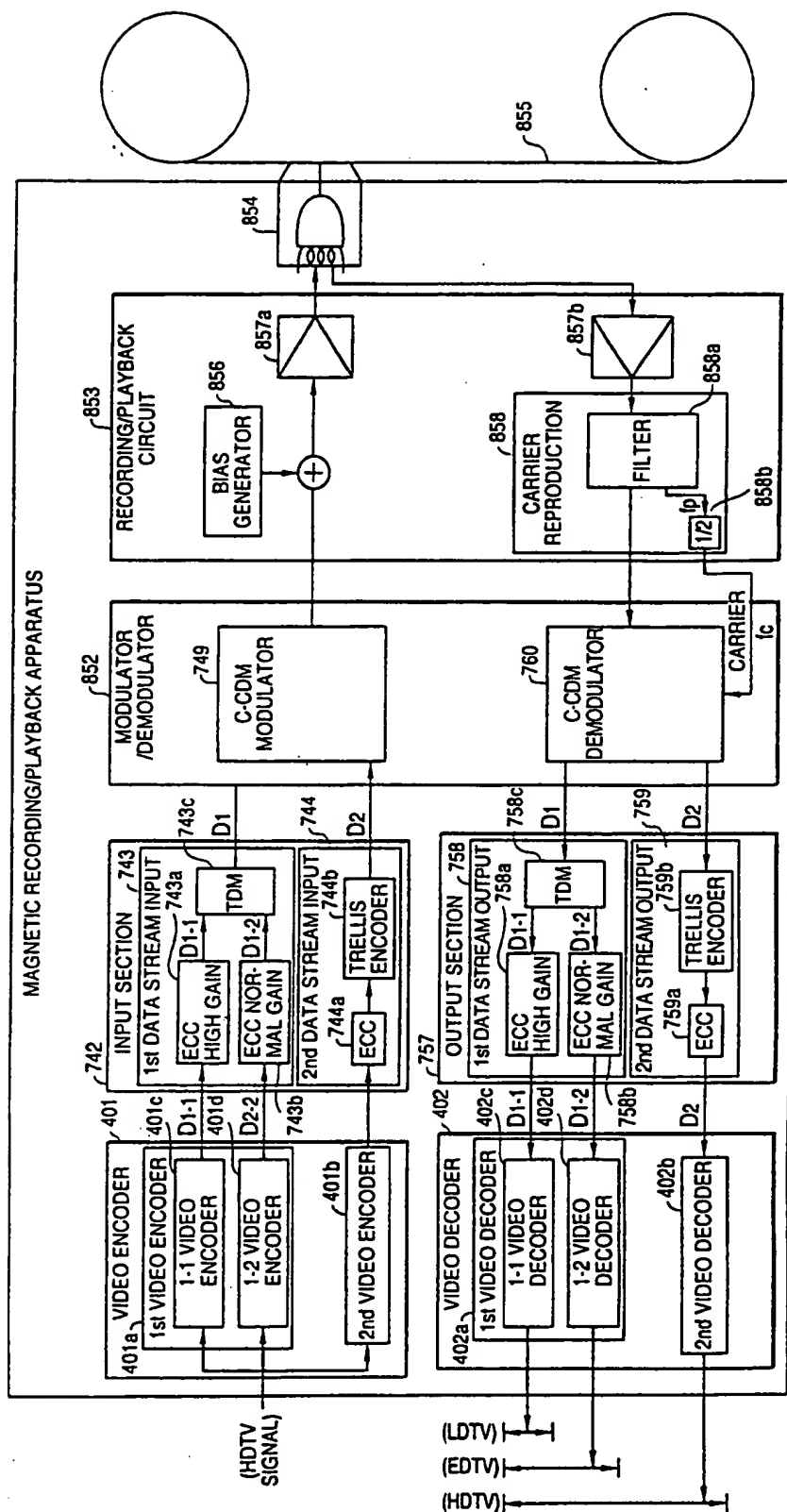


FIG. 131



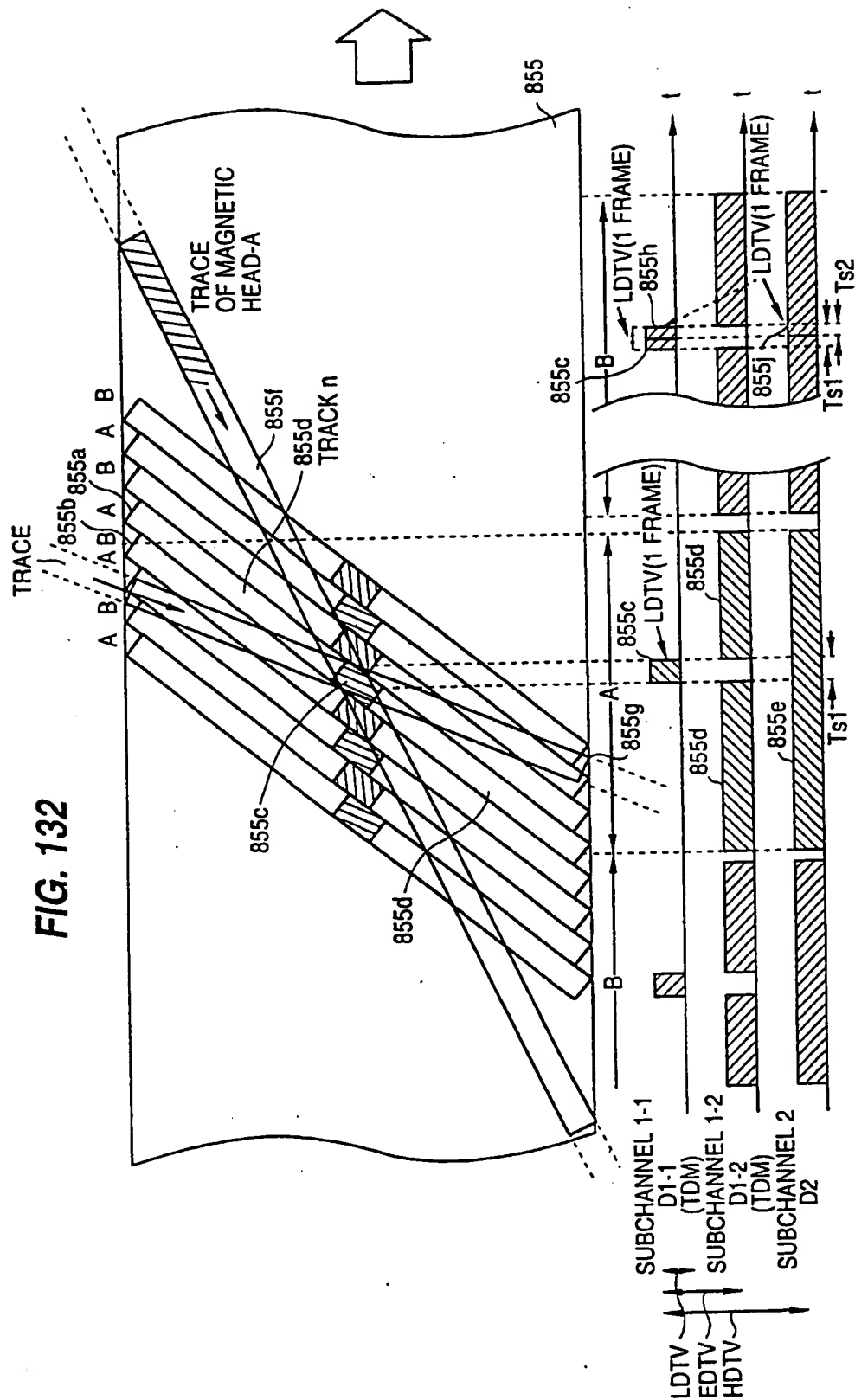


FIG. 133

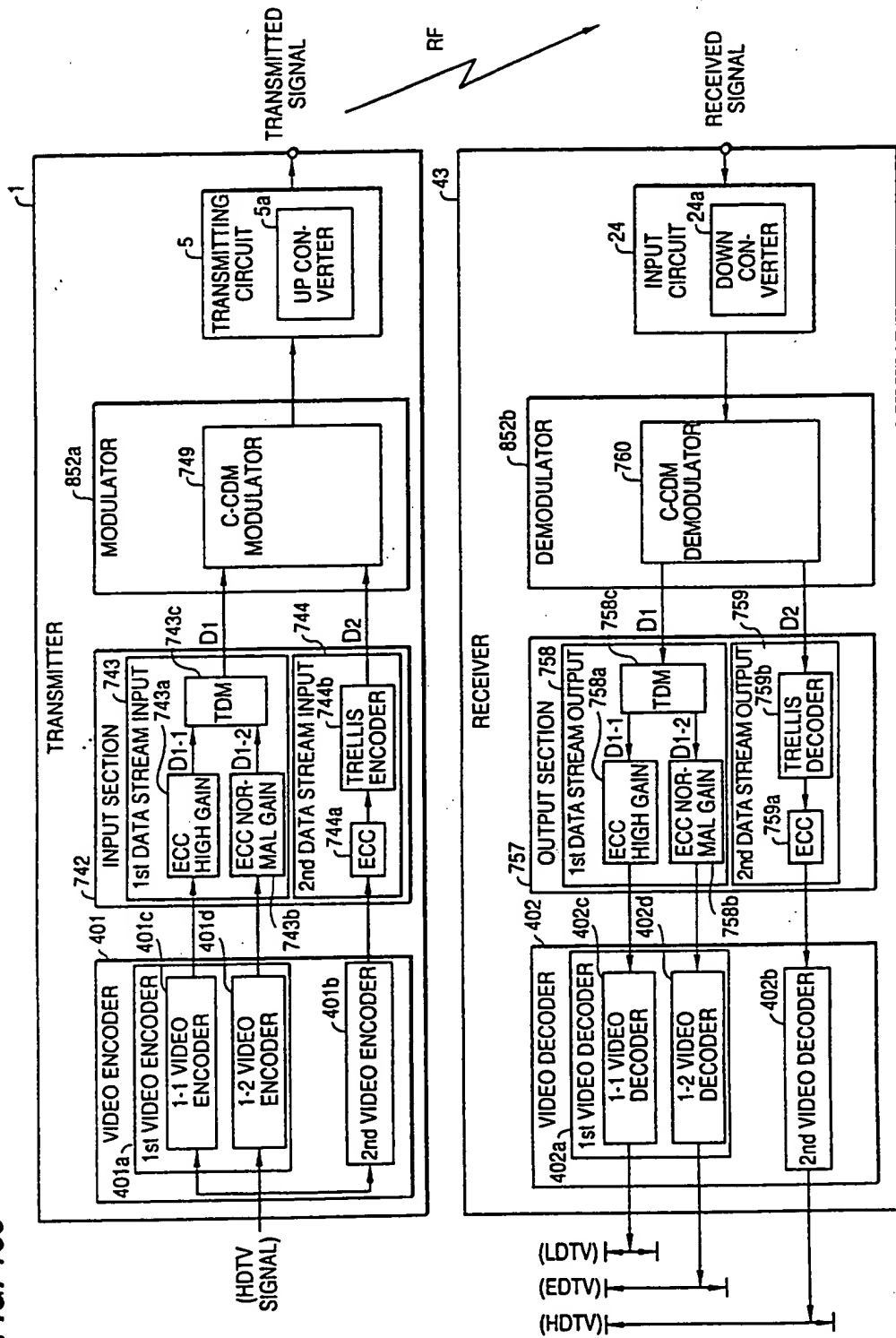


FIG. 134

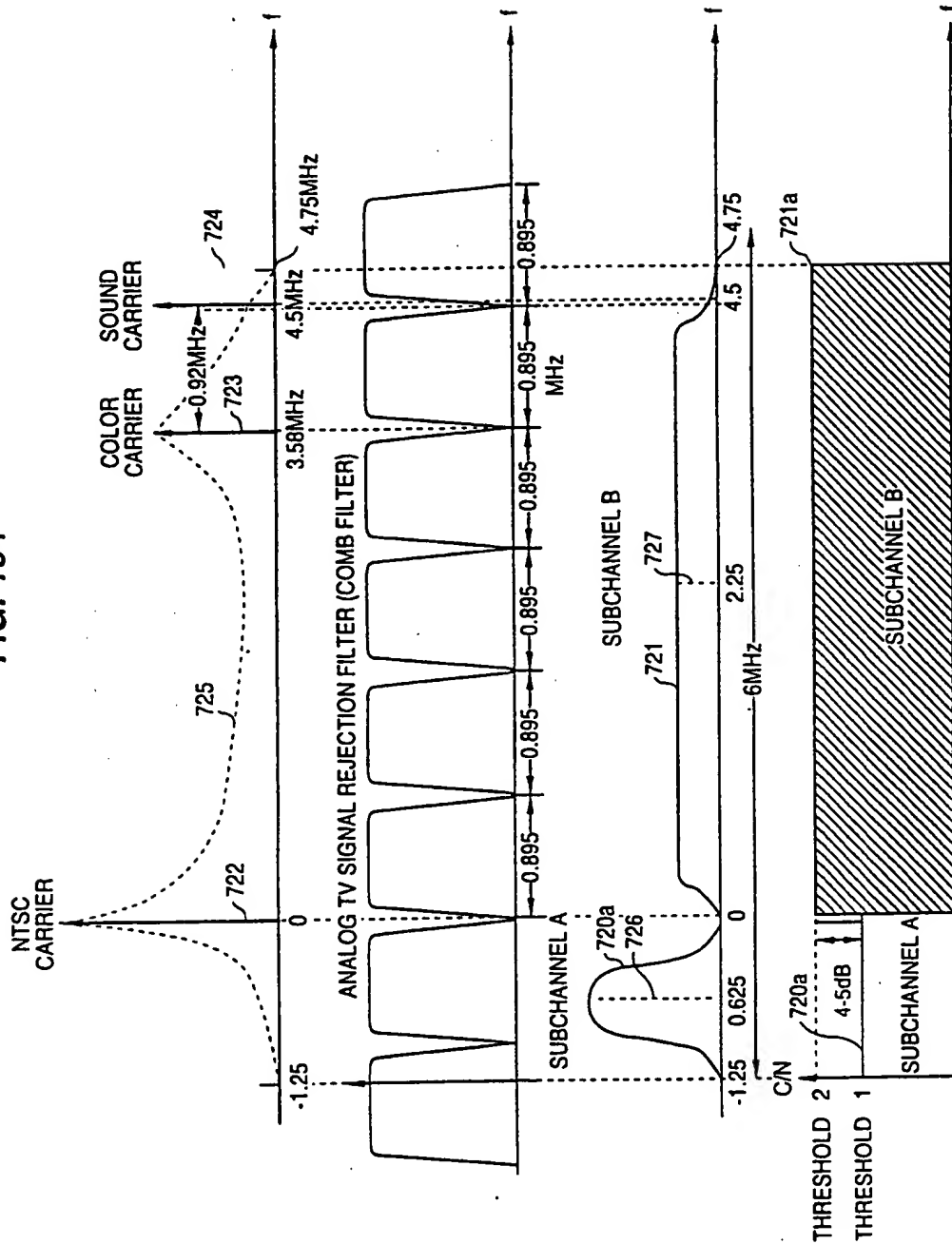


FIG. 135

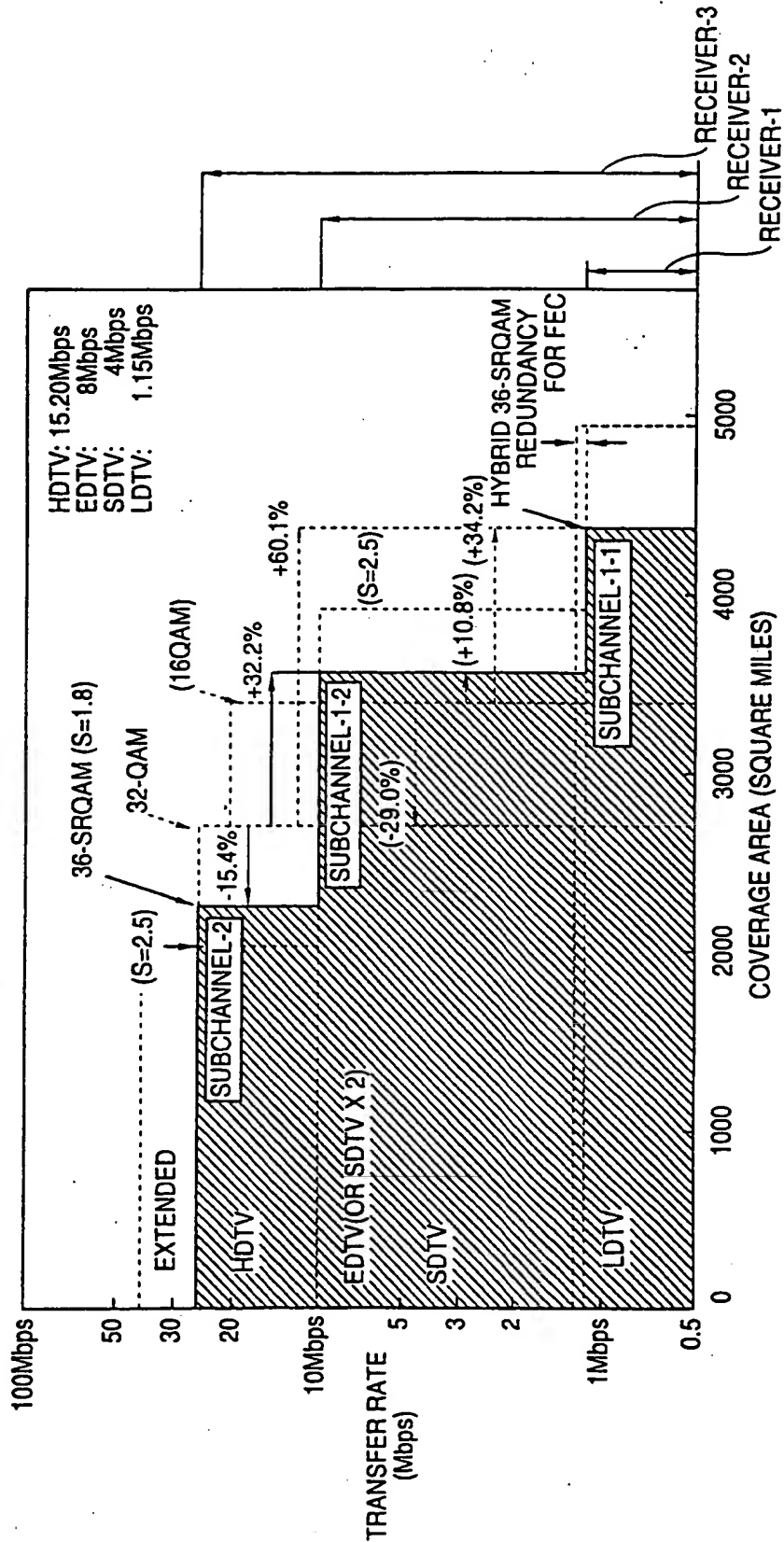


FIG. 136

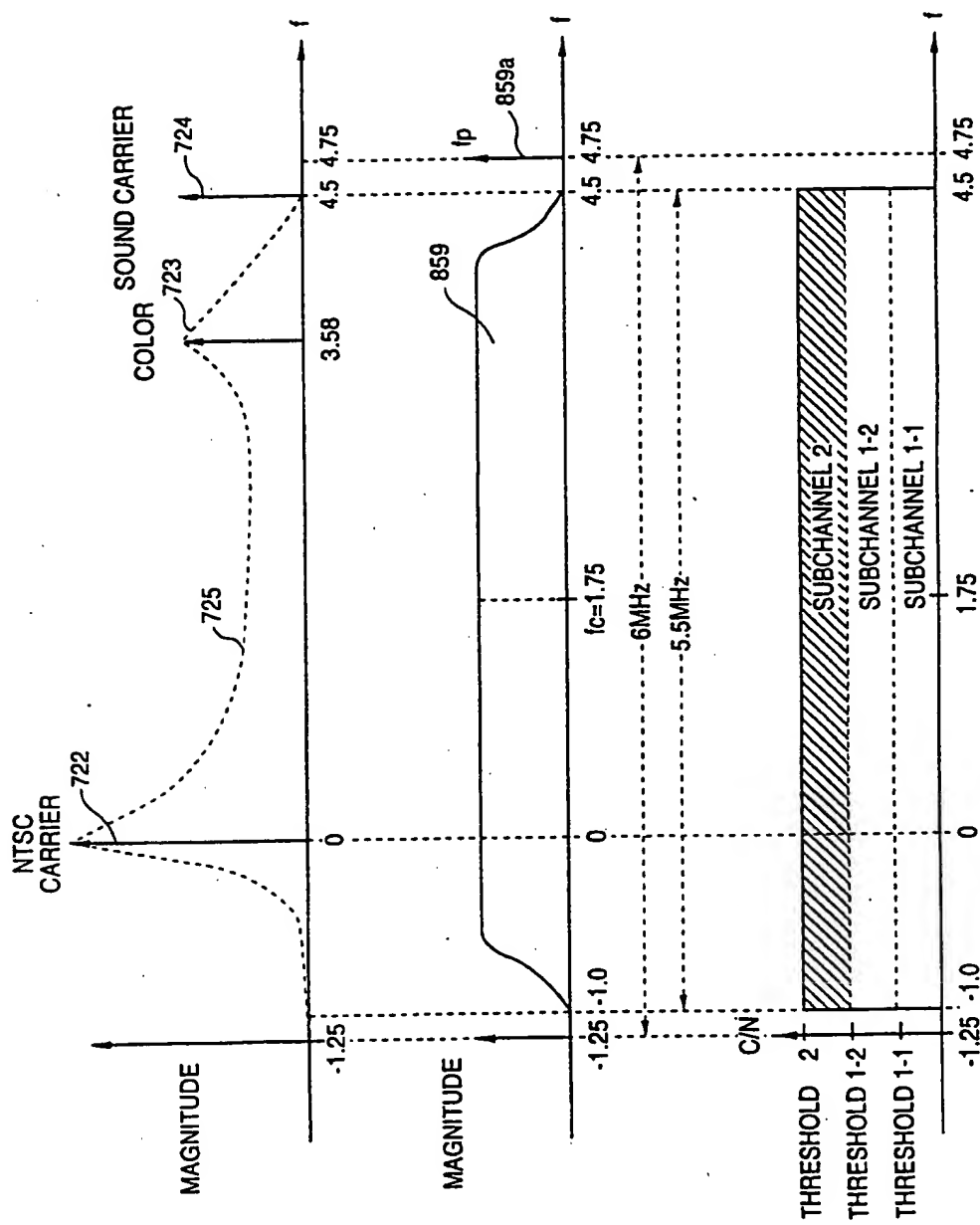


FIG. 137

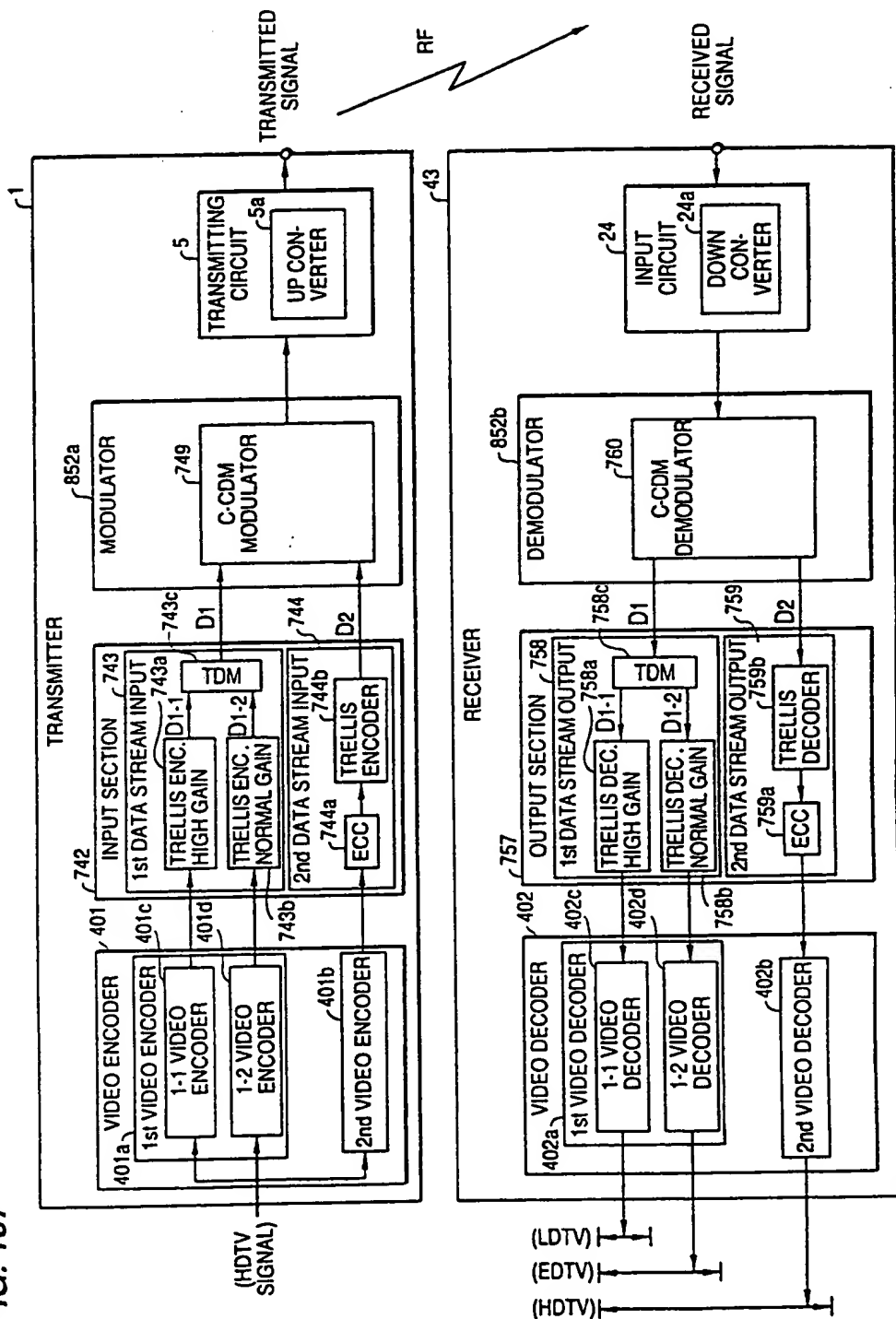


FIG. 138

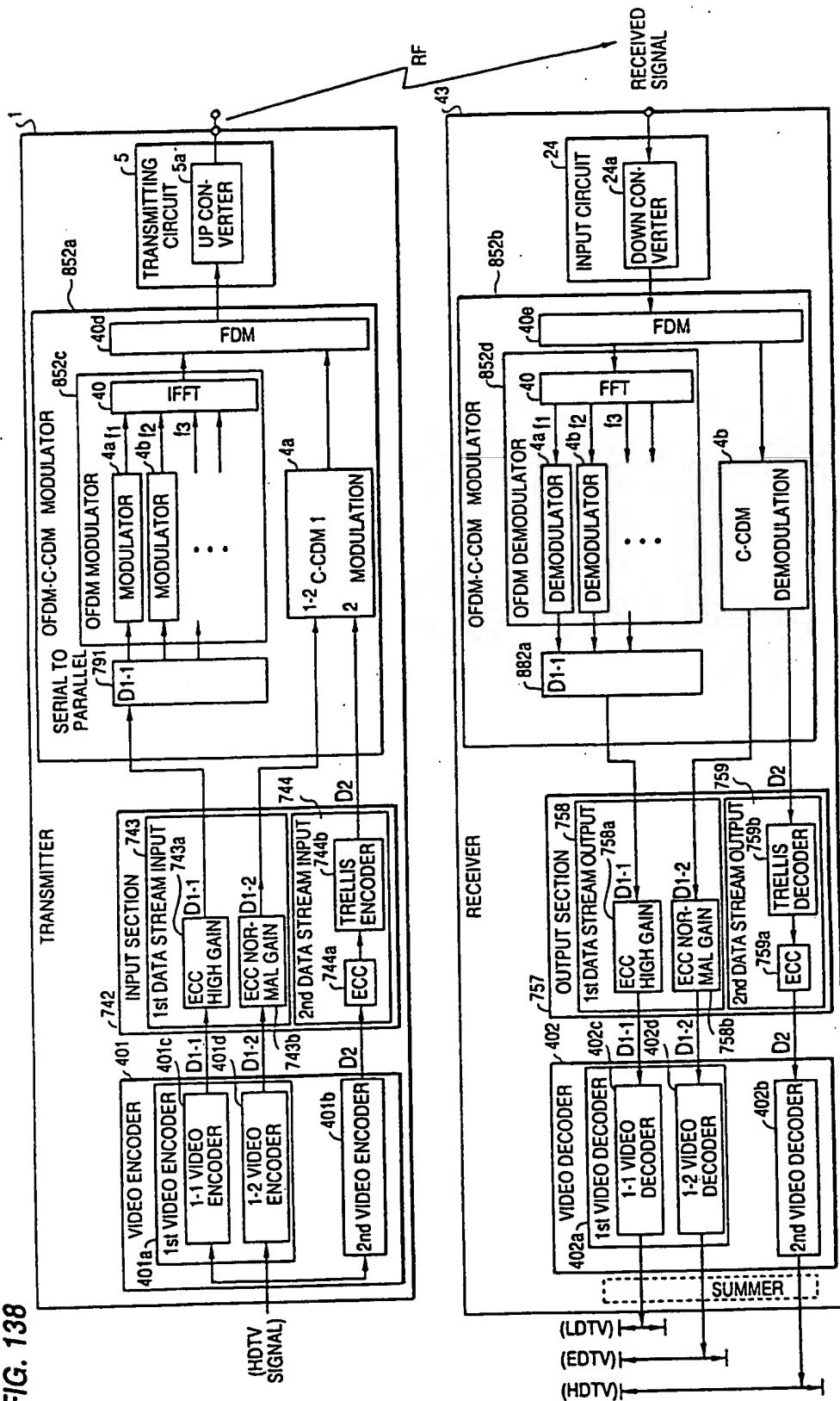


FIG. 139

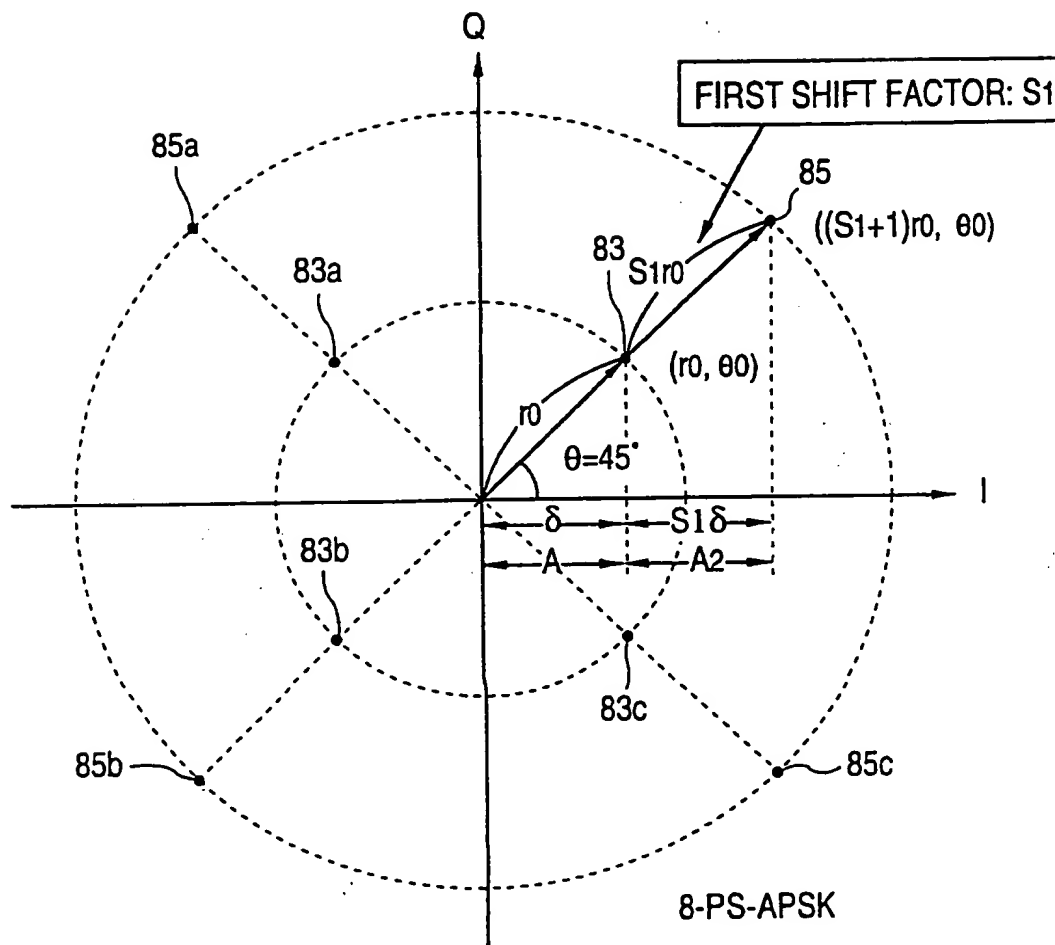


FIG. 140

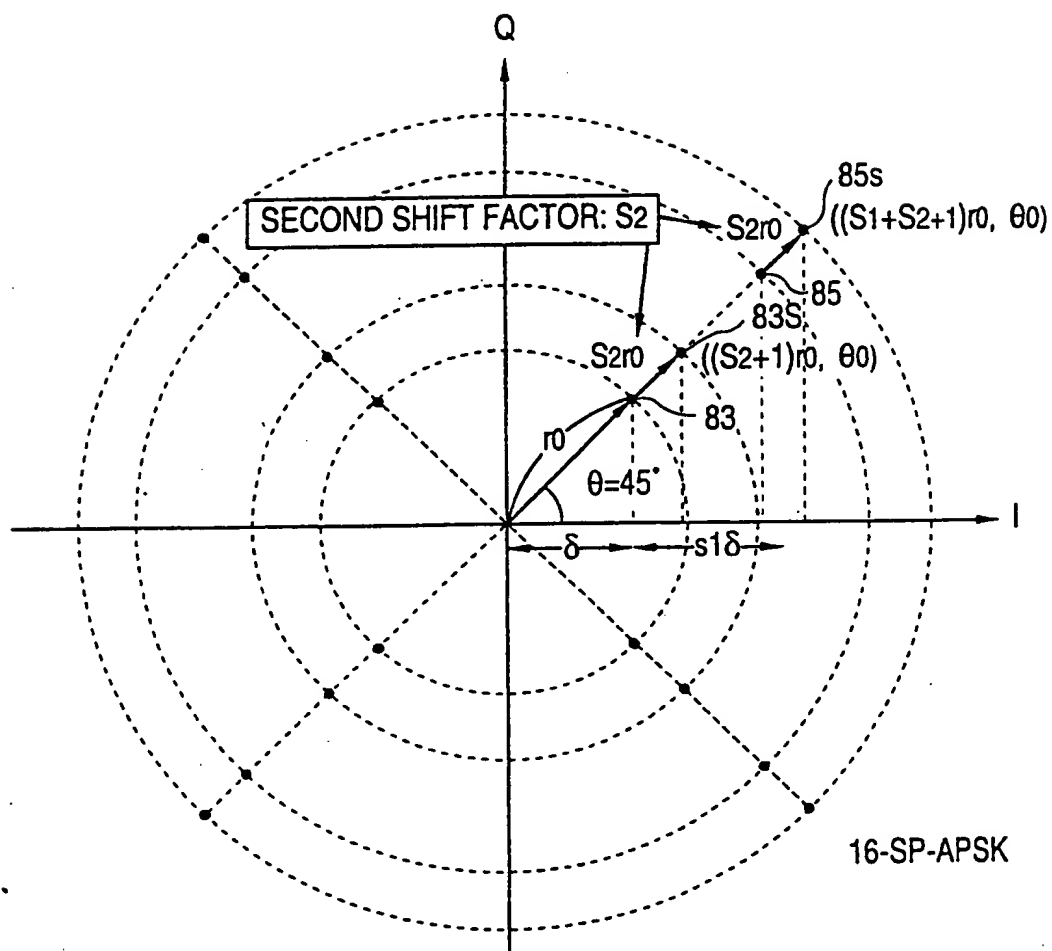


FIG. 141

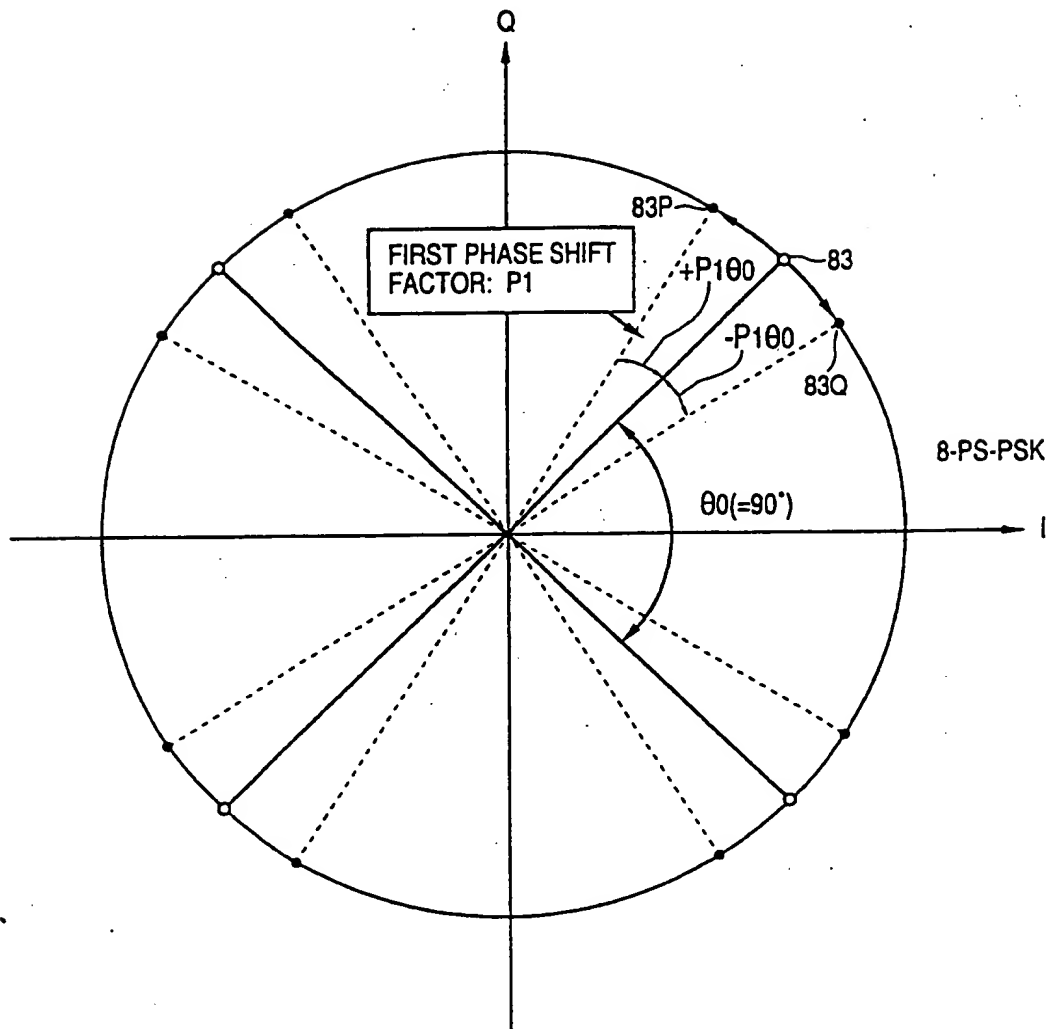


FIG. 142

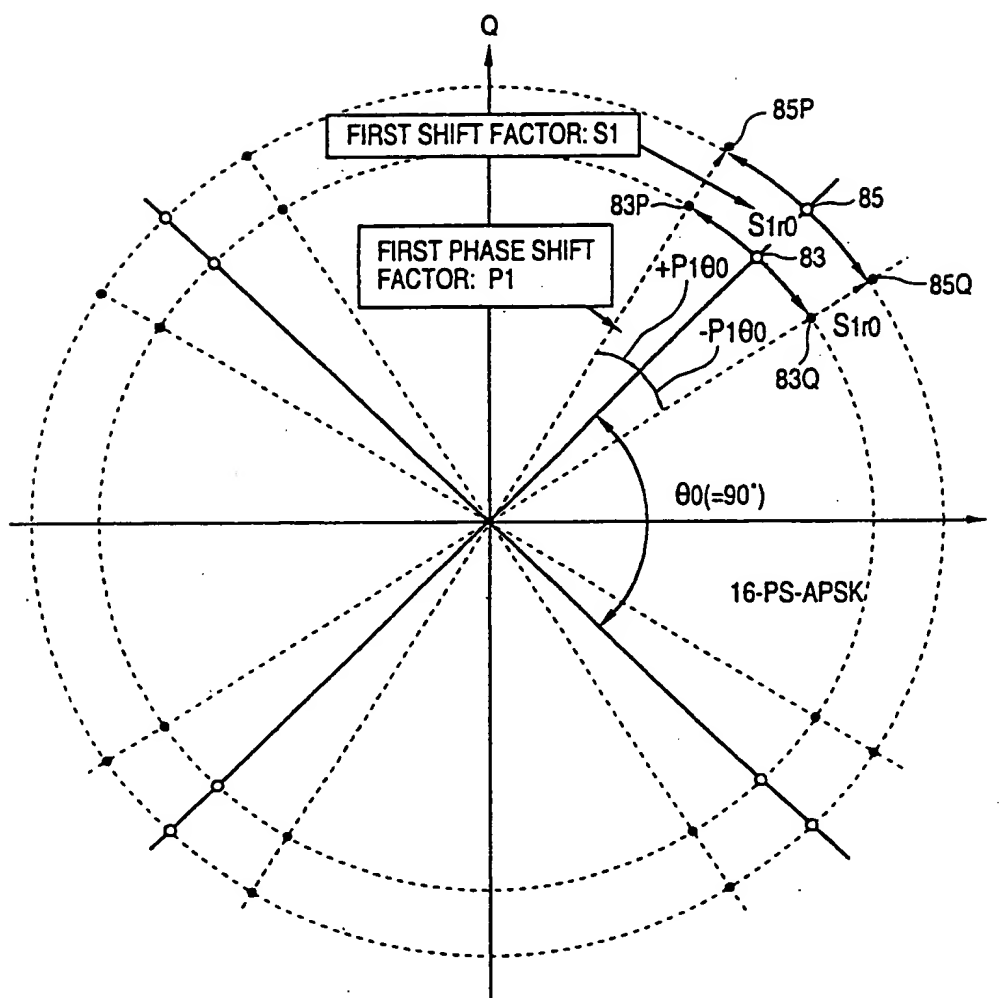


FIG. 143

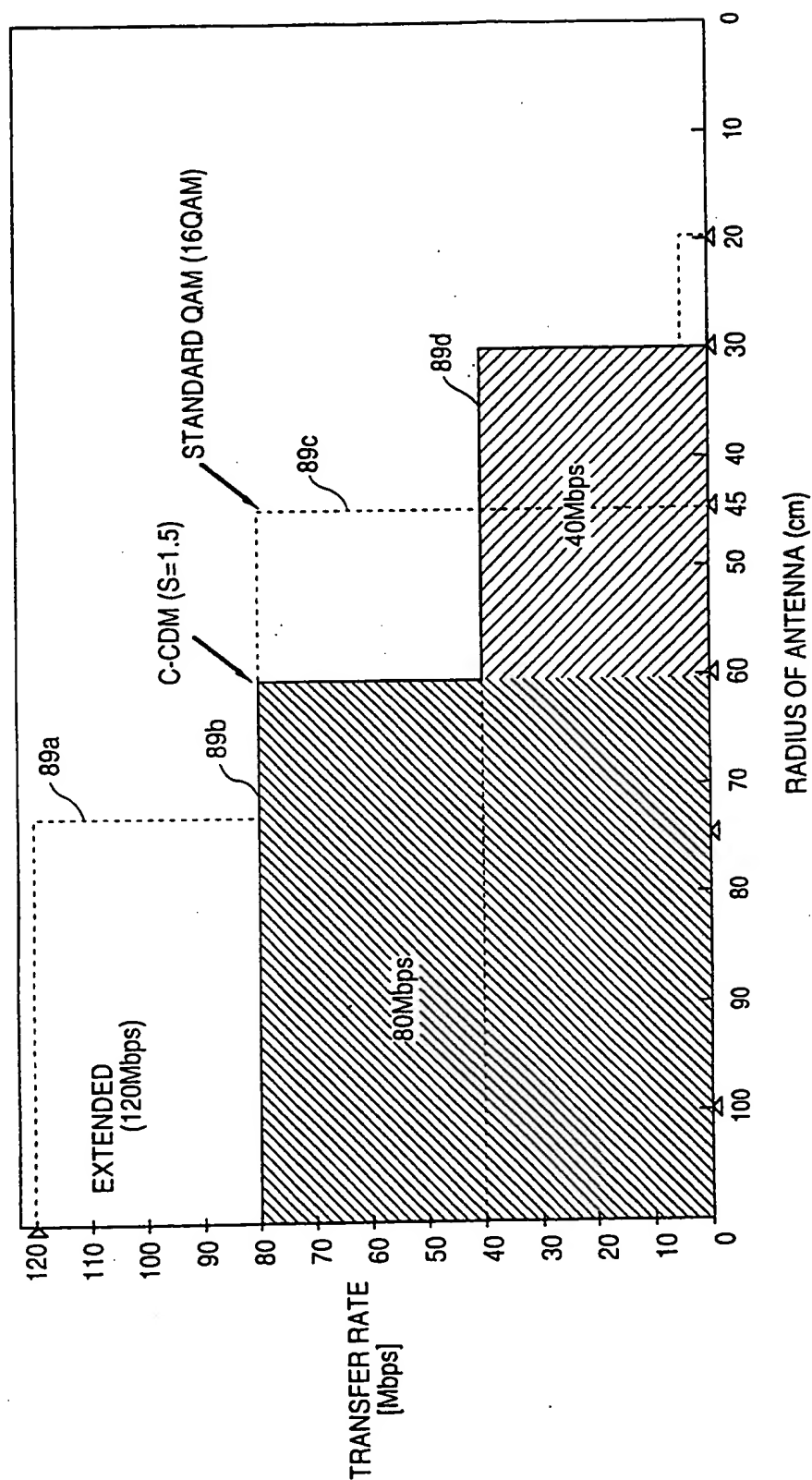
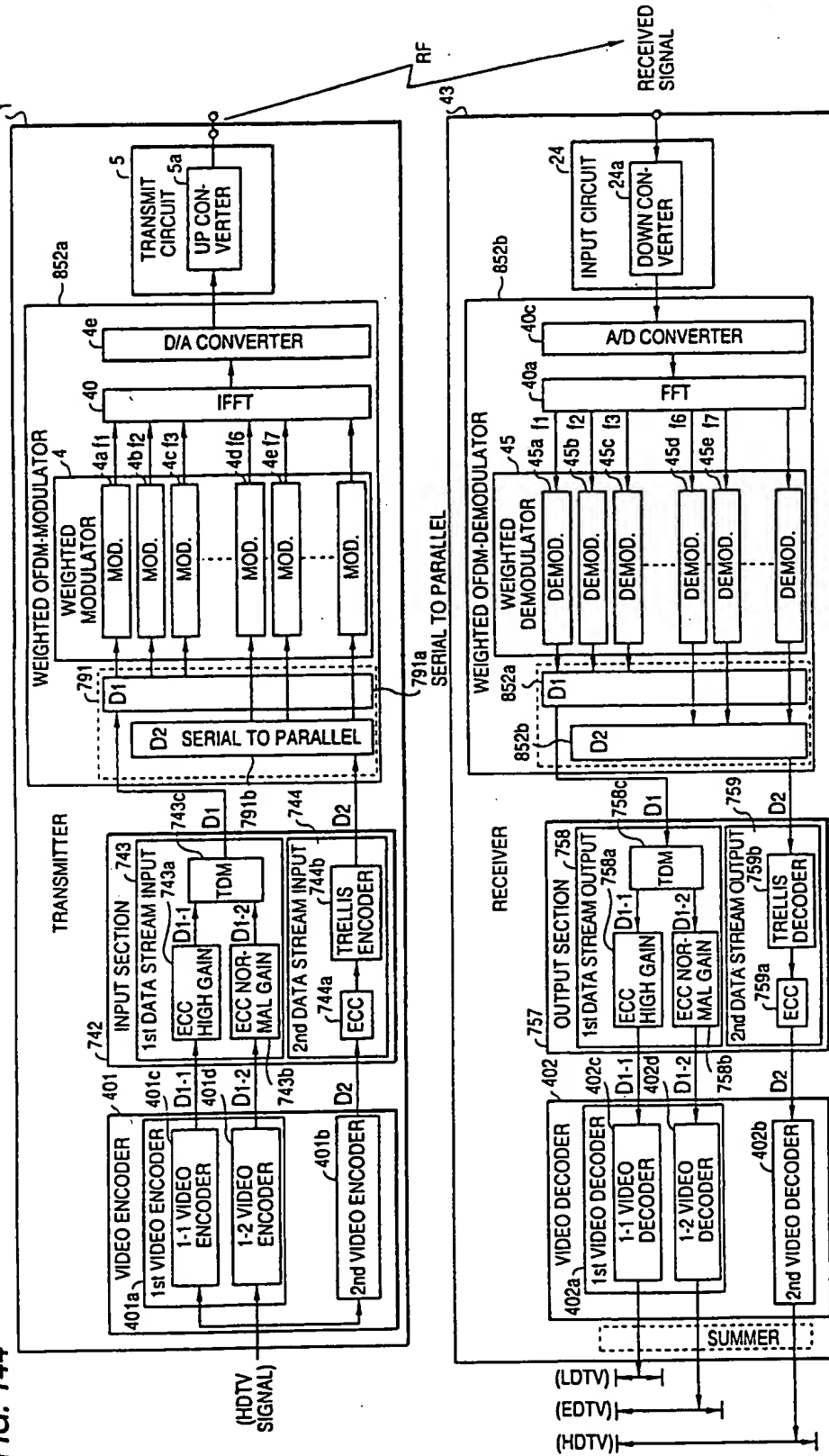
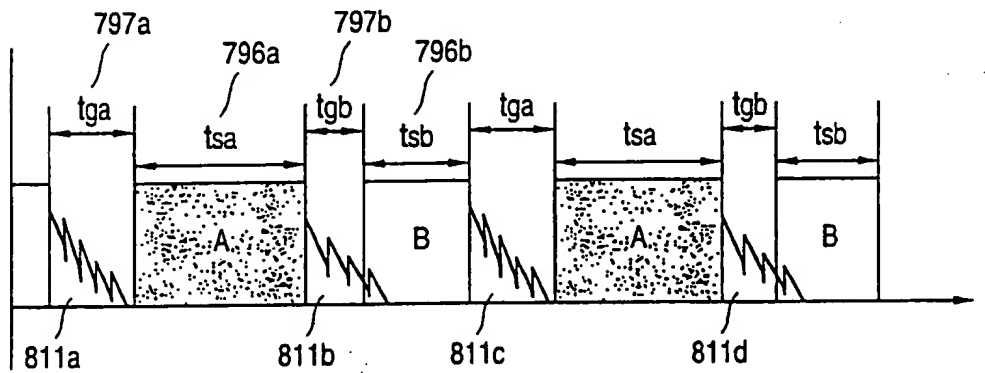
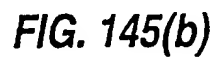


FIG. 144



[illegible]

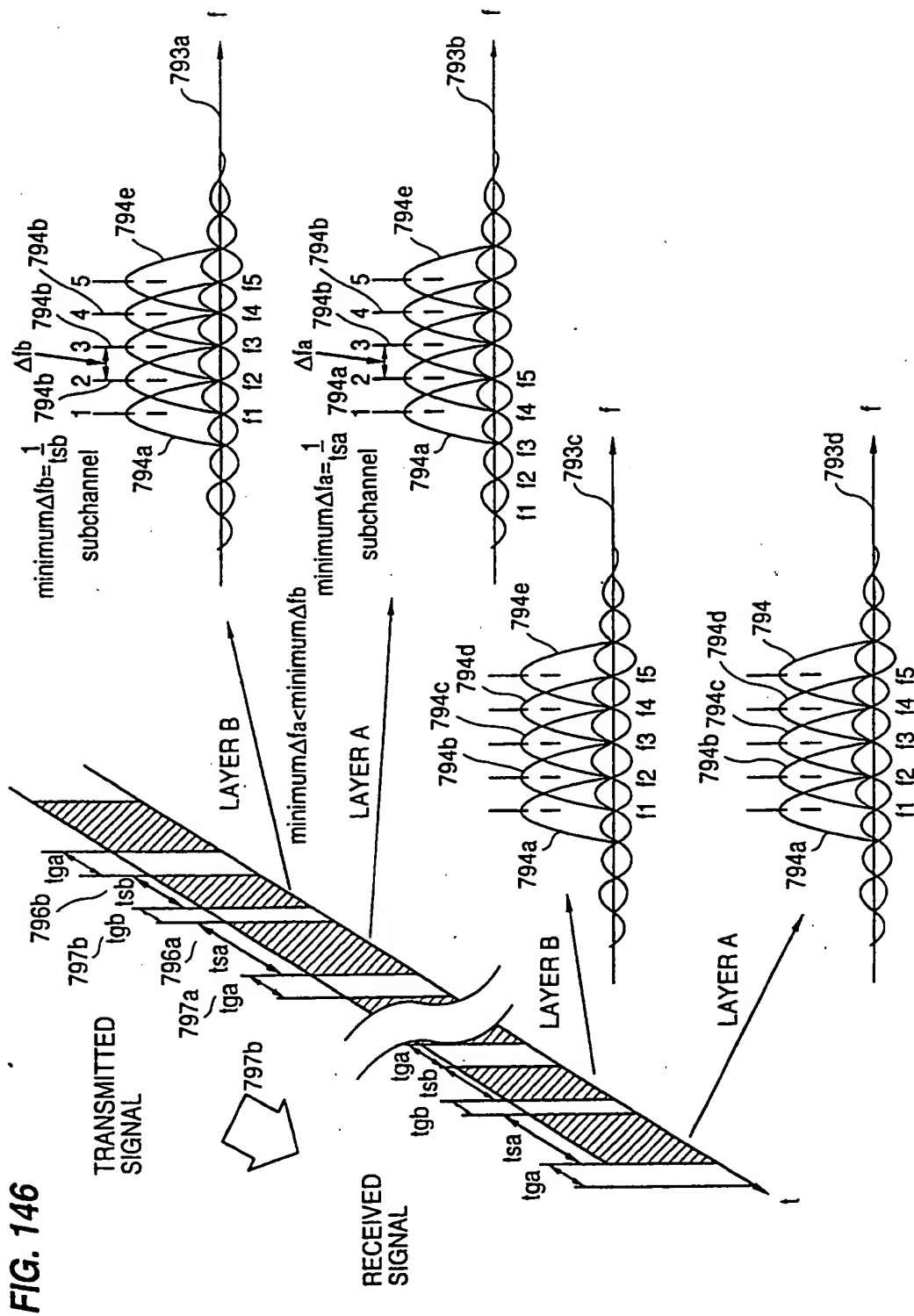


FIG. 147

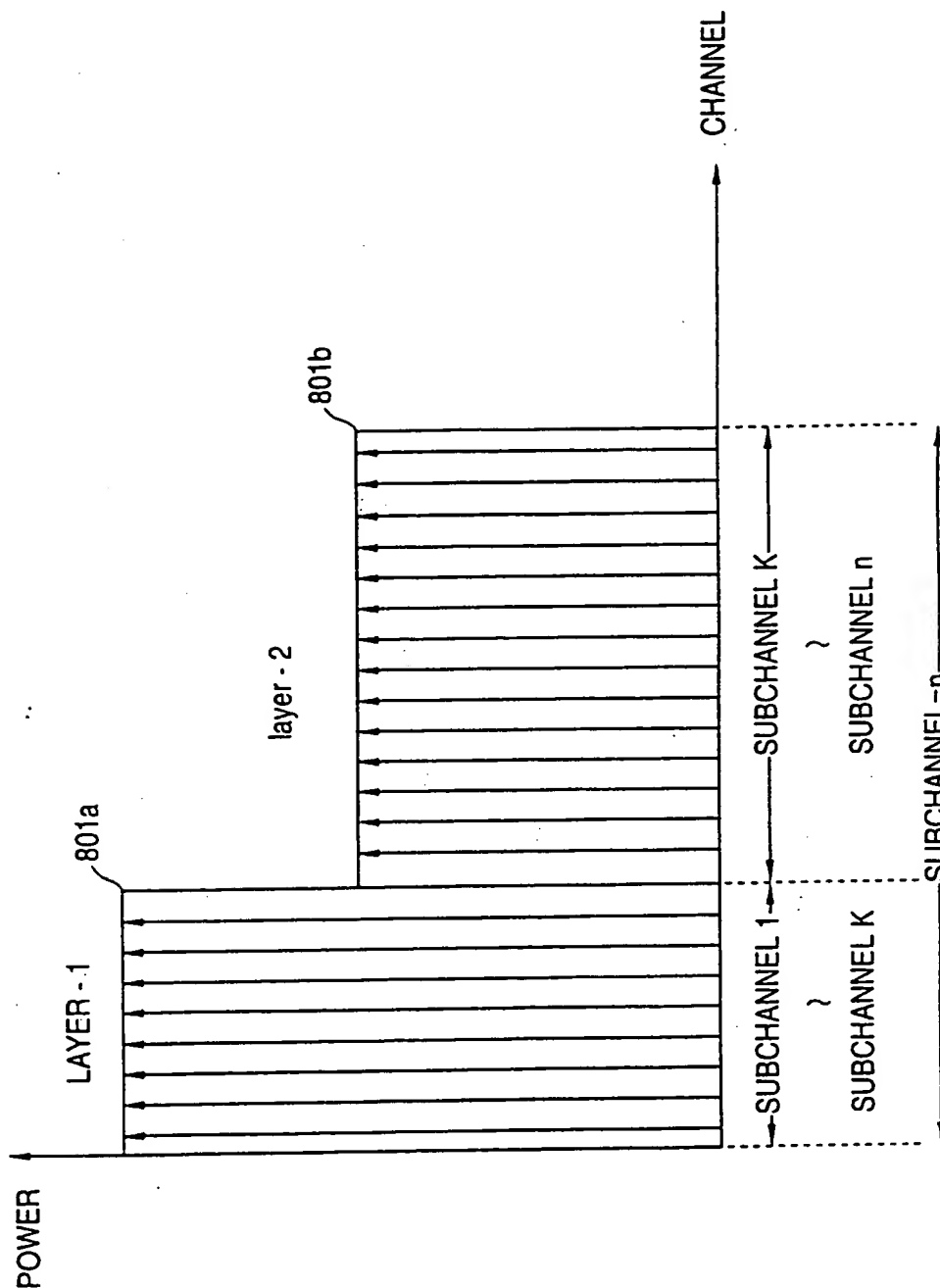
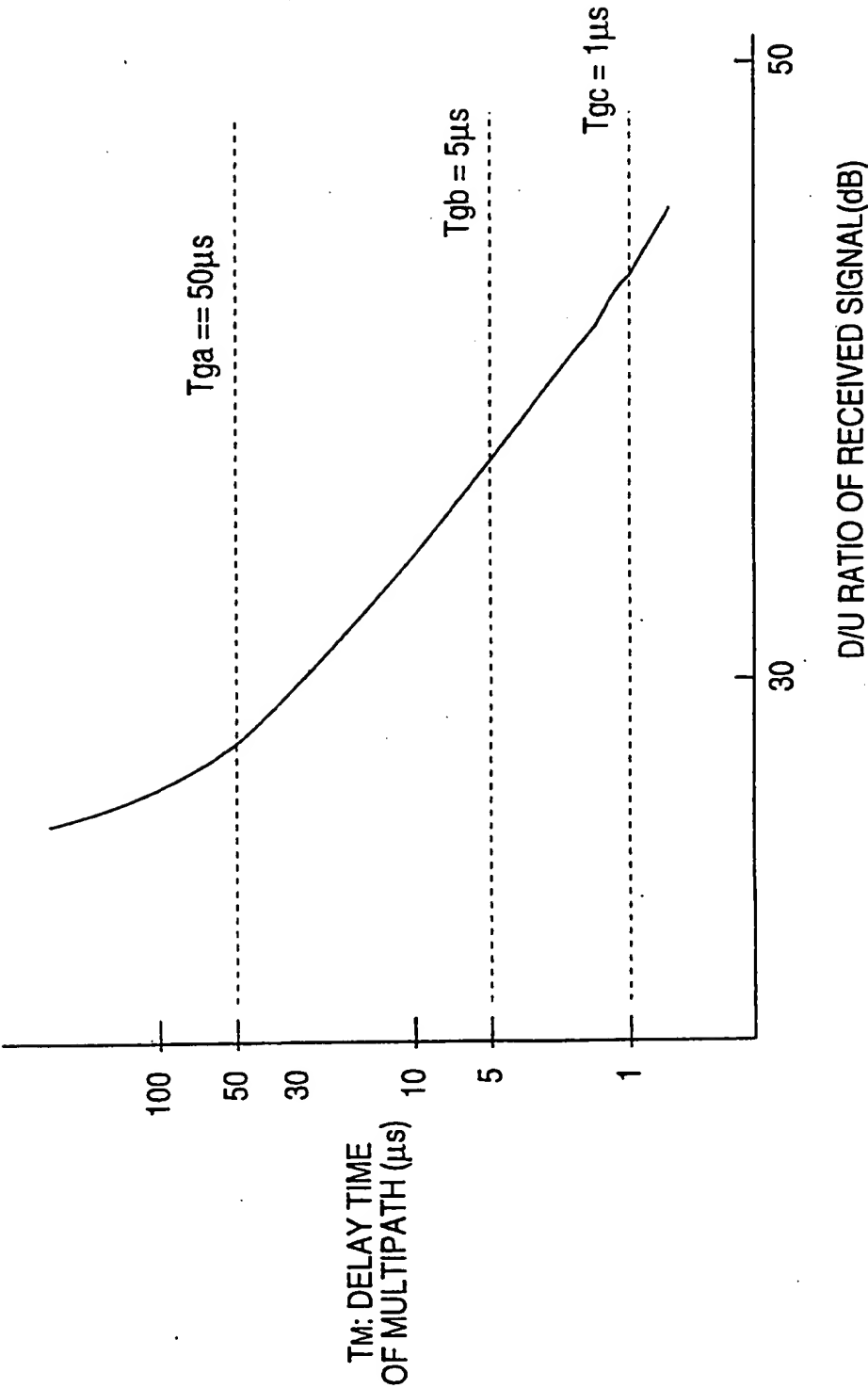


FIG. 148



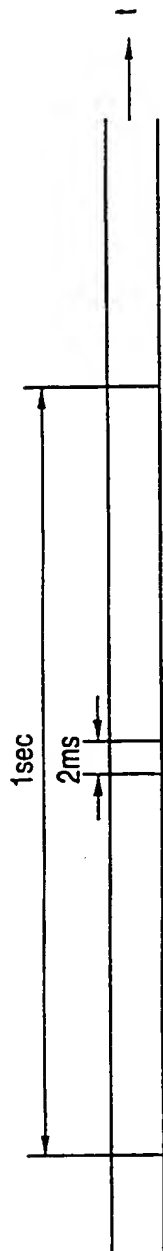


FIG. 149(a)

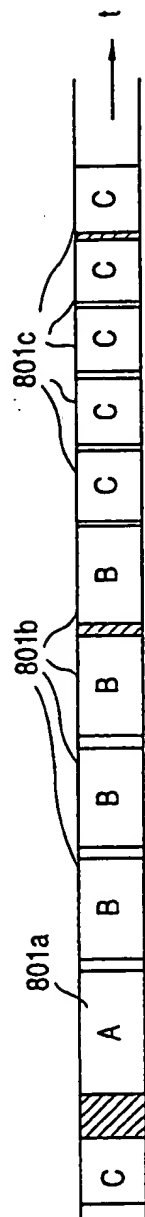


FIG. 149(b)

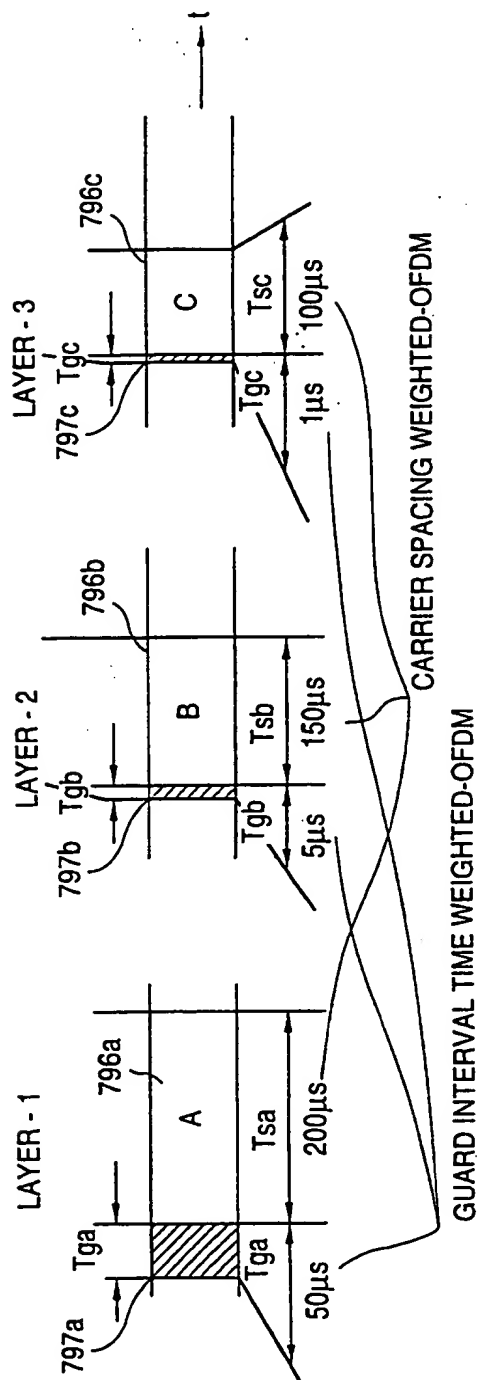


FIG. 149(c)

FIG. 150

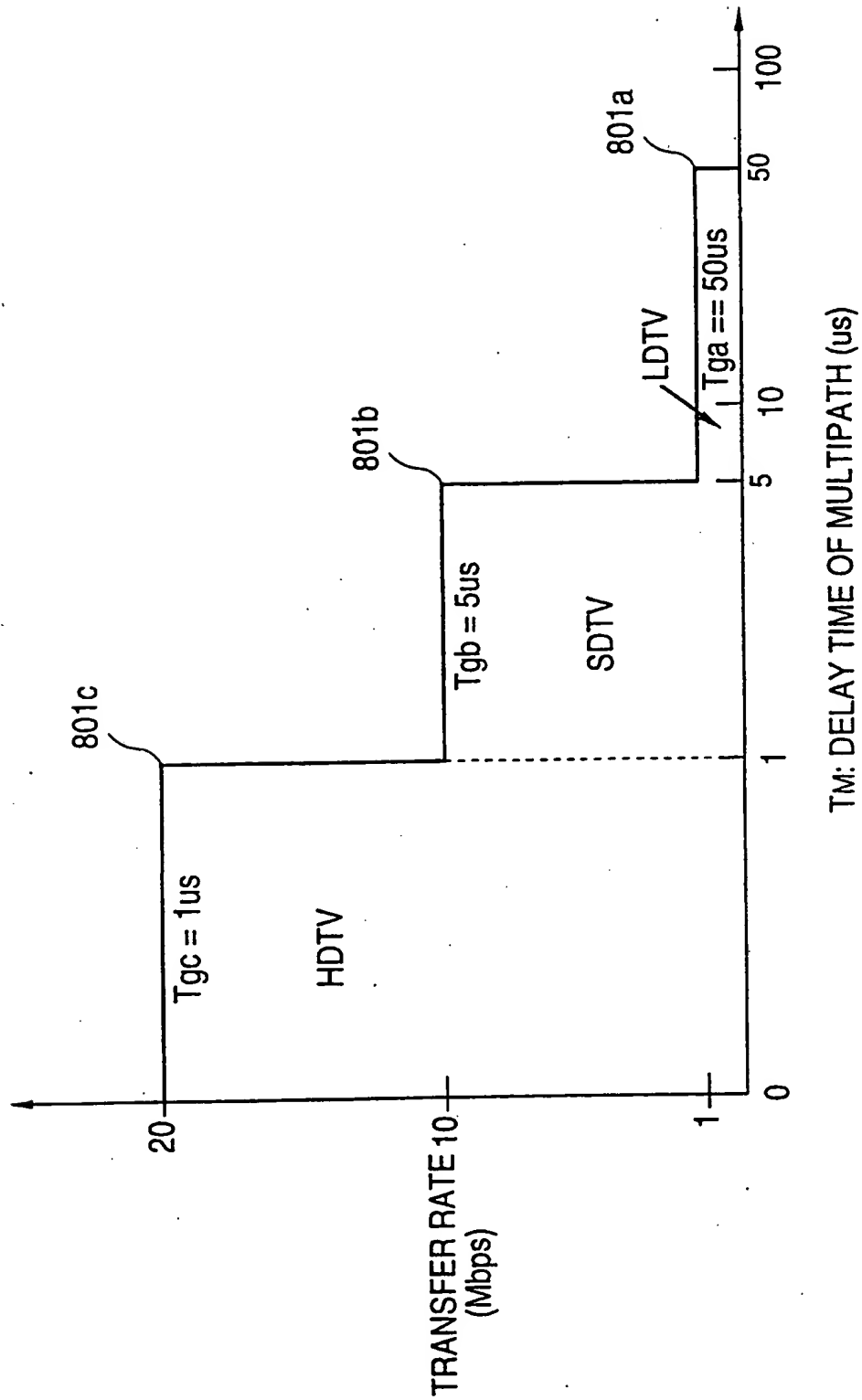


FIG. 151

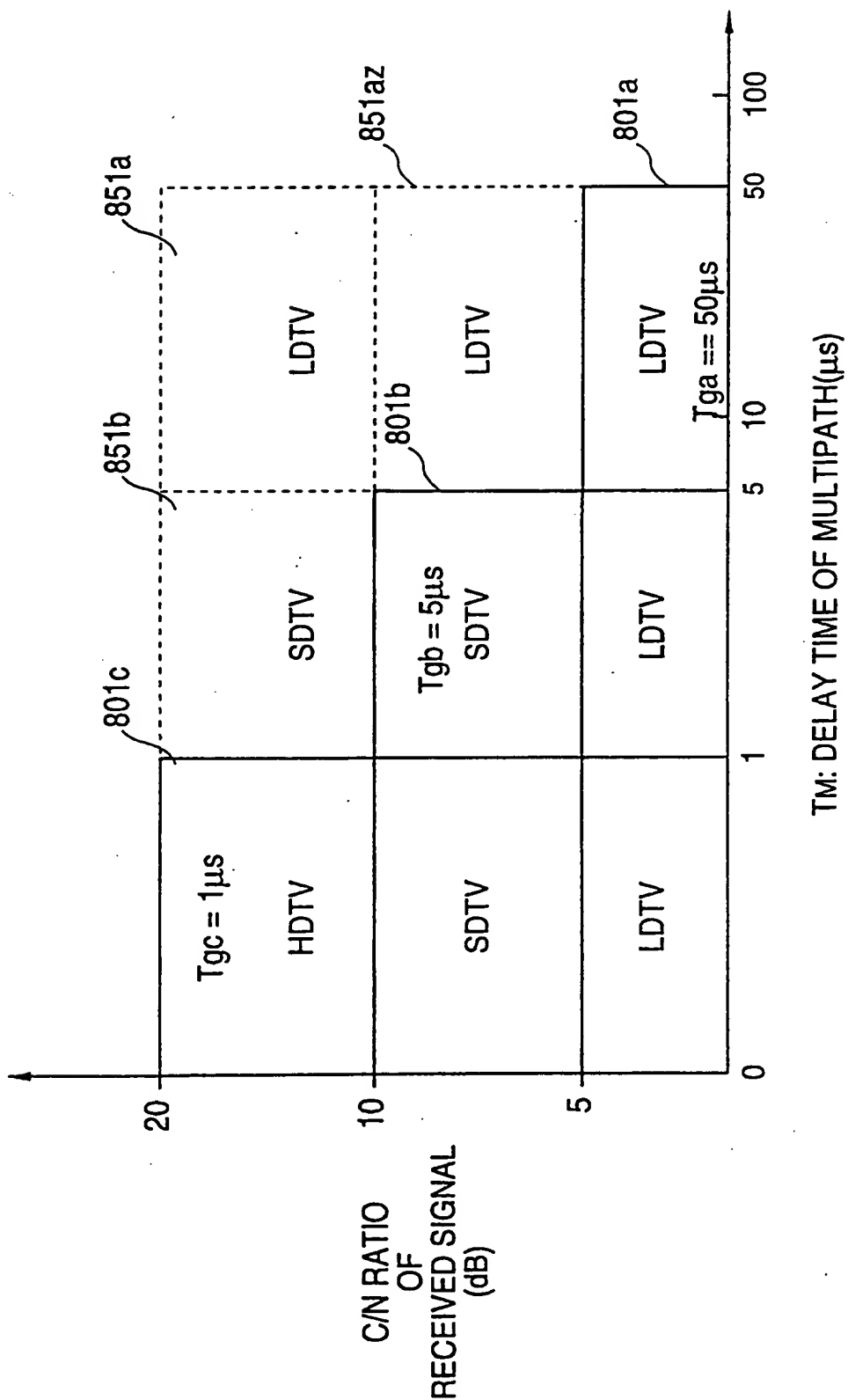


FIG. 152

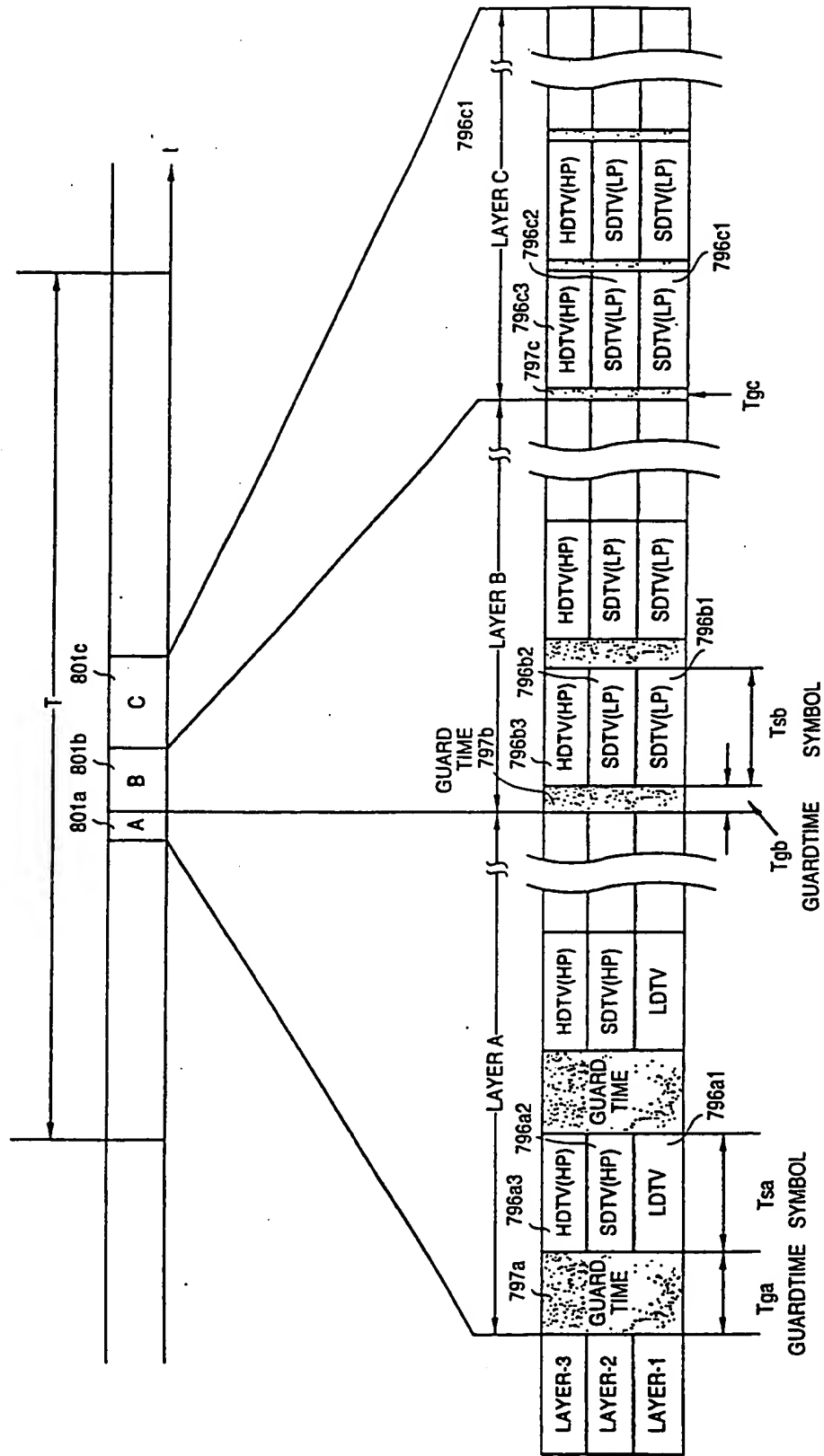
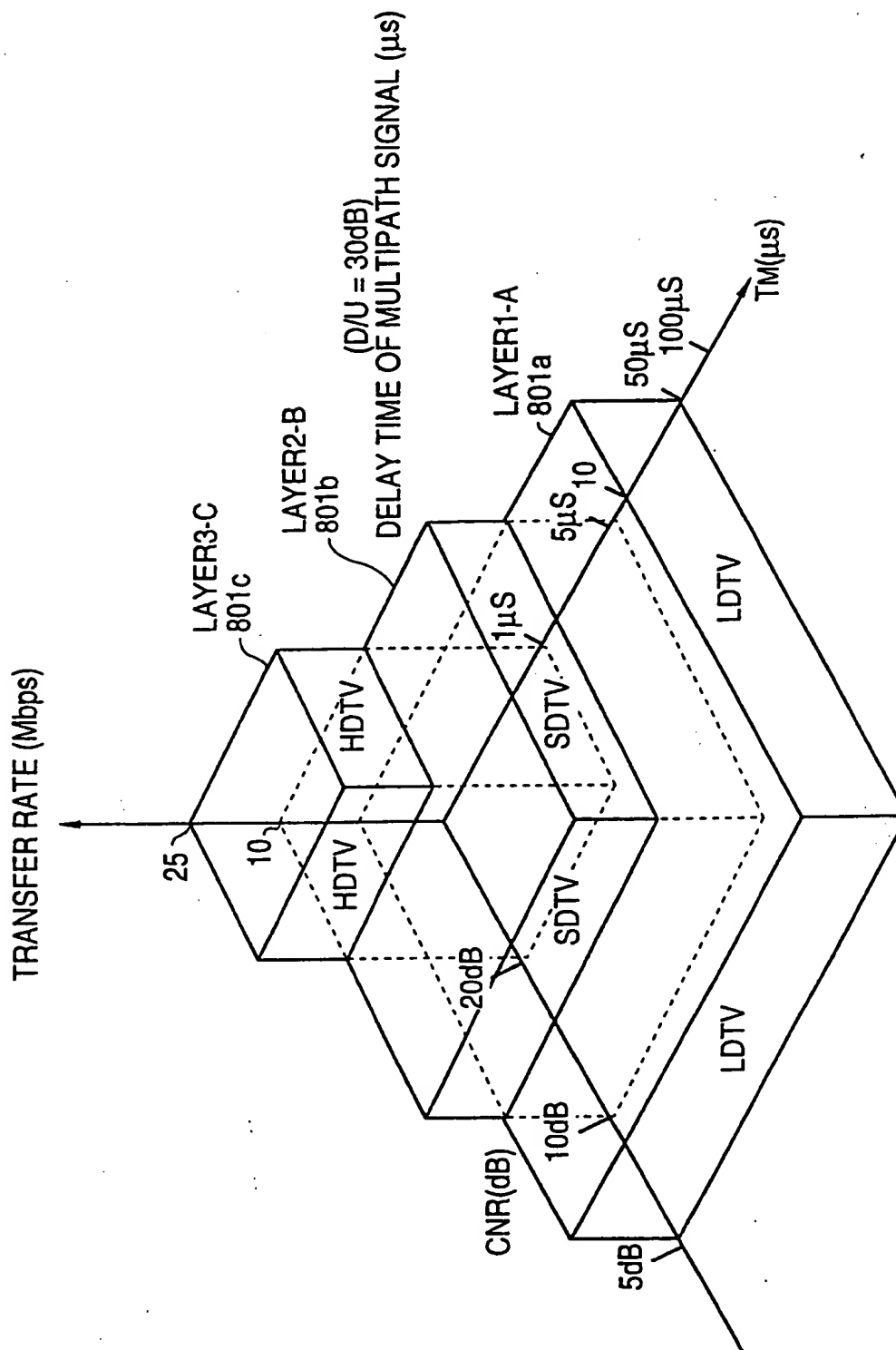


FIG. 153



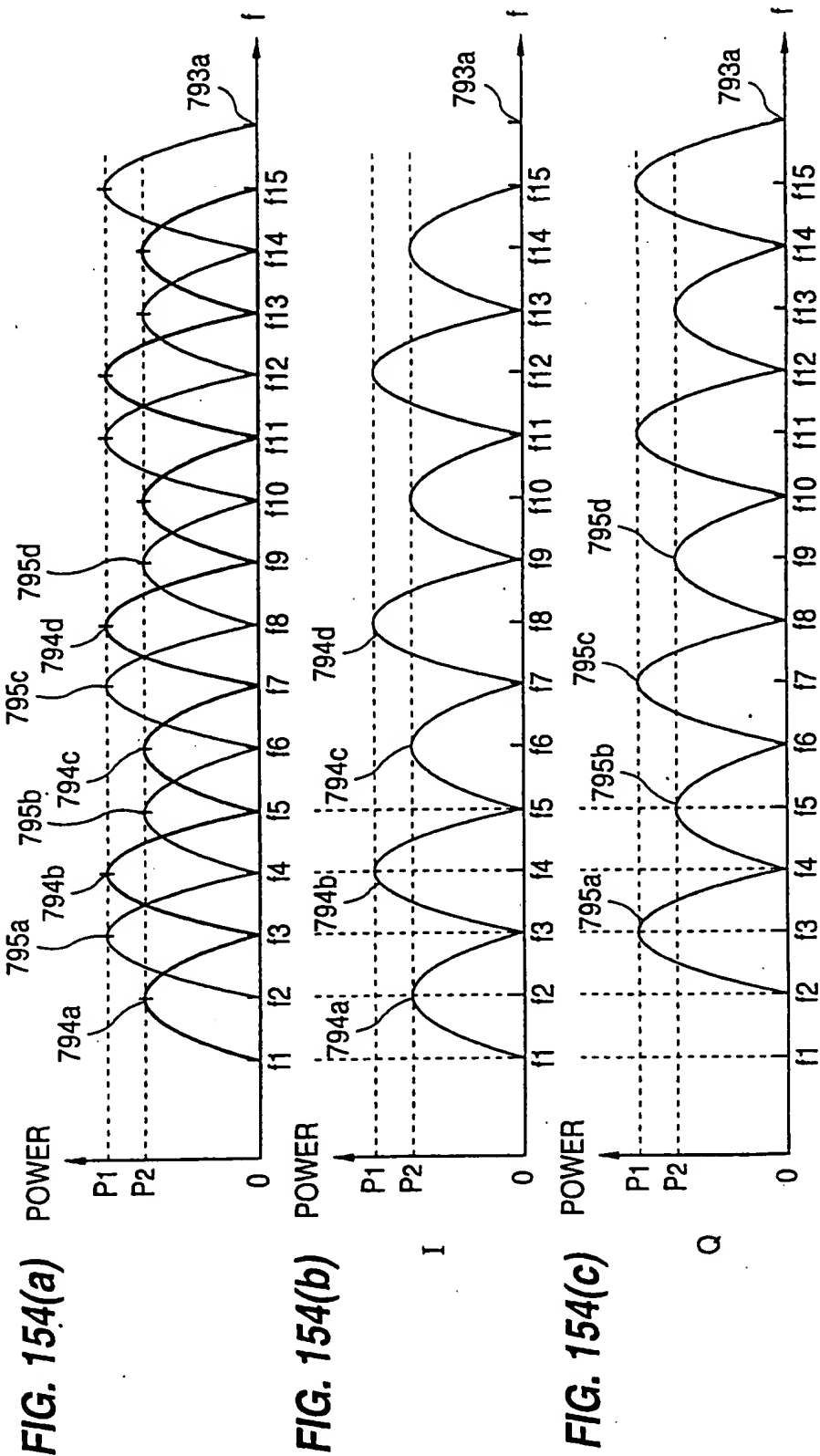


FIG. 155

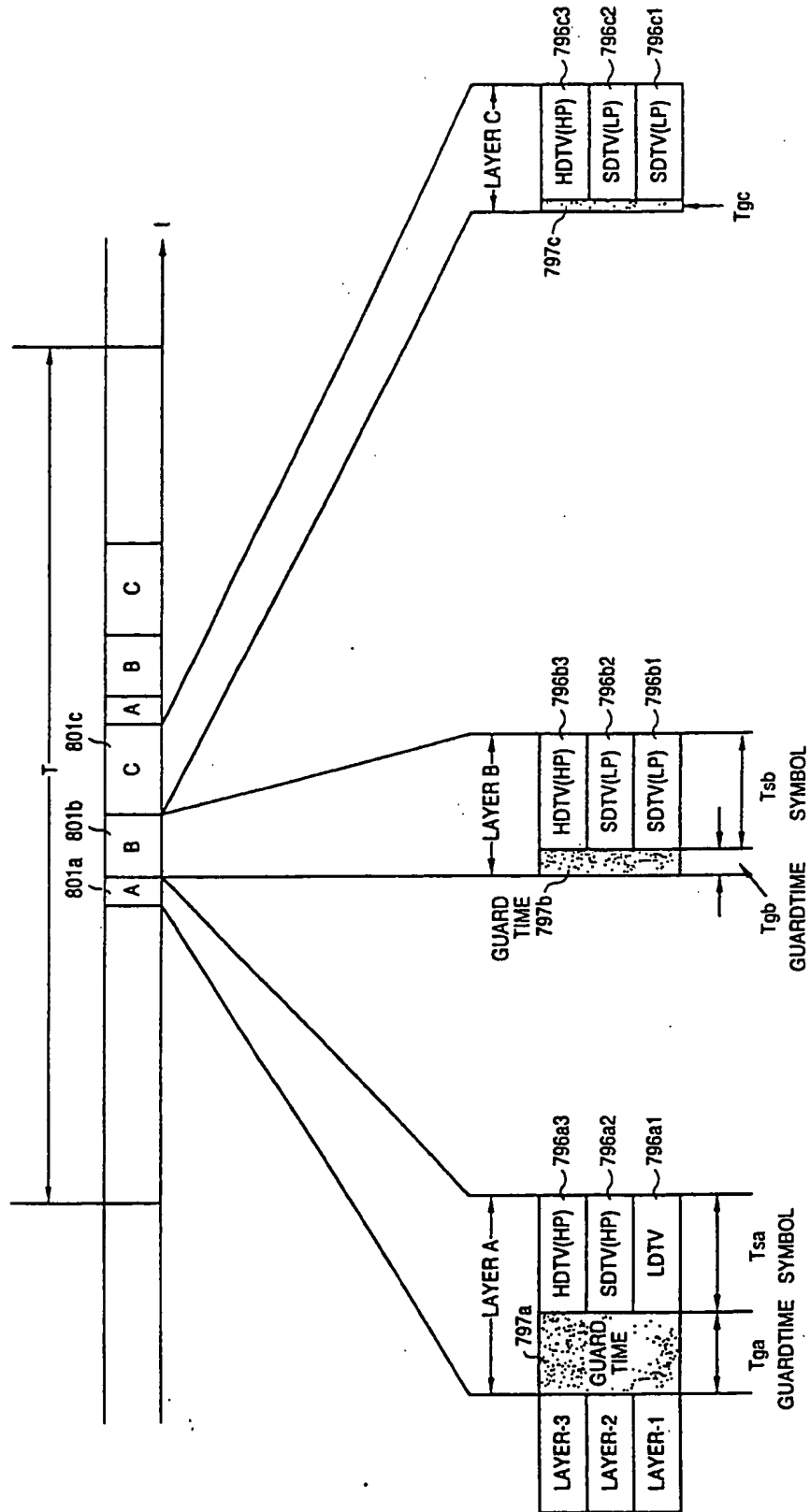


FIG. 156

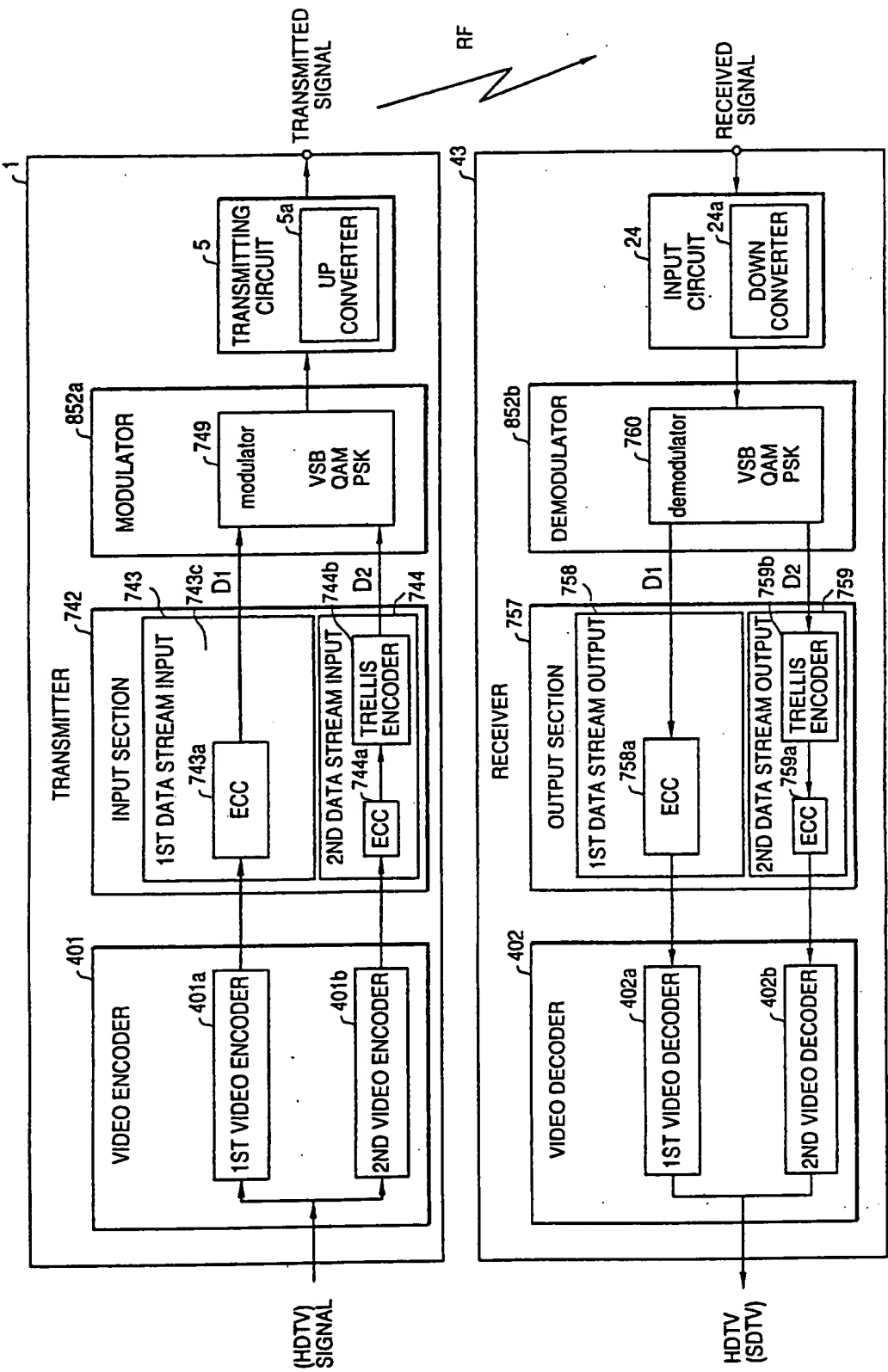


FIG. 157

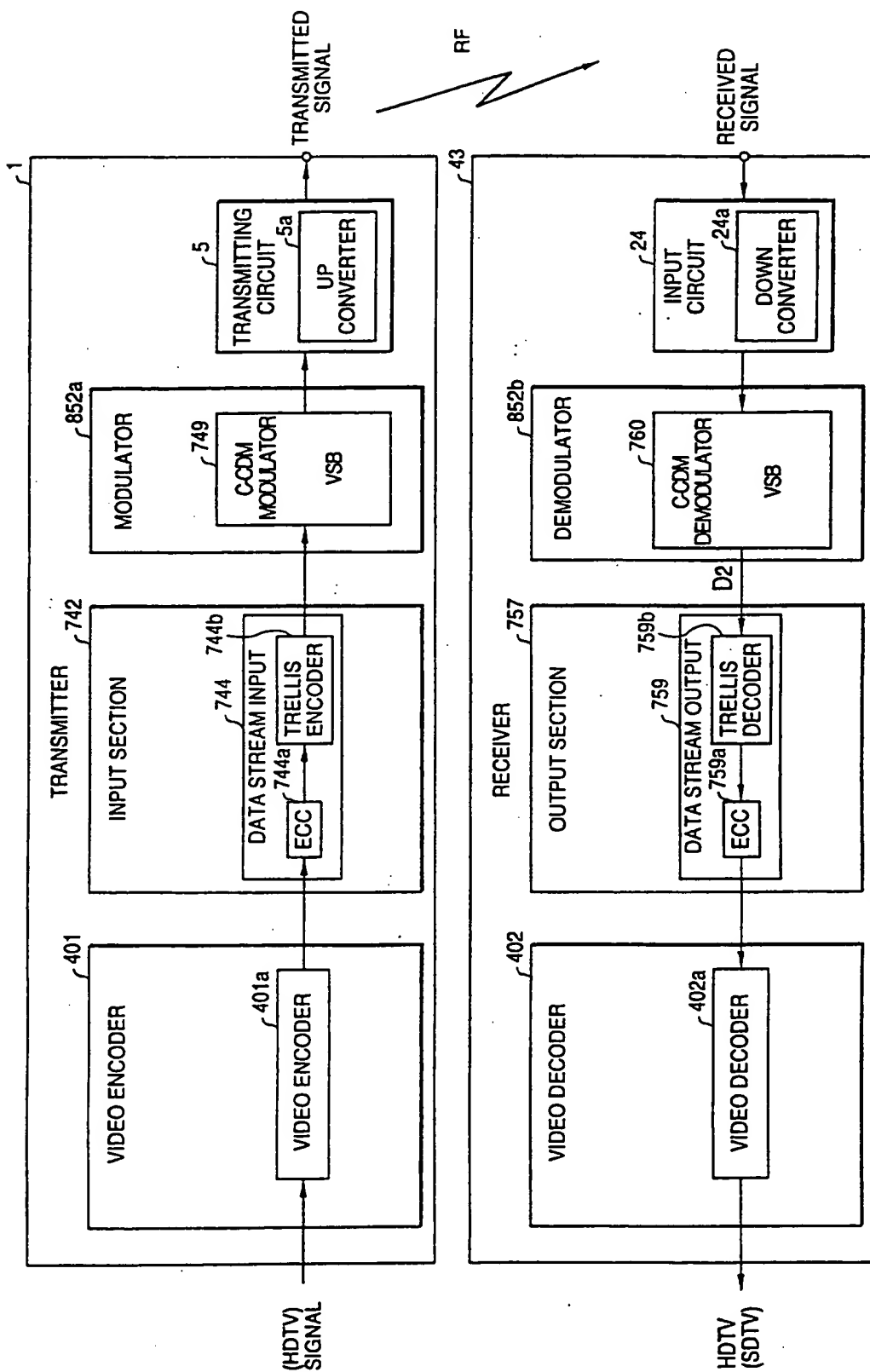
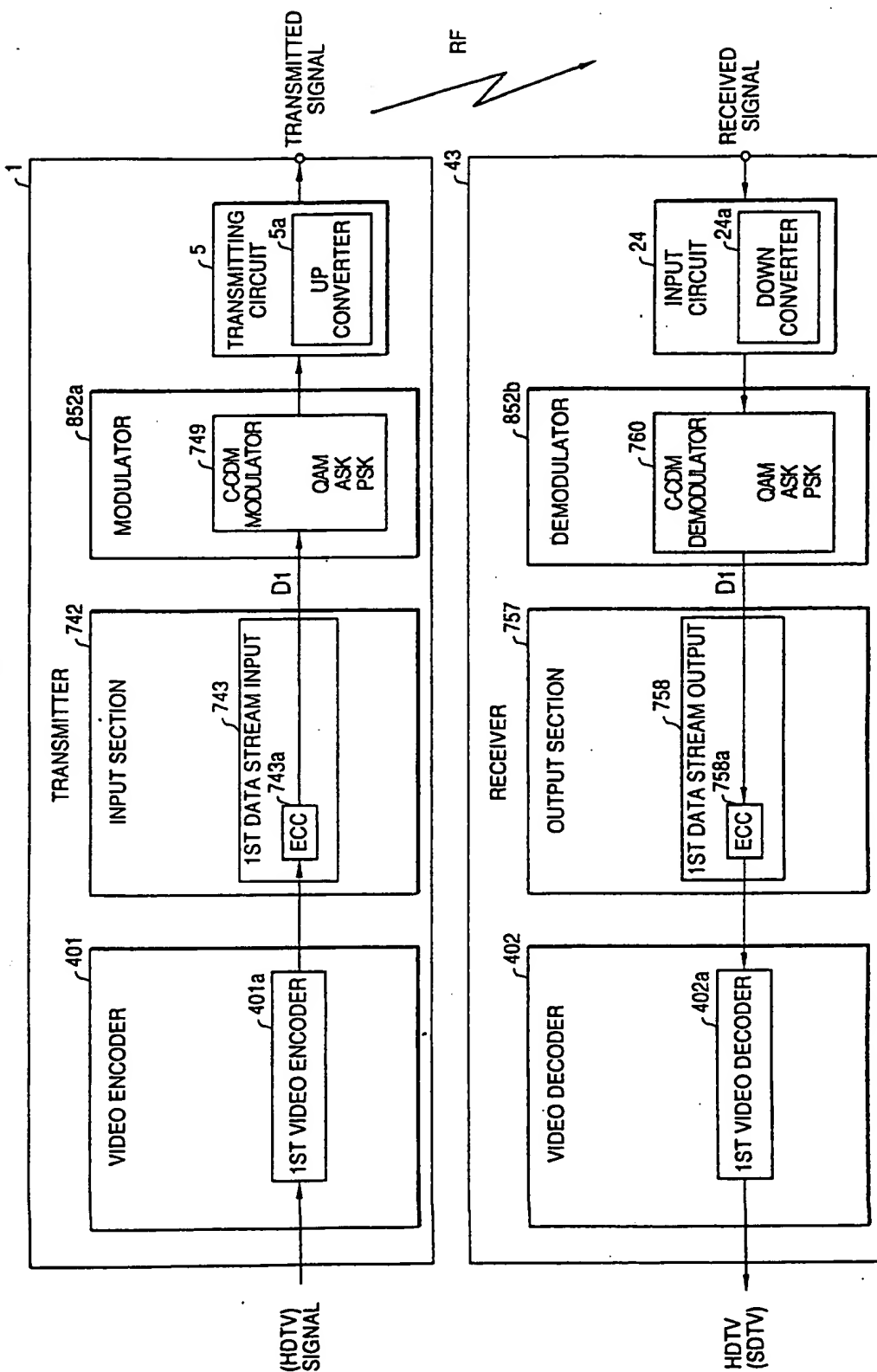


FIG. 158



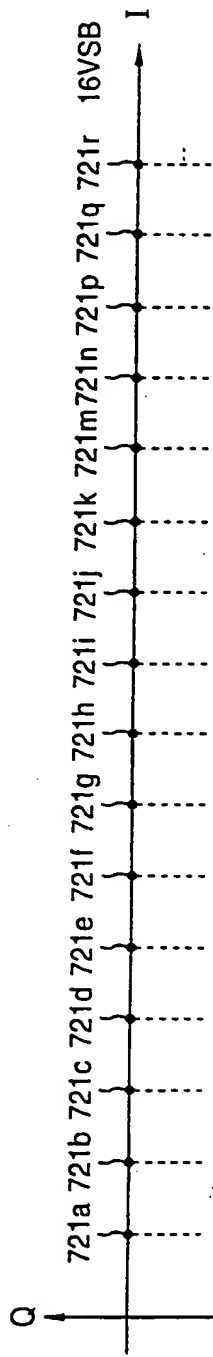


FIG. 159(a)

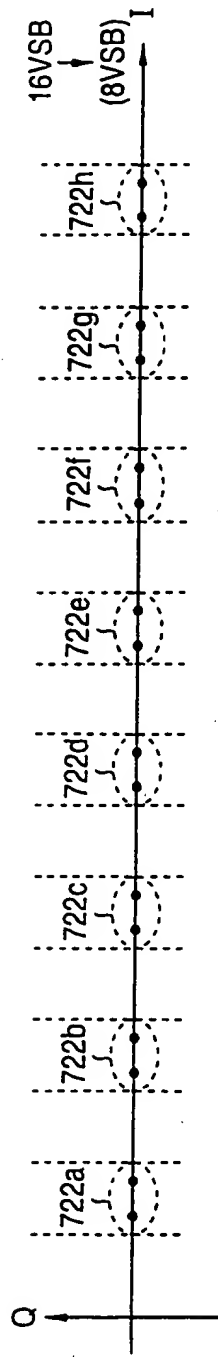


FIG. 159(b)

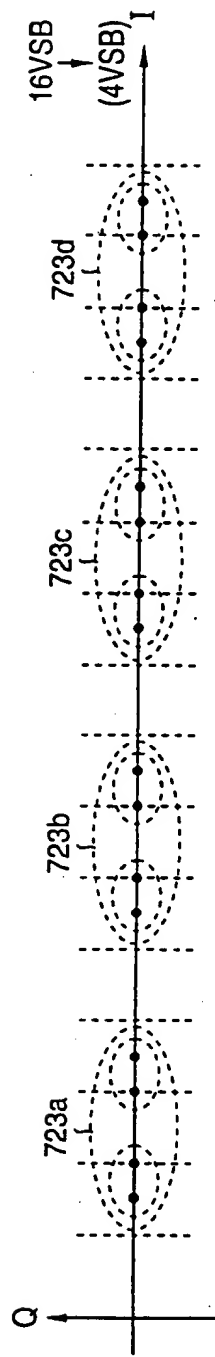


FIG. 159(c)

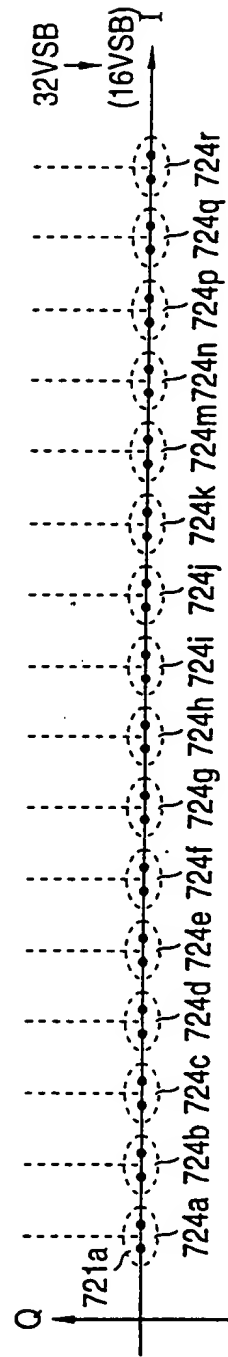


FIG. 159(d)

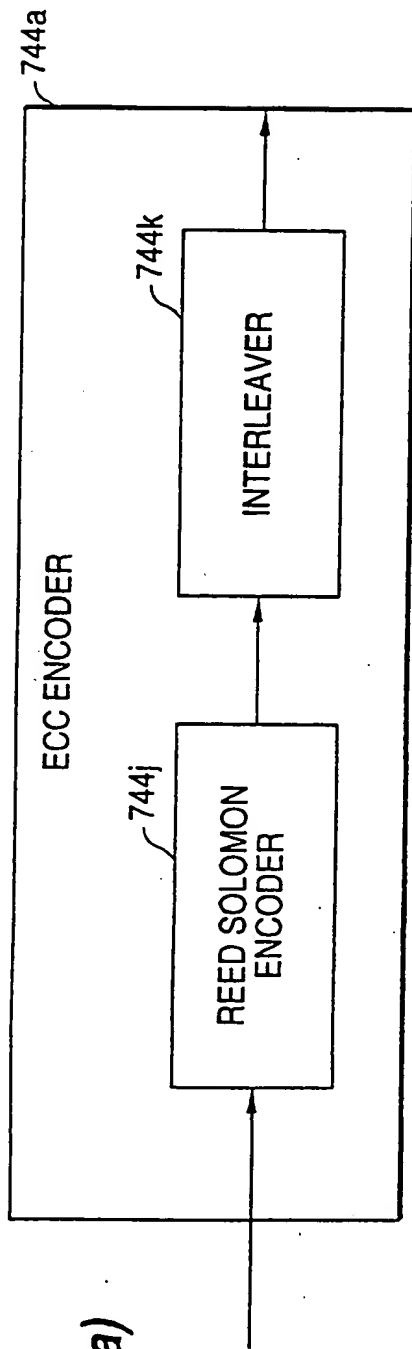


FIG. 160(a)

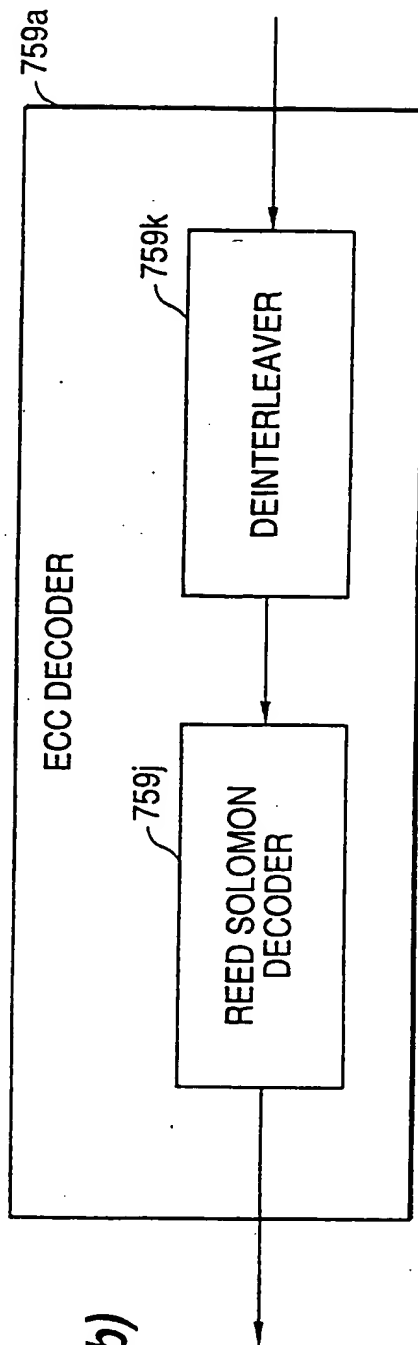


FIG. 160(b)

FIG. 161

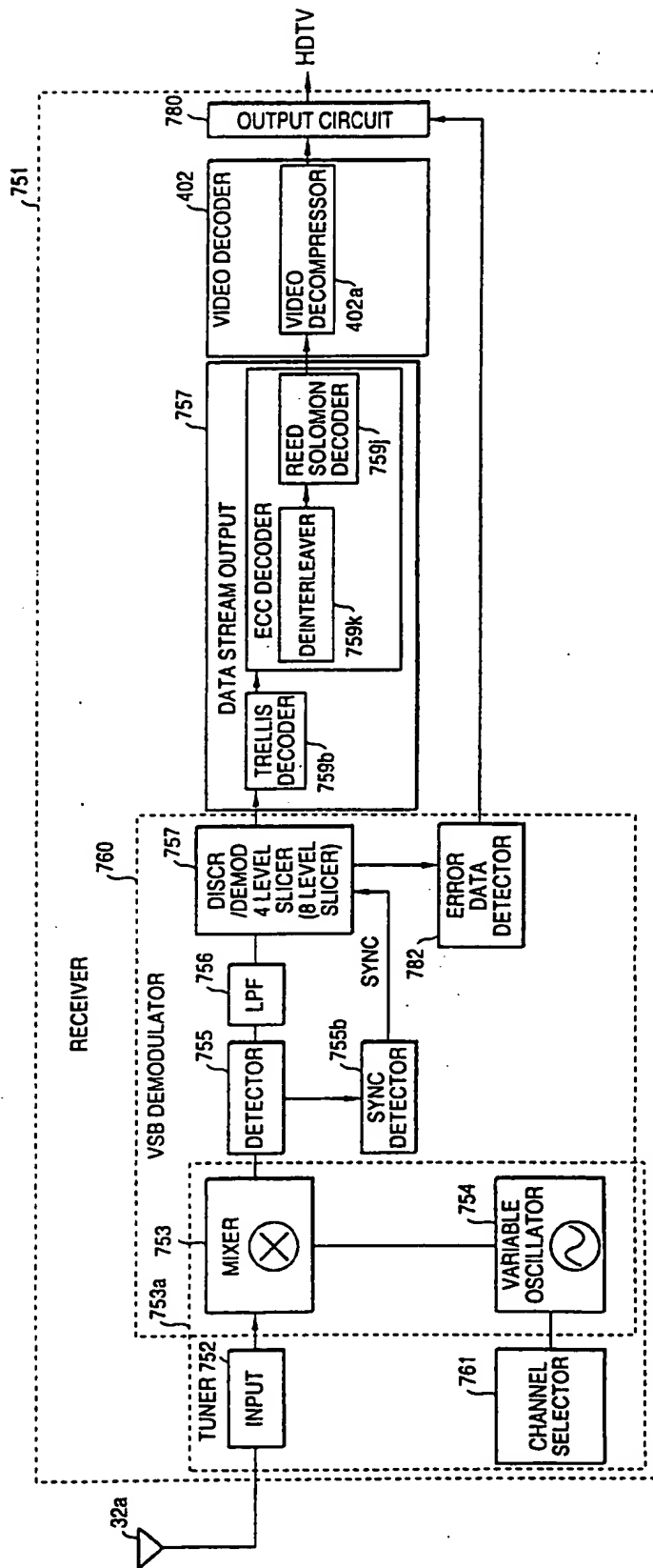


FIG. 162

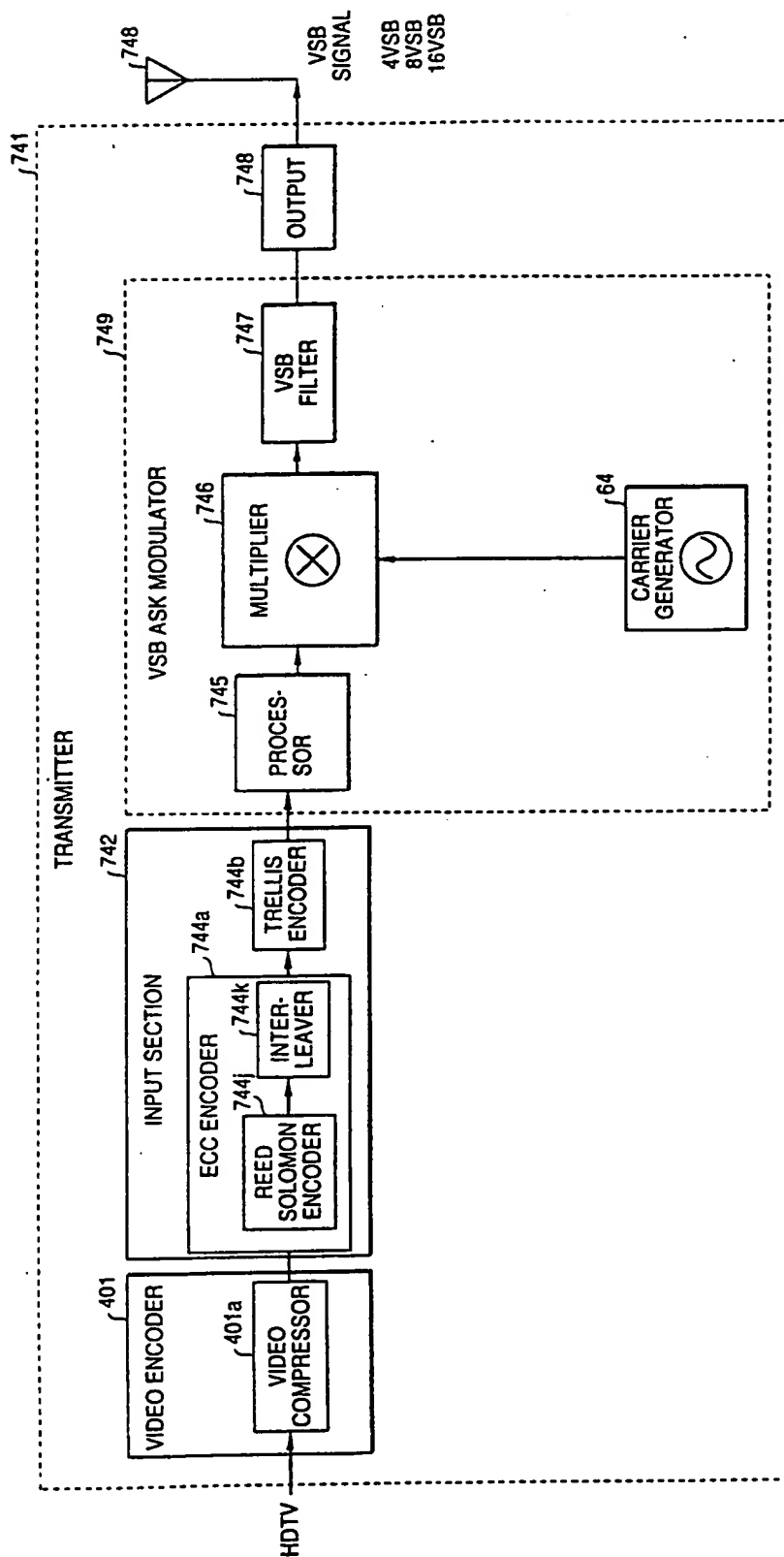


FIG. 163

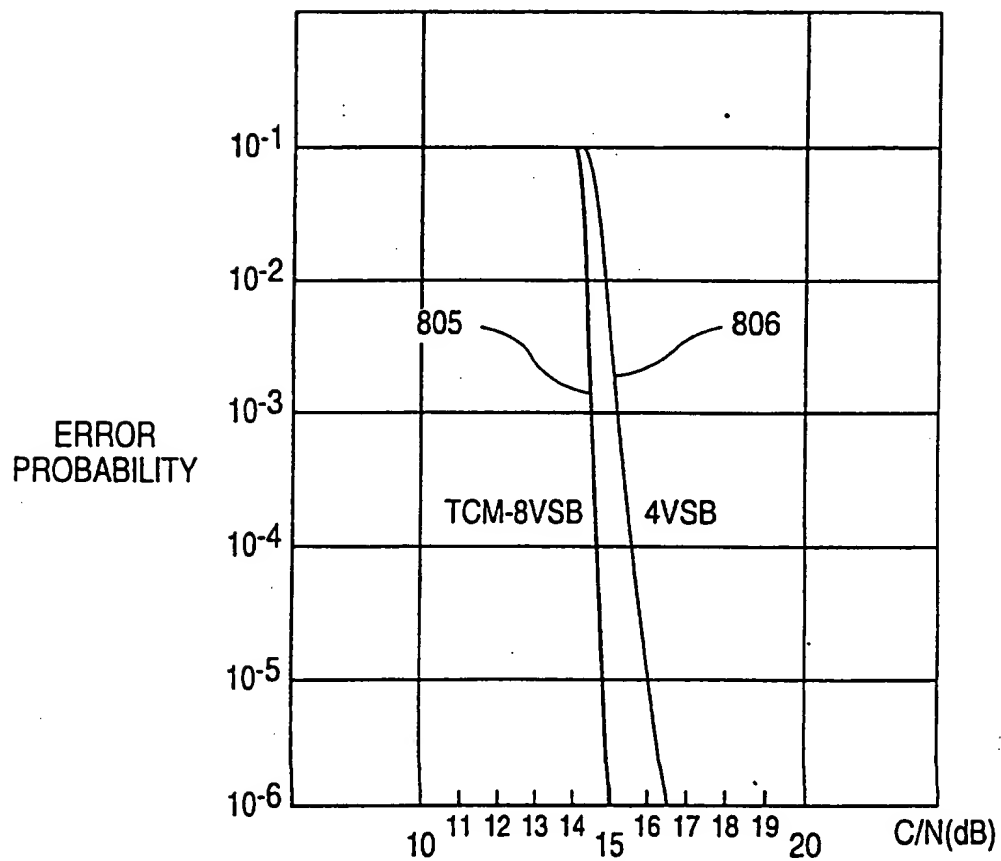


FIG. 164

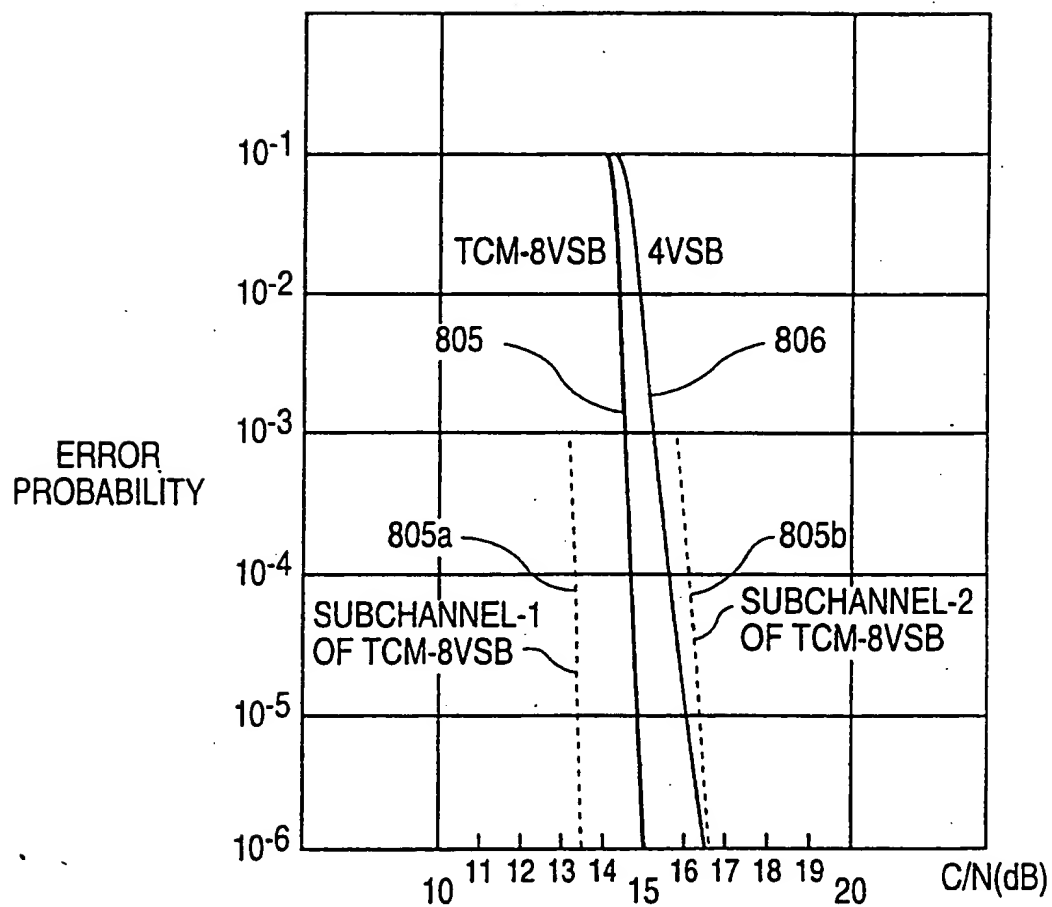


FIG. 165(a)

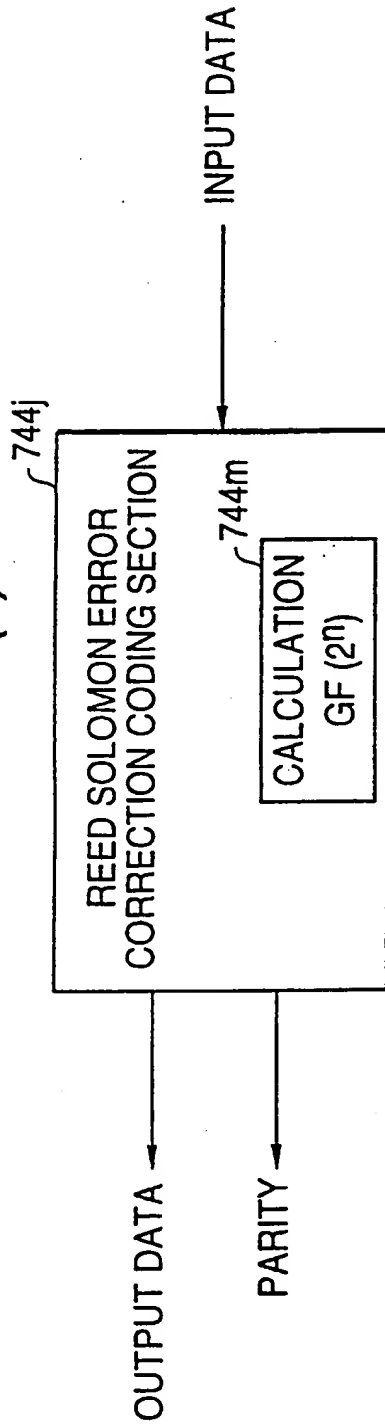


FIG. 165(b)

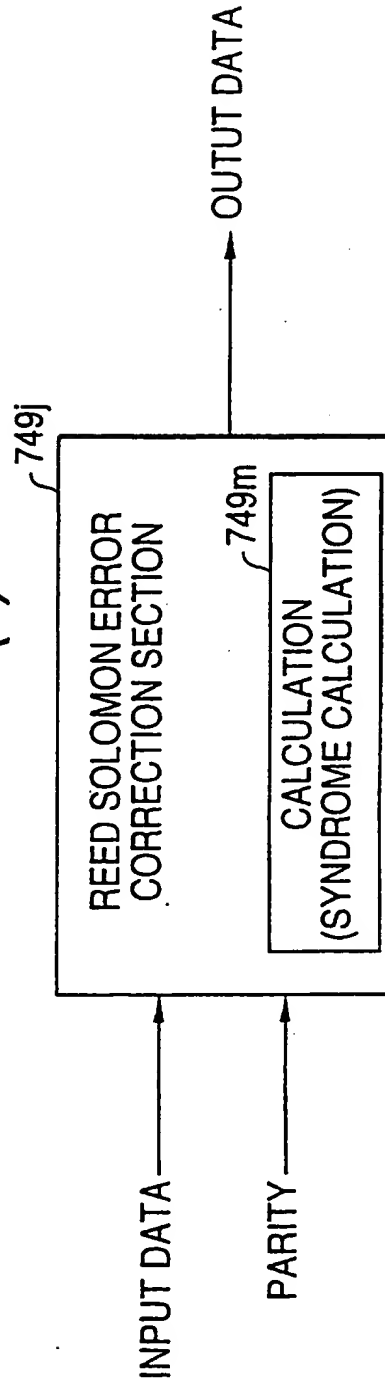


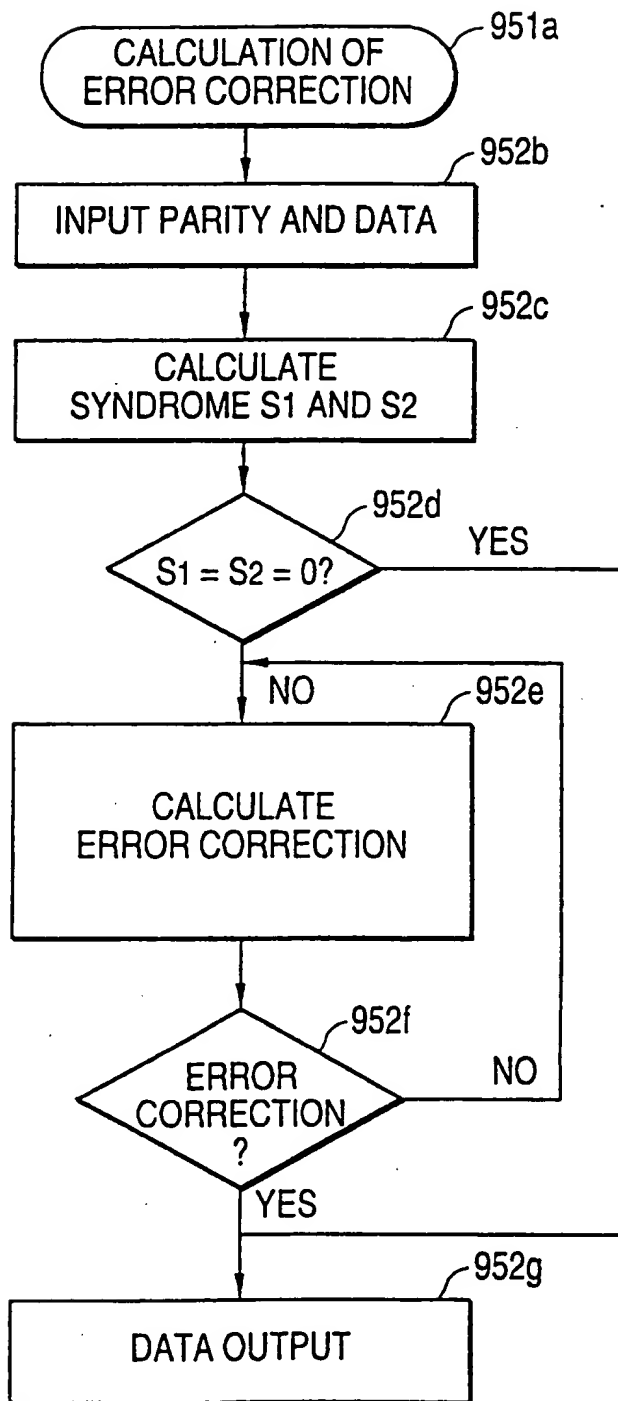
FIG. 166

FIG. 167

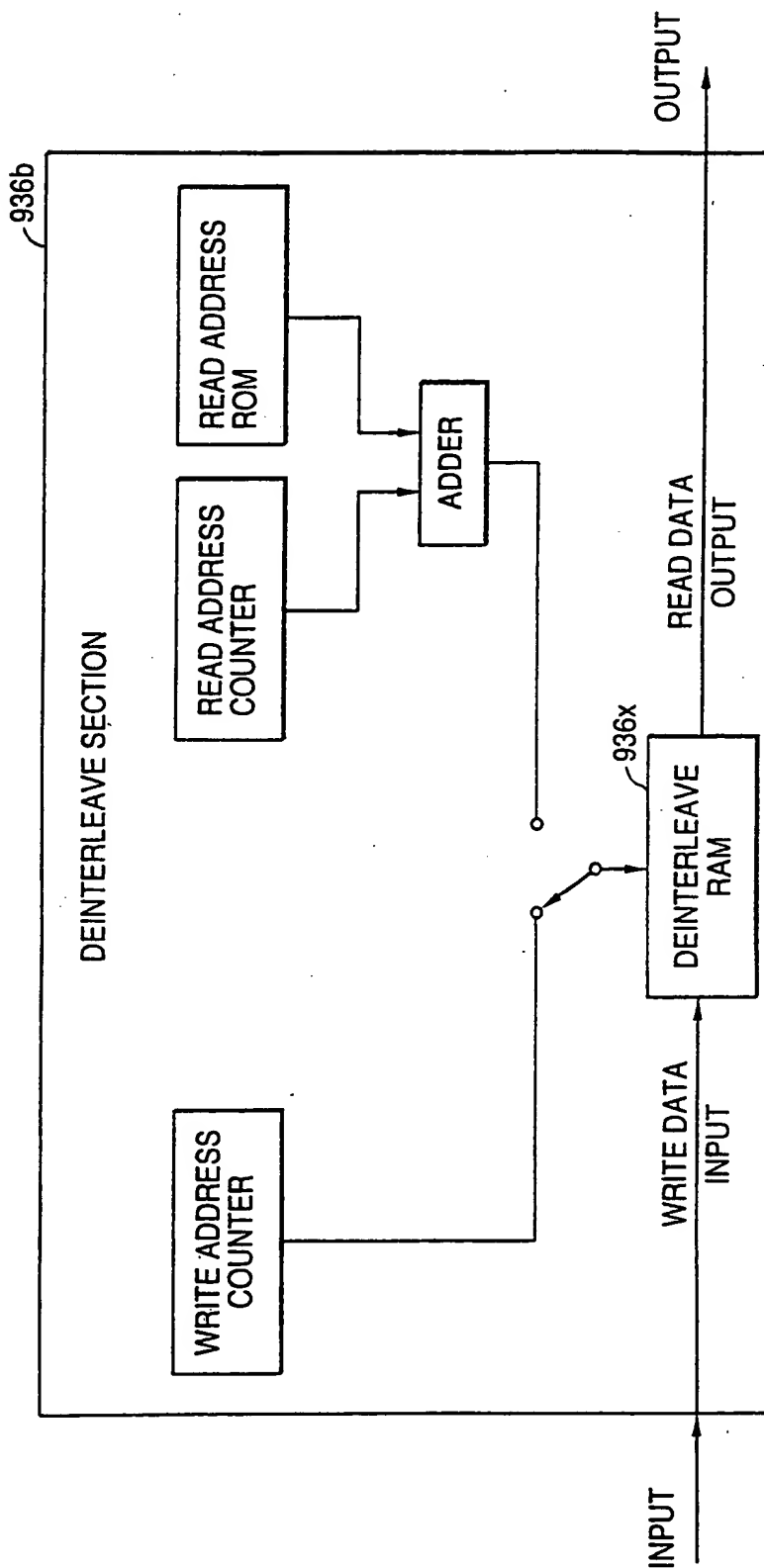


FIG. 168(a)

INTERLEAVE TABLE

1	2	3	4	5	6	7
DATA						C2 PARITY
1	A 1	A 2	A 3	A 4	A 5	A 6
2	B 1	B 2	B 3	B 4		
3	C 1					
4	D 1					
5	E 1					
6	F 1					
C1 PARITY	PARITY	PARITY	PARITY	PARITY	PARITY	PARITY

FIG. 168(b)

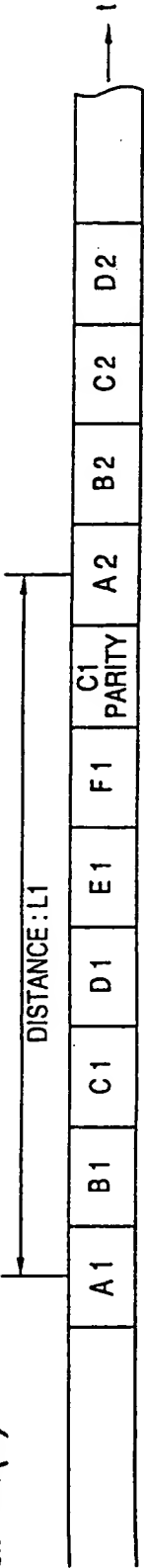


FIG. 169

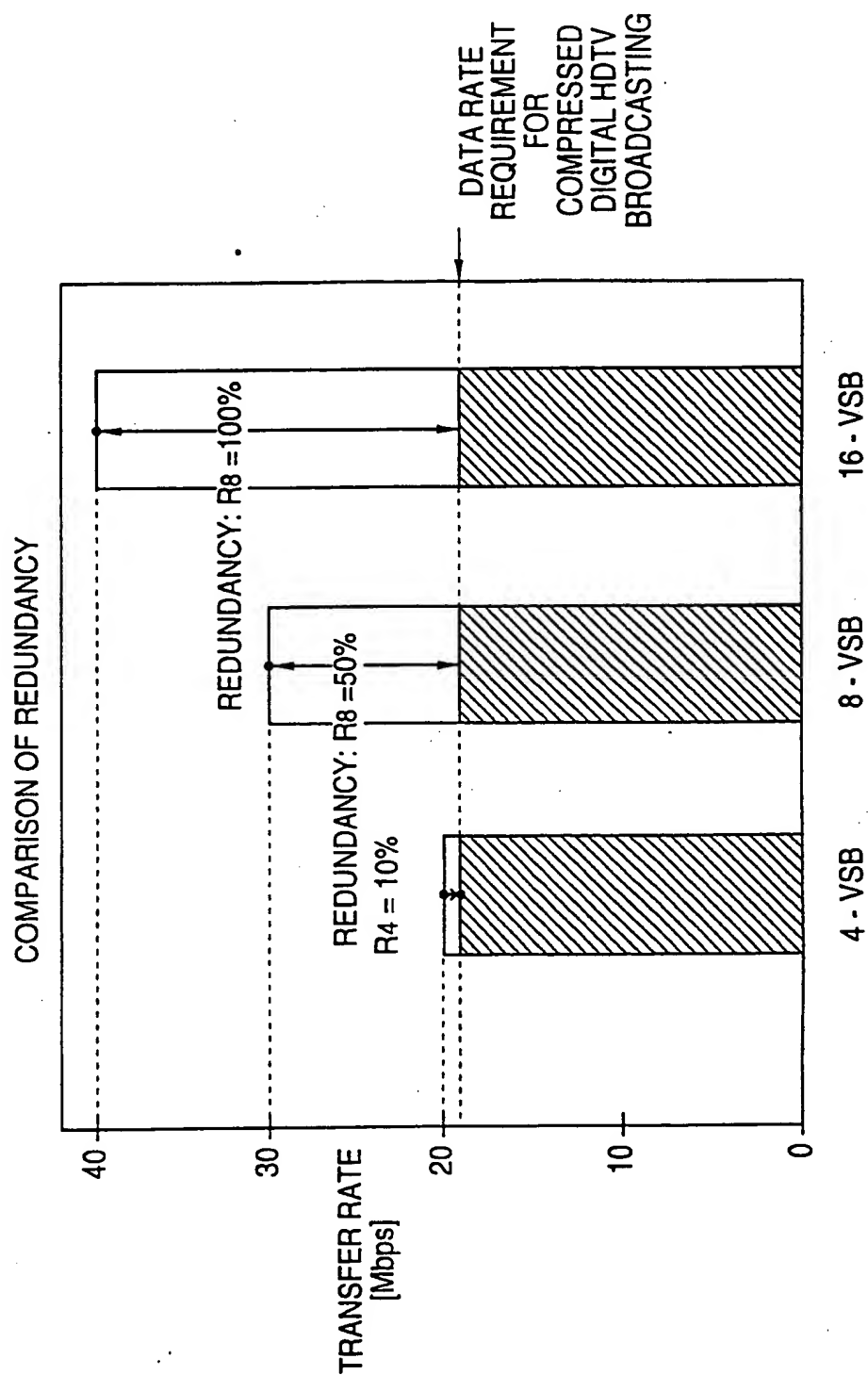


FIG. 170

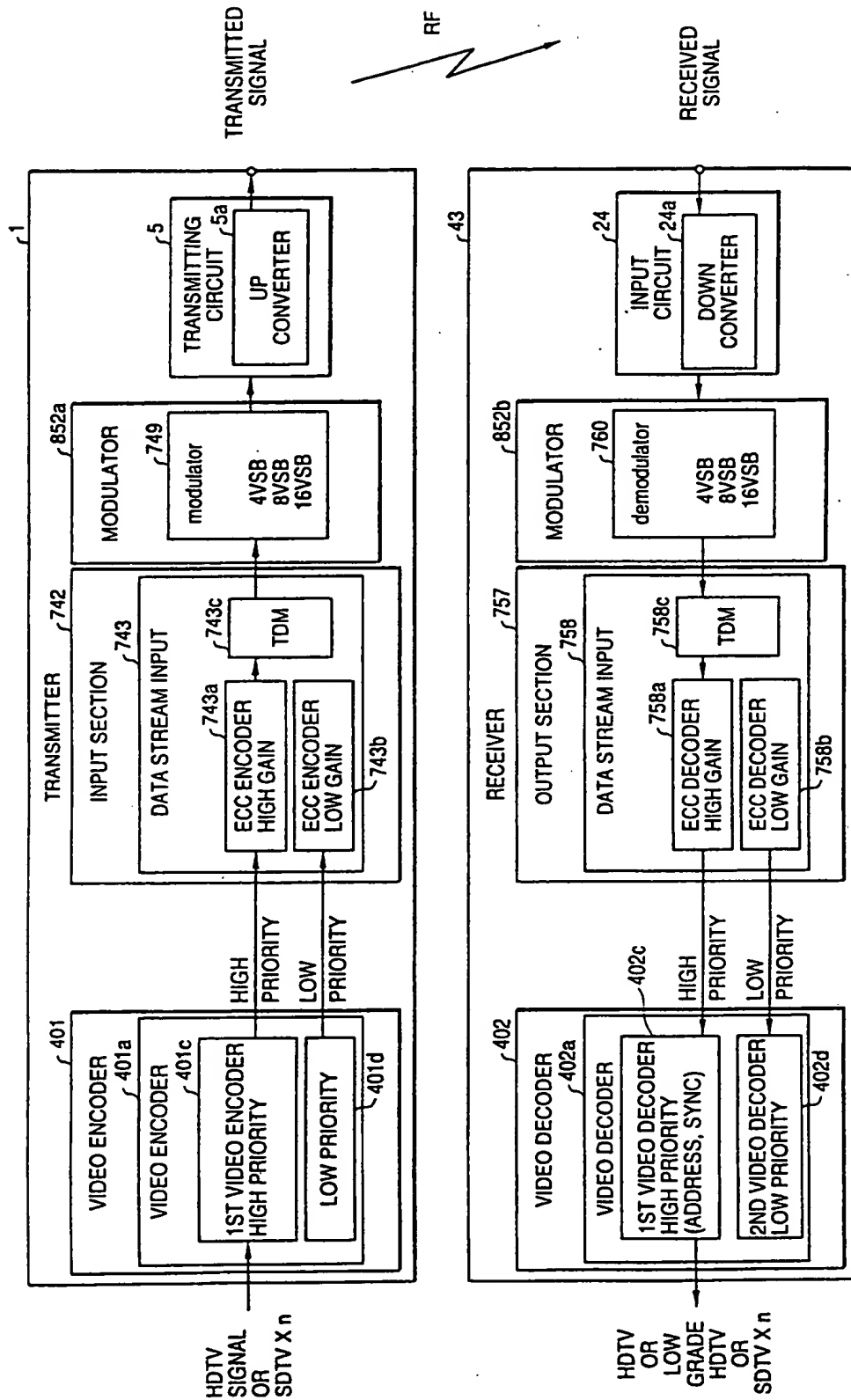


FIG. 171

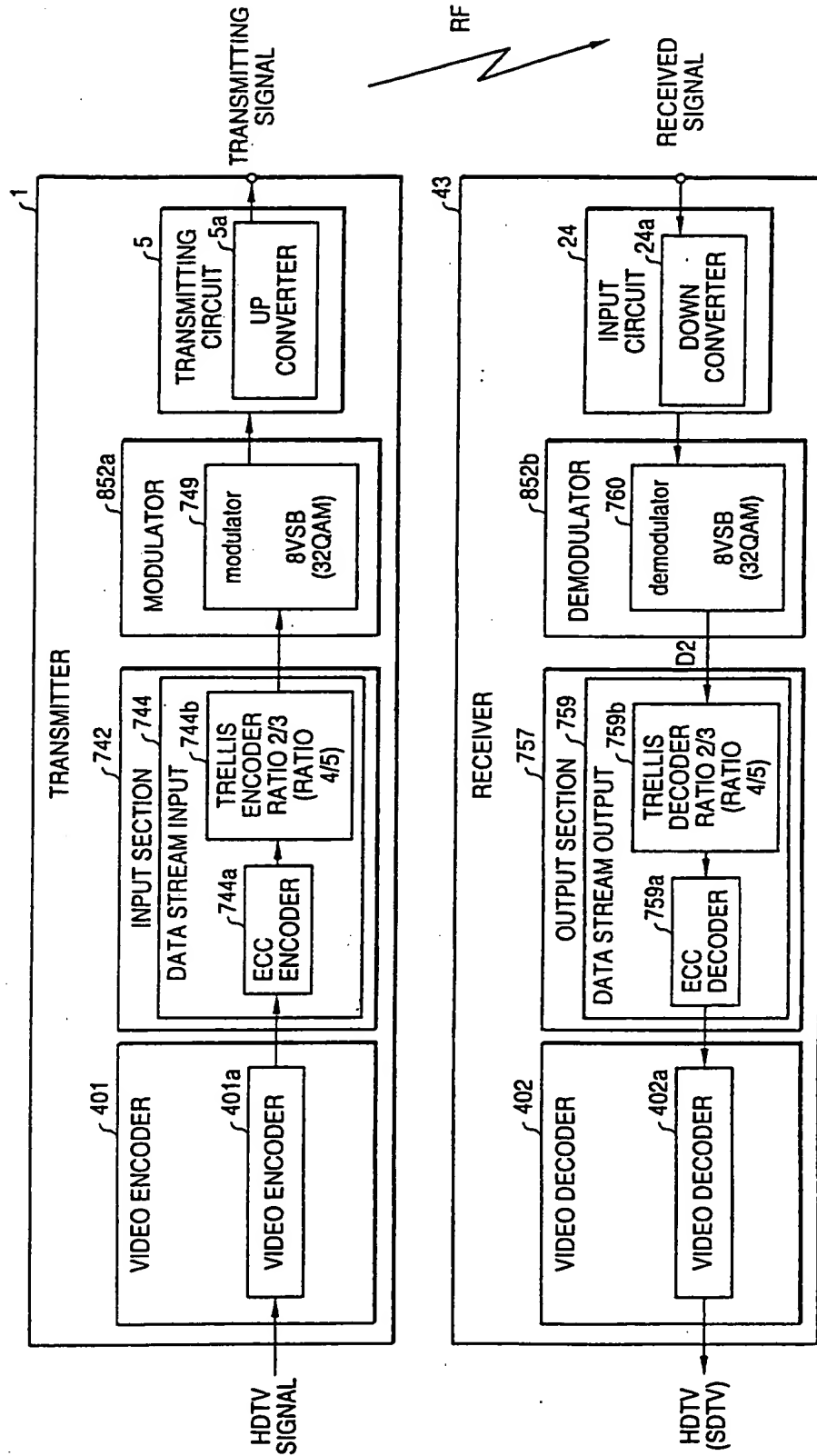


FIG. 172

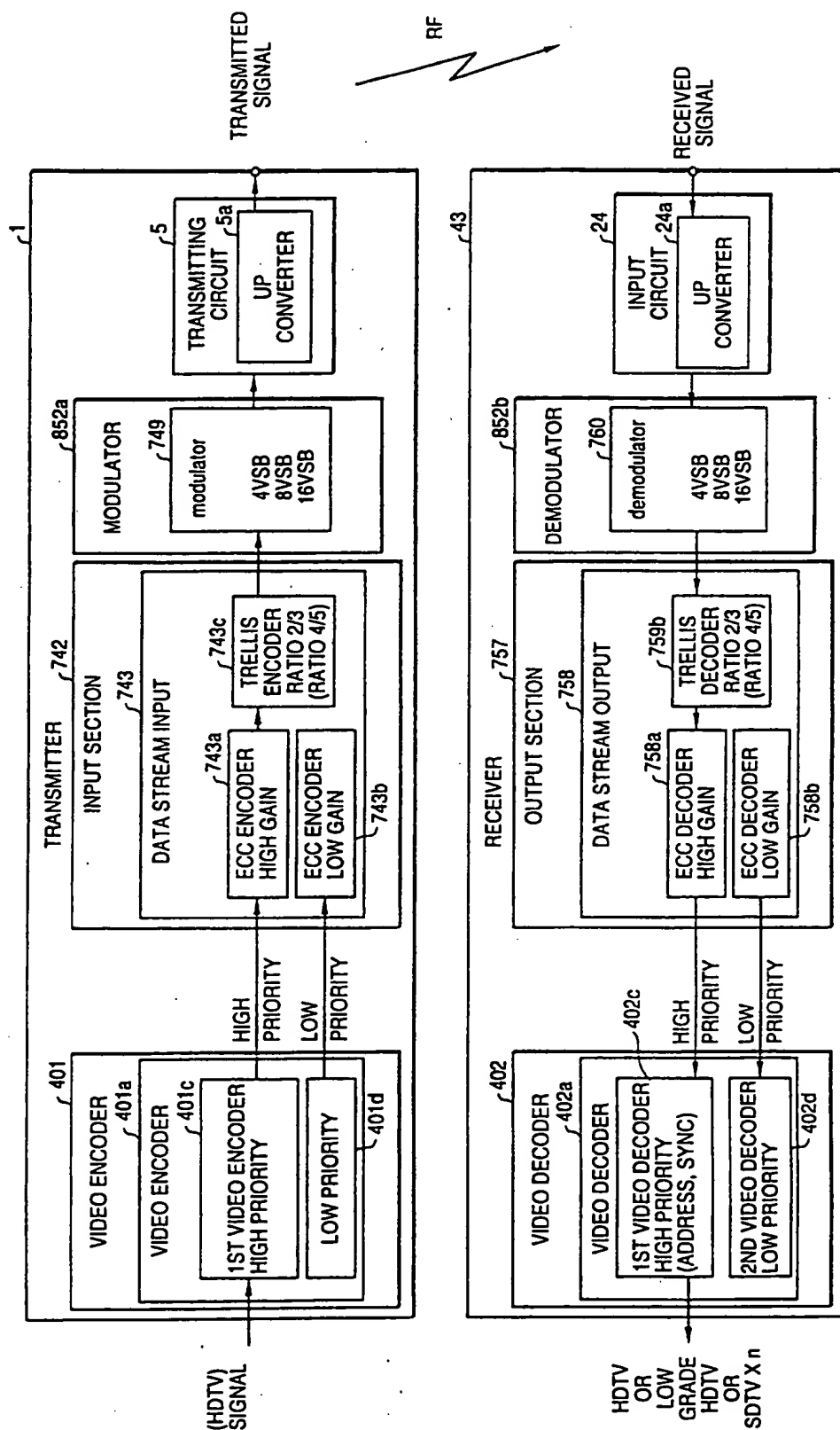


FIG. 173

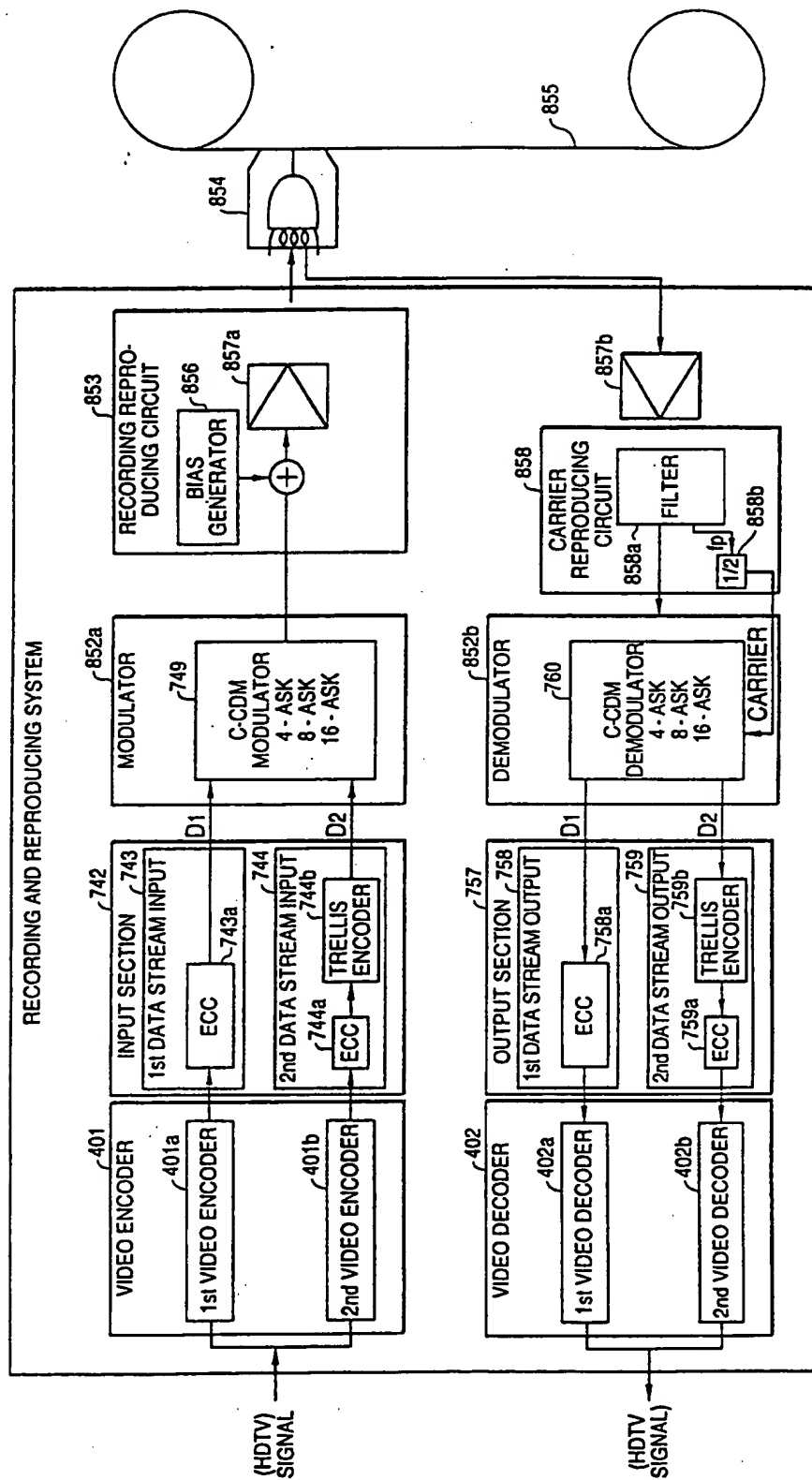


FIG. 174

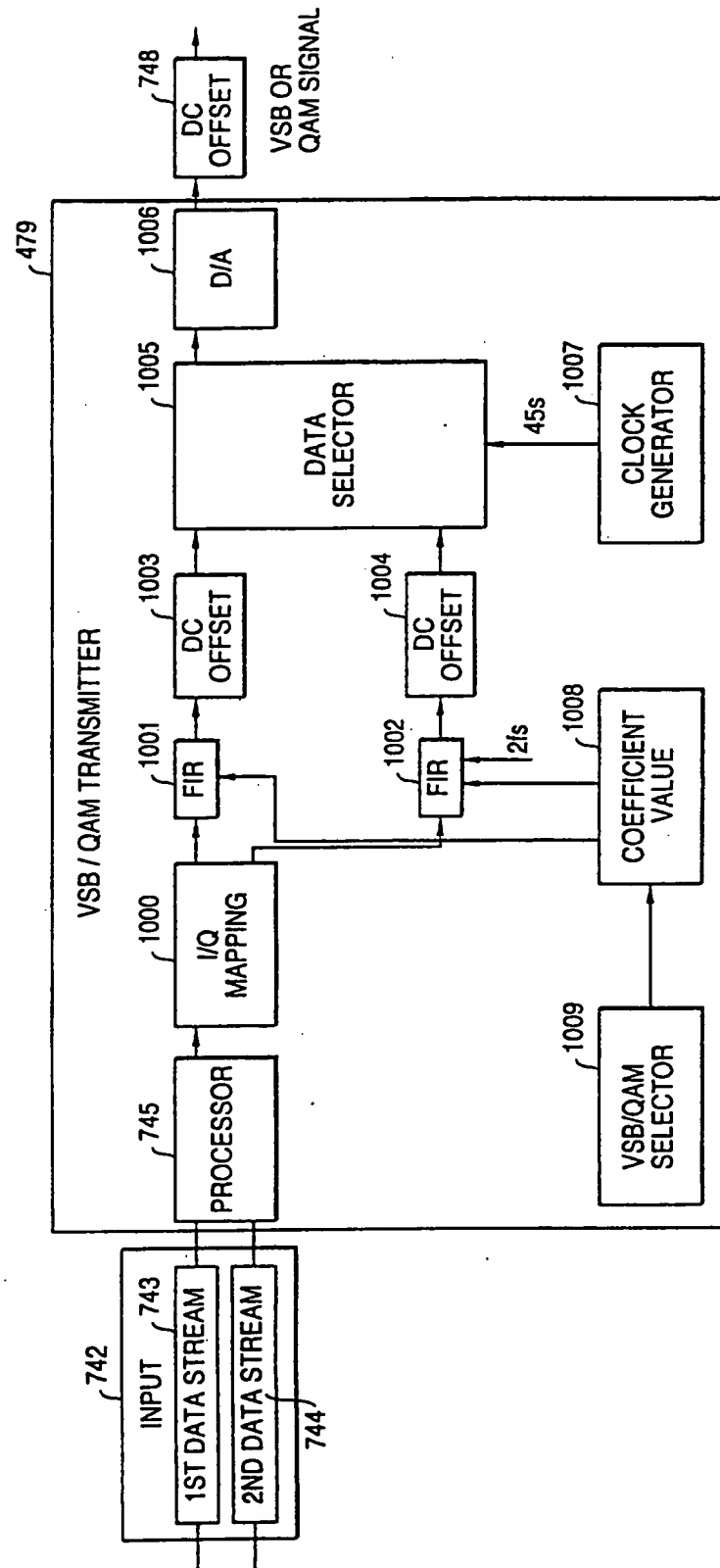


FIG. 175

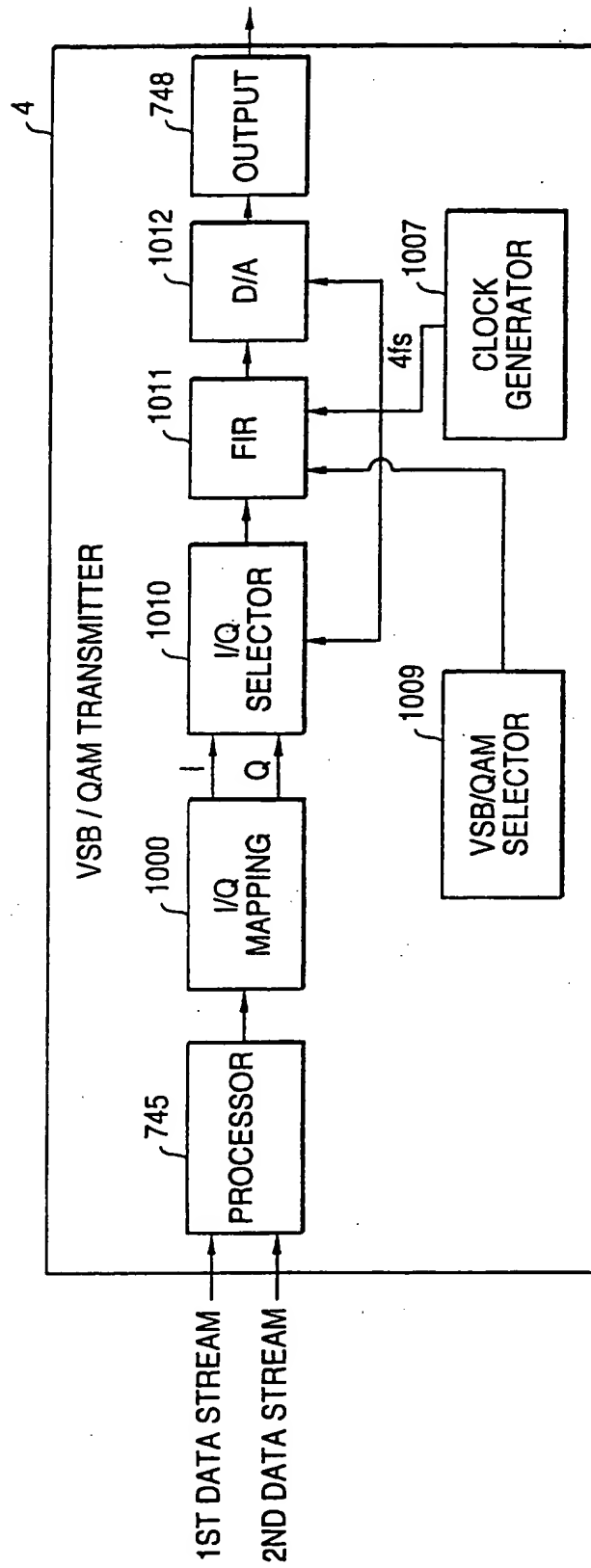


FIG. 176

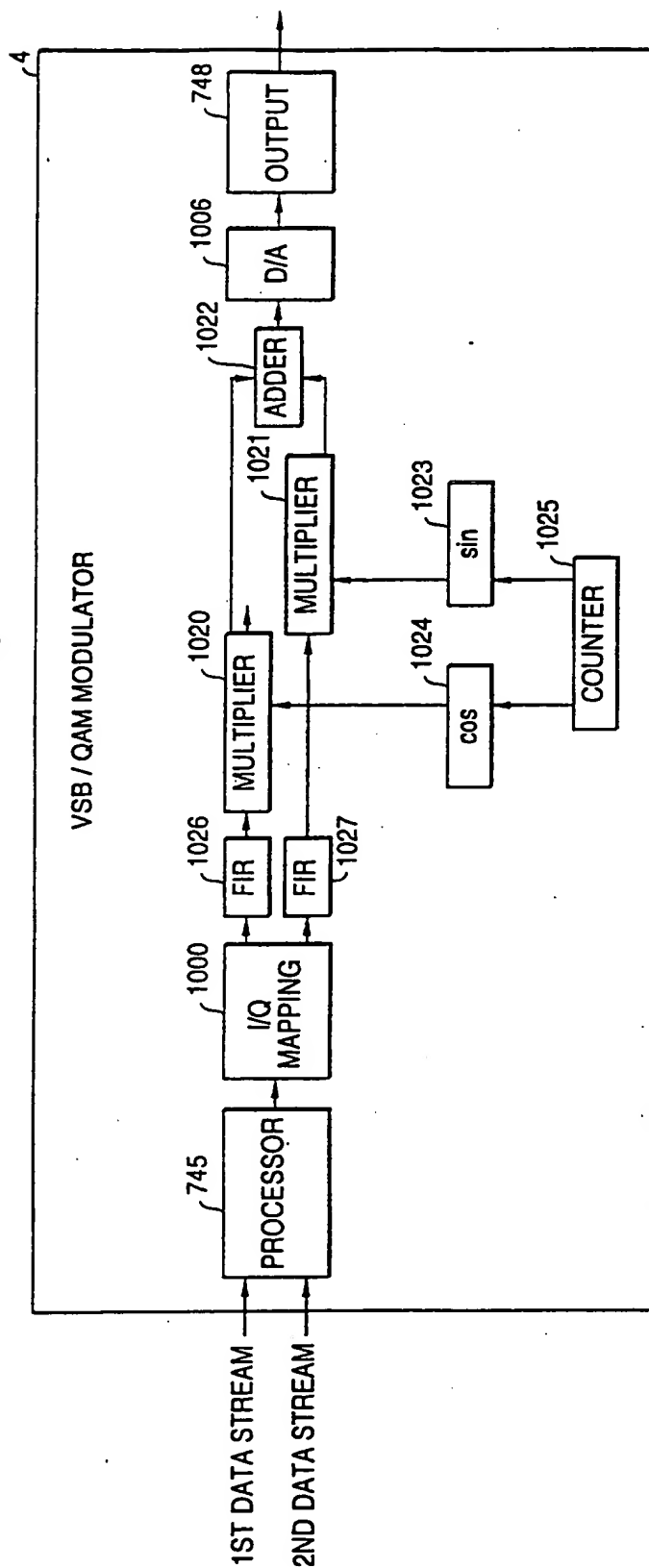


FIG. 177

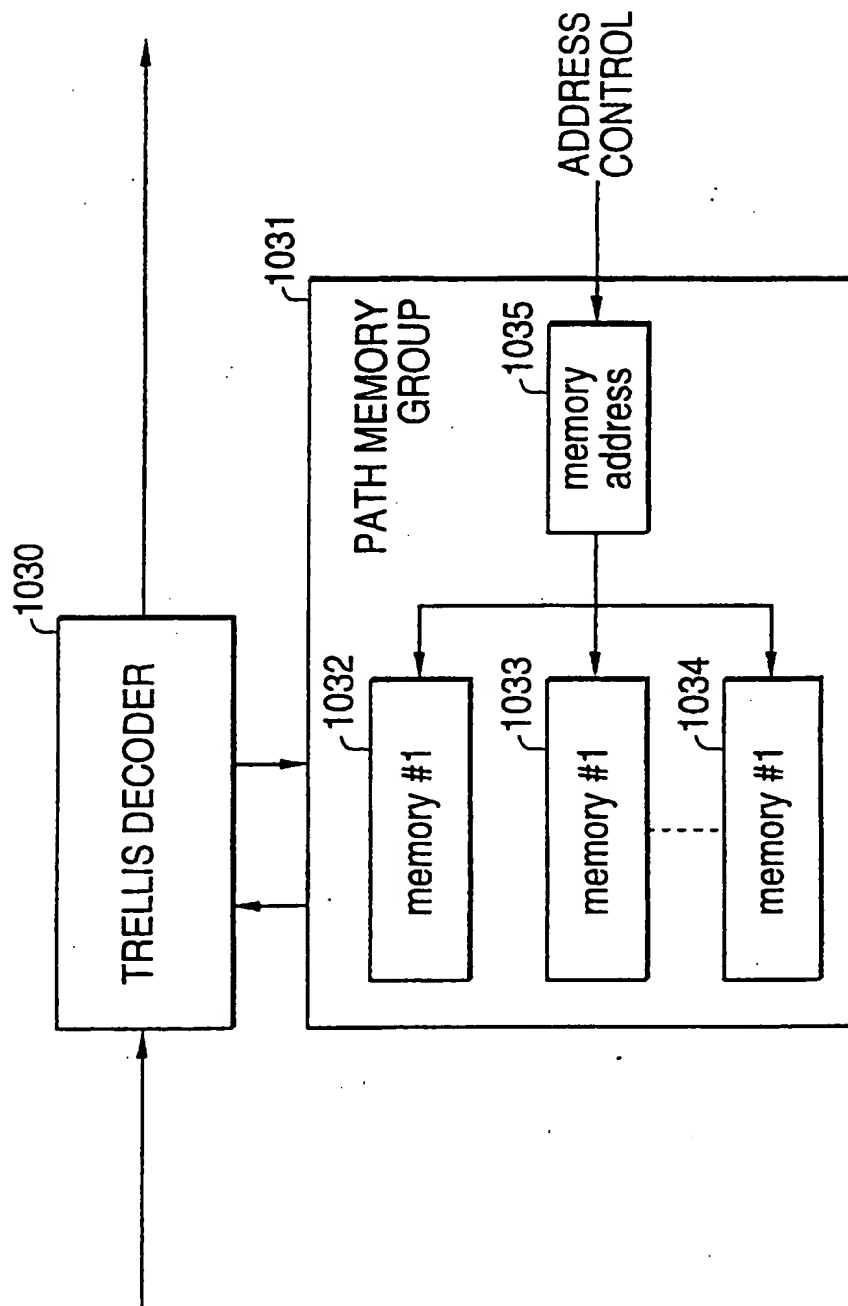


FIG. 178

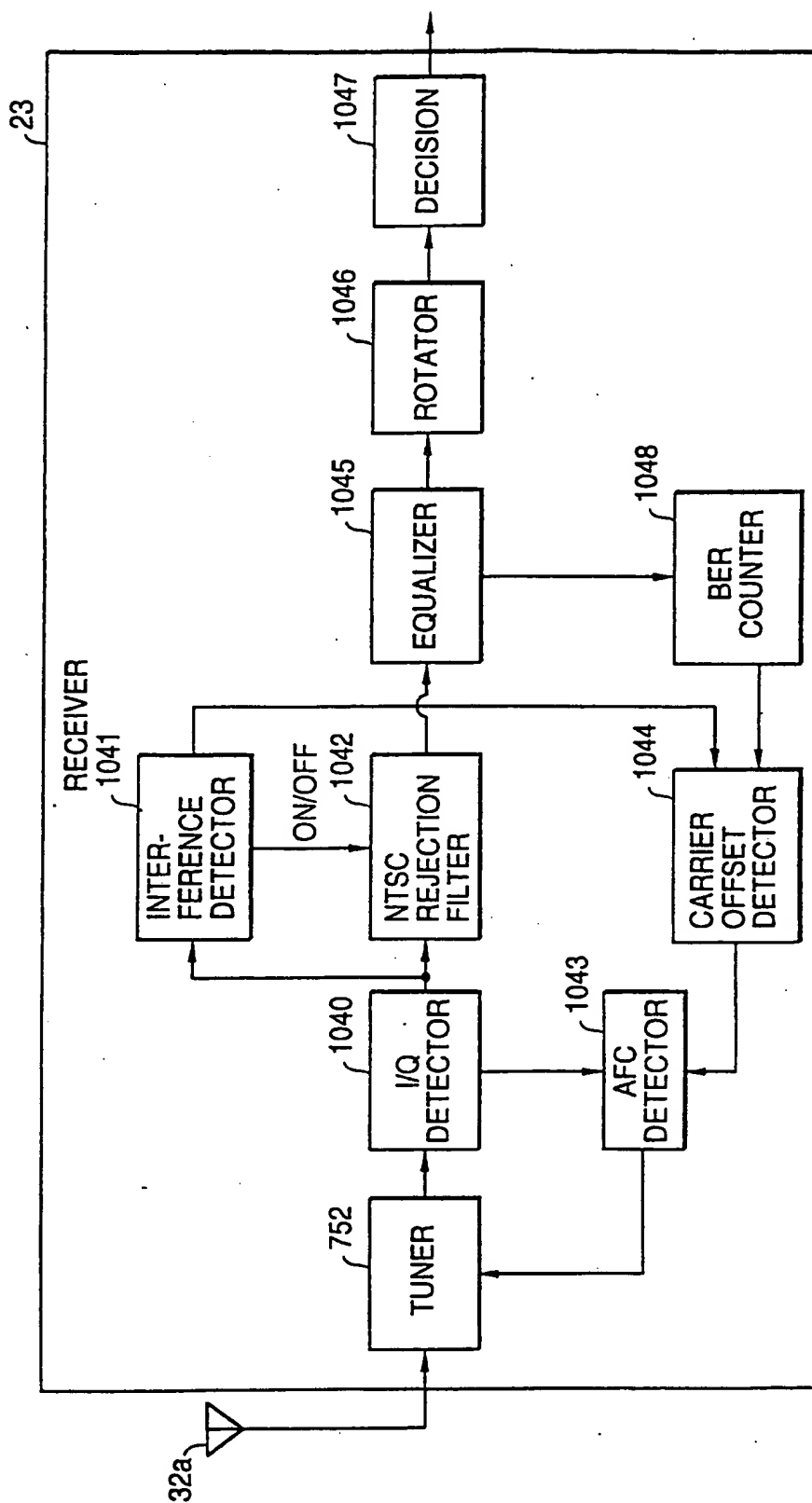


FIG. 179

